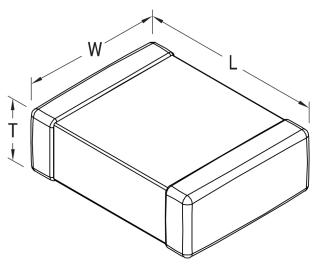


Aliases (C1210Z561K1GAH)

SMD Mil PRF123, Ceramic, 560 pF, 10%, 100 VDC, BP, SMD Chip, Ultra Stable, N/A, 1210



Click here for the 3D model.

Dimensions	,
Chip Size	1210
L	3.048mm +/-0.381mm (0.12 in +/-0.015 in)
W	2.54mm +/-0.381mm (0.1 in +/-0.015 in)
Т	1.651mm MAX (0.065 in MAX)

Packaging Specifications			
Packaging	Waffle		
Packaging Quantity	25000		

General Information		
Series	SMD Mil PRF123	
Style	SMD Chip	
Description	SMD Chip, Ultra Stable	
RoHS	No	
Prop 65	▲ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.	
SCIP Number	646e40c8-ce81-456e-b9ed-50112f3d27a5	
Termination	Nickel	
Failure Rate	N/A	
AEC-Q200	No	
Shelf Life	78 Weeks	
MSL	1	

Specifications	
Capacitance	560 pF
Capacitance Tolerance	10%
Voltage DC	100 VDC
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	BP
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1MHz 1.0Vms
Dissipation Factor	2.5%
Insulation Resistance	100 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

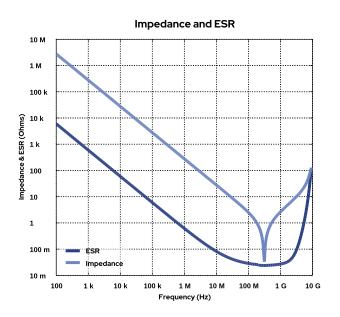


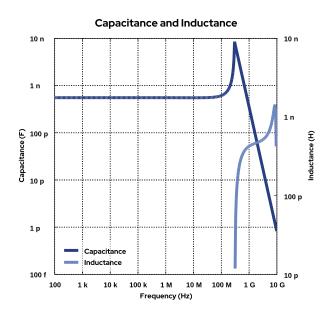
Aliases (C1210Z561K1GAH)

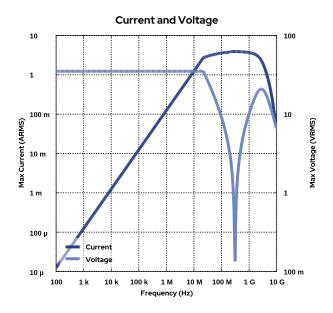
SMD Mil PRF123, Ceramic, 560 pF, 10%, 100 VDC, BP, SMD Chip, Ultra Stable, N/A, 1210

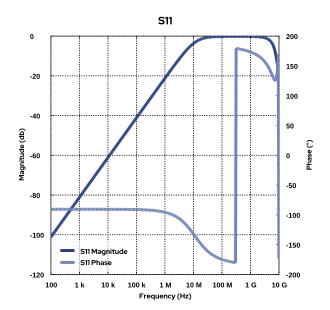
## **Simulations**

For the complete simulation environment please visit K-SIM.





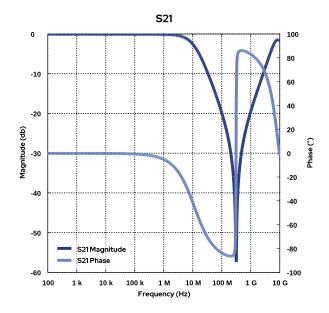






Aliases (C1210Z561K1GAH)

SMD Mil PRF123, Ceramic, 560 pF, 10%, 100 VDC, BP, SMD Chip, Ultra Stable, N/A, 1210





Aliases (C1210Z561K1GAH)

SMD Mil PRF123, Ceramic, 560 pF, 10%, 100 VDC, BP, SMD Chip, Ultra Stable, N/A, 1210

#### These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

  The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.

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# >>KEMET(基美)