## **KEMET Part Number: C0402T200F5GCLTU**

(C0402T200F5GCL7867)



SMD COTS COG, Ceramic, 20 pF, 1%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0402



Dimensions		
Chip Size	0402	
L	1mm +/-0.05mm	
W	0.5mm +/-0.05mm	
Т	0.5mm +/-0.05mm	
S	0.3mm MIN	
В	0.3mm +/-0.1mm	

Packaging Specifications		
Packaging:	T&R, 180mm, Paper Tape	
Packaging Quantity:	10000	

General Information		
Series:	SMD COTS COG	
Style:	SMD Chip	
Description:	SMD, MLCC, COTS, Ultra- Stable, Low Loss, Class I	
Features:	Ultra-Stable, Low Loss, Class I	
RoHS:	No	
Prop 65:	warning: Cancer and reproductive harm - www.p65warnings.ca.gov.	
SCIP Number:	2d771165-5336-48a3-96fa-36639	29fd828
Termination:	Lead (SnPb)	
Marking:	No	
Failure Rate:	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A	
AEC-Q200:	No	
Component Weight:	1060 ug	
Shelf Life:	78 Weeks	
MSL:	1	

Specifications		
Capacitance:	20 pF	
Measurement Condition:	1 MHz 1.0Vrms	
Capacitance Tolerance:	1%	
Voltage DC:	50 VDC	
Dielectric Withstanding Voltage:	125 VDC	
Temperature Range:	-55/+125°C	
Temperature Coefficient:	COG	
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	30 ppm/C, 1MegaHz 1.0Vrms	
Dissipation Factor:	0.1% 1 MHz 1.0Vrms	
Aging Rate:	0% Loss/Decade Hour	
Insulation Resistance:	100 GOhms	

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.



## 单击下面可查看定价,库存,交付和生命周期等信息

## >>KEMET(基美)