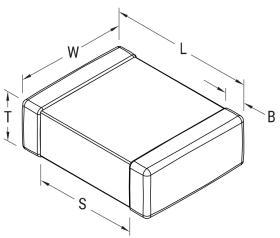


C0603C104J4RECTU

Aliases (C0603C104J4REC7867)

ESD SMD Comm X7R, Ceramic, 0.1 uF, 5%, 16 VDC, X7R, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II, 0603



Click here for the 3D model.

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
Т	0.8mm +/-0.07mm
S	0.7mm MIN
В	0.35mm +/-0.15mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

General Information	
Series	ESD SMD Comm X7R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Component Weight	4800 ug
Shelf Life	78 Weeks
MSL	1

Specifications	
Capacitance	0.1 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	5%
Voltage DC	16 VDC
ESD Level per AEC-Q200	25,000 V ESD Level
Dielectric Withstanding Voltage	40 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	3.5% 1 kHz 1.0 Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	5 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

单击下面可查看定价,库存,交付和生命周期等信息

>>KEMET(基美)