

TYS-Low Profile SMT Power Inductor

TYS6045 Series

FEATURES AND APPLICATIONS

Laird TYS series high current power inductors improve performance, reliability and power efficiency. A lower profile benefits consumer electronics, industrial and telecom design. Products feature extremely low DCR with greater efficiency and enable a large current in a small size. Inductors are of magnetic shielding and wire wound construction and perform in operating temperatures ranging from -40 C to 125 C including self-heating rise in temperature.

FEATURES

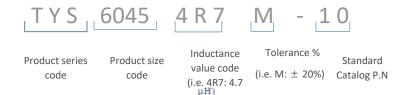
- Magnetic shielded structure
- Low DCR and high efficiency
- · Low profile and small size
- Ferrite core with high saturation

APPLICATIONS

- DC-DC Converter and Power Suppliers
- LCD TV'S and Gaming Console
- Tablet, Notebooks, Servers and Printers
- Networking and Data storage
- GPS, Set-top-box and Base stations
- Smart meters and Medical instruments



PART NUMBER EXPLANATION



ELECTRICAL SPECIFICATIONS

- Tolerance: M: ±20% or N: ±30%
- Inductance tested at 1MHz, 1.0Vrms
- Heat Rated Current (Irms) is defined based on temperature rise approximate 40°C (ambient temperature 25±5°C)
- Saturation Current (Isat) is the DC current at which the inductance drops off approximately 30% from its value without current. (ambient temperature 25±5°C)
- Operating temperature range: -40°C~+125°C (including self-heating temperature rise)
- Storage temperature range (packaging conditions): -10°C~+40°C and RH 70%(MAX.)

Note: Heat Rated Current (Irms) is tested on a typical PCB and apply a constant current in still air. The temperature rise is dependent on the application system condition including PCB PAD pattern, trace width and thickness and adjacent components etc. It's suggested to verify the temperature rise of the component under the real operation application conditions.



Shielded Power Inductor

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	www.laird.com	TYS6045 Ser	ies Rev: A
SPECIFICATION			
1.MECHANICAL & DIMENSIONS			(UNIT: mm)
		А	6.00±0.30
- A - - C - - D	-	В	6.00±0.30
		С	4.5+0.2/-0.3
		D	4.90±0.30
MARK MARK		Е	1.80±0.40
		F	2.40±0.30
		G	5.7 REF
		Н	2.5 REF
		Т	1.8 REF
H=		RE	MARK
†			
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1			
2.PART NUMBER NOMENCLATOR:			
<u>TYS 6045 4R7 M - 10</u>	D: Inductance Tolerance	e. (M=±20% ,N=	±30%)
TYS 6045 4R7 M - 10 A B C D E	E: "X"=0:Standard catal	og part number	
A: Product Series.	"X"=1-9:Controlled cu	stomized part C	r different
B: Series number, part size	performance than	n std catalog par	rt.
C: Inductance code			
3.EQUIVALENT CIRCUIT:			
3.EQUIVALENT CIRCUIT.			
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Shielded Power Inductor

			www.lair	d.com TYS6045 Series		Rev: A
SPECIFICA	TION					
PART NUMBER	INDUCTANCE (uH)	Irms(A) Typ.	Isat(A) Typ.	DCR(mΩ) TYP	DCR(mΩ) Max	SRF MH
TYS60451R0N-10	1.00	5.14	9.85	11.0	14.3	100
TYS60451R5N-10	1.50	4.95	8.80	12.0	15.6	65
TYS60451R8N-10	1.80	4.95	7.60	12.0	15.6	74
TYS60452R2N-10	2.20	4.60	6.75	14.0	18.2	52
TYS60453R3M-10	3.30	3.70	5.90	21.0	27.3	32
TYS60454R7M-10	4.70	3.30	4.97	26.0	33.8	24
TYS60456R8M-10	6.80	3.00	3.90	31.0	40.3	20
TYS6045100M-10	10.00	2.45	3.20	48.0	62.4	15
TYS6045150M-10	15.00	2.05	2.50	68.0	88.4	12
TYS6045220M-10	22.00	1.80	2.05	89.0	115.7	10
TYS6045330M-10	33.00	1.45	1.65	137.0	178.1	7.8
TYS6045470M-10	47.00	1.20	1.40	200.0	260.0	6.4
TYS6045680M-10	68.00	1.00	1.20	289.0	375.7	6.4
TYS6045101M-10	100.00	0.80	0.95	433.0	563.0	4.2
TYS6045221M-10	220.00	0.59	0.70	834.0	1084.0	3.5
TYS6045331M-10	330.00	0.57	0.57	1270.0	1651.0	2.8
GENERAL SPE	CIFICATION:					<u>.</u>
• Tolerance: M: ±2	0% or N: ±30%					
 Inductance teste 	d at 100KHz, 1.0	Vrms				
Heat Rated Curre	ent (Irms) is defin	ed based on ten	nperature rise ap	proximate 40°C		
(ambient temper	ature 25±5°C)					
Saturation Curre	nt (Isat) is the DO	Current at whic	h the inductance	drops off approx	ximately 30%	
from its value wit	thout current. (a	mbient tempera	ture 25±5°C)			
 Operating tempe 	rature range: -40	O°C~+125°C (incl	uding self-heatin	g temperature ri	se)	
 Storage tempera 	ture range (pack	aging conditions): -10°C~+40°C a	nd RH 70%(MAX.)	



Shielded Power Inductor

www.laird.com TYS6045 Series Rev: A **SPECIFICATION Characteristics Curve** TYS60451R0N-10 TYS60451R5N-10 1.5 50 2.0 50 40 1.2 40 1.6 nductance (uH) nductance (uH) Rise Temperature Rise 0.9 30 1.2 30 Temperature | 0.6 20 0.8 20 0.3 10 0.4 10 0.0 0.0 0 2.5 5 7.5 10 12.5 7.5 10 12.5 IDC(A) IDC(A) TYS60451R8N-10 TYS60452R2N-10 2.5 50 2.5 50 2.0 40 2.0 40 nductance (uH) Inductance (uH) Temperature Rise 1.5 30 1.5 30 1.0 20 1.0 20 0.5 10 0.5 0.0 0.0 0 2.5 7.5 10 12.5 8 10 IDC(A) IDC(A) TYS60453R3M-10 TYS60454R7M-10 6.0 50 4.0 50 4.8 40 3.2 nductance (uH) Inductance (uH) 3.6 30 2.4 30 Temperature Temperature 2.4 20 1.6 20 1.2 10 8.0 10 0.0 0 0.0 0 0 1.6 3. 2 4.8 8 6.4 0 1.6 3.2 4.8 8 6.4 IDC(A) IDC(A)

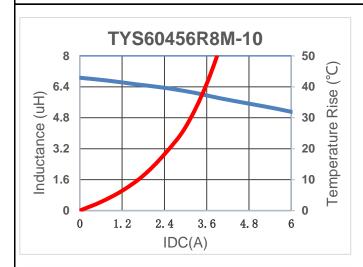


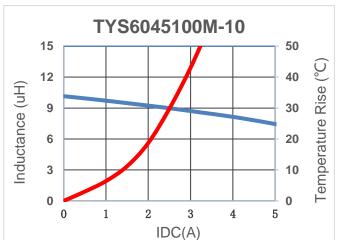
Shielded Power Inductor

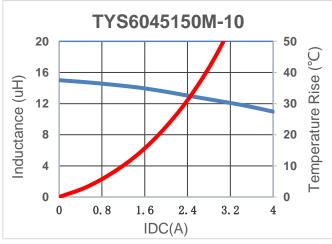
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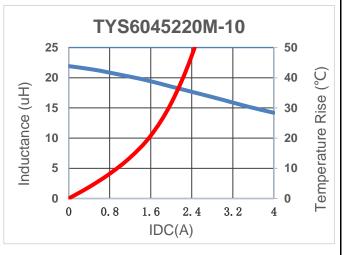
SPECIFICATION

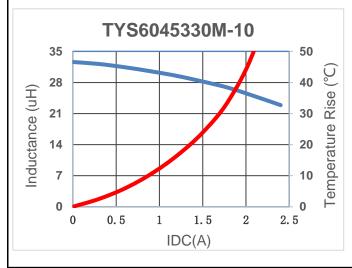
Characteristics Curve

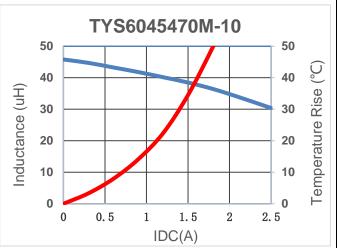












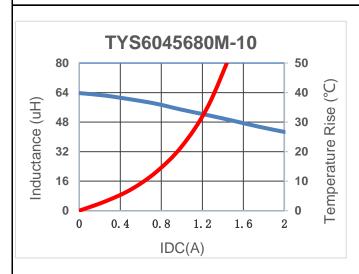


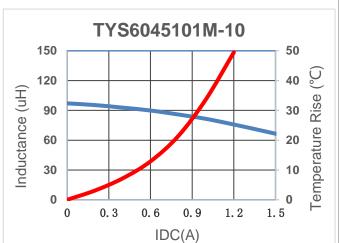
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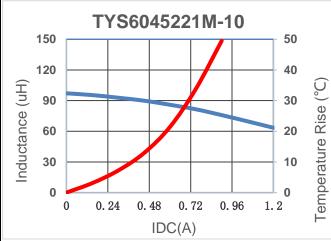
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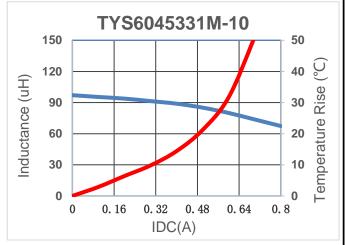
SPECIFICATION

Characteristics Curve



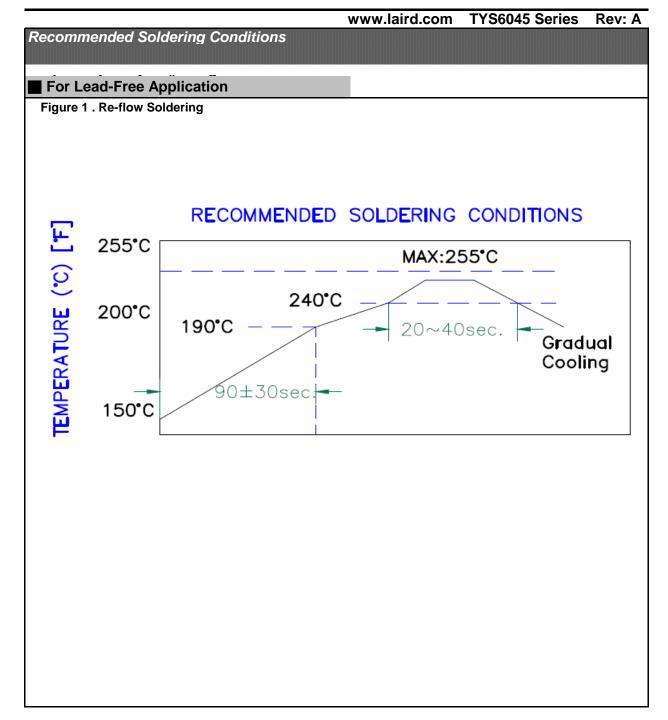








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SMD series(Consumer)				
Item	Reference	Additional Requirements		
Operating temperature range	-40°C ∼ +125°C (Including self-temperature	rise)		
Storage temperature and humidity range	-10 $^\circ$ C to +40 $^\circ$ C , 70% RH Max			
High Temperature Exposure (Storage)	MIL-STD-202 Method 108	85±2°ℂ, 168+24hours		
Temperature Cycling	JESD22 Method JA-104	-40°C→+85, transforming interval:20s, 100cycles		
Operational Life	MIL-PRF-2	85±°C, 168+24hours Apply maximum rated voltage and current according part drawing		
External Visual	MIL-STD-883 Method 2009	Inspect device construction, marking and workmanship. Electrica Test not required.		
Physical Dimension	JESD22 Method JB-100	Verify physical dimensions to the applicable device detail specification. Note: User(s) and Suppliers spec. Electrical Test not required		
Vibration	MIL-STD-202 Method 204	10~55Hz,1.5mm, 2 hours in each 3mutually perpendicular directions (total of 6 hours)		
Resistance to Soldering Heat	MIL-STD-202 Method 210	1. Max. 260±5℃,10±1s, 2 times 2.Solder Composition: Sn/3Ag/0.5Cu		
Solderability	J-STD-002	245±5℃, 5±1sec, Solder: Sn/3.0Ag/0.5Cu		
Electrical Characterization	Print Spec	Parametrically test per lot and sample size requirements, summary to show Min, Max, Mean and Standard deviation at room as well as Min and Max Operating temperatures		
Board Flex	AEC-Q200-005	2mm,30±1s		
Ferminal Strength(SMD) AEC-Q200-006		10N, 5S, X,Y direct		

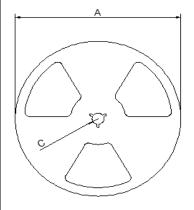


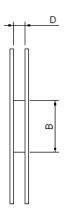
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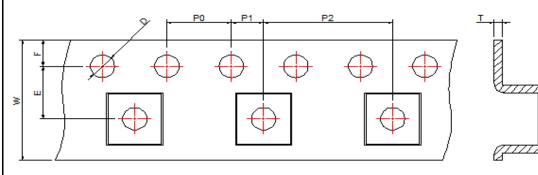
PACKAGING

Reel Dimension





Tape Dimension

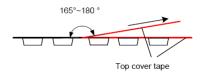


W	Е	F	P0	P1	P2	Т	D
12.0±0.3	5.5±0.1	1.75±0.1	4.0±0.1	2.0±0.1	8.0±0.1	0.3±0.05	1.5±0.1

Packaging Quantity

P/N	Chip/Reel
TYS6045 series	1500pcs

Peeling Off Force



The force peeling off cove tape is 10 to 100 grams						
in the arrow direction under the following conditions						
Room	Room Humidity	Room atrn	Teaming Speed			
Temp	(%)	(hPa)	(mm/min)			
5~35	45~85	860~1060	300			

- **XStorage Conditions**1. Temperature and humidity conditions: -10-+40℃ and 70% RH.
- 2. Recommended products should be used within 12 months
- from the time of manufacturing.

 3. The packaging material should be kept where no chlorine or sulfur exists in the air.
- 4. Allowable stacking condition of Packaging box: max height 1.5m or 5 boxes stacking

单击下面可查看定价,库存,交付和生命周期等信息

>>Laird(莱尔德)