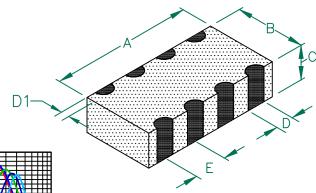
DA1206B601R-10



ELEC ⁻	ISTICS:		
Z @ 100M (Ω)		DCR $\left(\begin{array}{c}\Omega\end{array}\right)$	Rated Current
Nominal	600		
Minimum	Minimum 450		
Maximum	750	0.35	200 mA

LINE TO LINE INSULATION RESISTANCE $>100 \text{ M}\Omega$ AT 75 VOLTS.

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 3000 PCS/REEL EMBOSSED PLASTIC TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)
 5. COSMETIC SPECIFICATION REFER TO WI-QA-124.

C 600 MPEDANCE 500 400 300 100

PHYSICAL DIMENSIONS:

0.80 [.031] ± 0.20 [.008] D 0.40 [.016] ± 0.15 [.006] D1 0.30 [.012] ± 0.20 [.008] E 0.80 [.031] ± 0.10 [.004]

A 3.20 [.126]

B 1.60 [.063]

FREQUENCY (MHz)

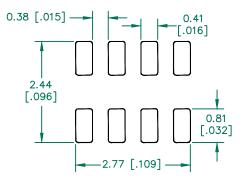
|Z| , R, AND X vs. FREQUENCY

± 0.20 [.008] ± 0.20 [.008]

Z vs FREQUENCY

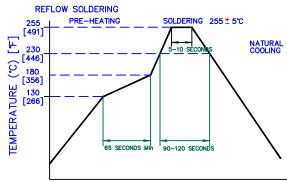
IMPEDANCE UNDER DC BIAS

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 (0.030) to this dimension)

RECOMMENDED SOLDERING CONDITIONS



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AGILEN	AGILENT E4991A RF Impedance/Material Analyzer																			

HP 16194A Test Fixture. TEST REF. 3236



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Γ		DIMENSIONS ARE IN mm [INCHE	:S].	This print is the property of Lair						
I					Tech, and is loaned in confidence subject to return upon request a		_	.	.I.	
					with the understanding that no			2	ir(1
L					copies shall be made without the			u	I I (Al .
	Ε	OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	written consent of Laird Tech. Al rights to design or invention are reserved.	1				
	D	UPDATE COMPANY LOGO	05/20/09	JRK	PROJECT/PART NUMBER:	T	ŒV	PART TY	PE:	DRAWN BY:
	С	D1 dim chgd from 0.008 ±0.004 to 0.012 ± 0.008. UPDATE COMPANY LOGO	10/30/07	JRK	DA1206B601R-10		E	co-	FIRE	ТМВ
	В	CHANGE DCR, MODIFY NOTE 1 ADD ROHS				CÁL	E: N	TS	SHEET:	
Γ	Α	ORIGINAL DRAFT	03/30/04	TMB	CAD # 1	OOL				
	REV	DESCRIPTION	DATE	INT	CAD # DA1206B601R-10-E		•	-	1	of 1

单击下面可查看定价,库存,交付和生命周期等信息

>>Laird(莱尔德)