

TYS -Low Profile SMT Power Inductor

TYS5040 Series

FEATURES AND APPLICATIONS

Laird TYS series high current power inductors improve performance, reliability and power efficiency. A lower profile benefits consumer electronics, industrial and telecom design. Products feature extremely low DCR with greater efficiency and enable a large current in a small size. Inductors are of magnetic shielding and wire wound construction and perform in operating temperatures ranging from -40 C to 125 C including self-heating rise in temperature.

FEATURES

- Magnetic shielded structure
- Low DCR and high efficiency
- Low profile and small size
- Ferrite core with high saturation

APPLICATIONS

- DC-DC Converter and Power Suppliers
- LCD TV'S and Gaming Console
- Tablet, Notebooks, Servers and Printers
- Networking and Data storage
- GPS, Set-top-box and Base stations
- Smart meters and Medical instruments



PART NUMBER EXPLANATION



ELECTRICAL SPECIFICATIONS

- Tolerance: M: ±20% or N: ±30%
- Inductance tested at 1MHz, 1.0Vrms
- Heat Rated Current (Irms) is defined based on temperature rise approximate 40°C (ambient temperature 25±5°C)
- Saturation Current (Isat) is the DC current at which the inductance drops off approximately 30% from its value without current. (ambient temperature 25±5°C)
- Operating temperature range: -40°C~+125°C (including self-heating temperature rise)
- Storage temperature range (packaging conditions): -10°C~+40°C and RH 70%(MAX.)

Note: Heat Rated Current (Irms) is tested on a typical PCB and apply a constant current in still air. The temperature rise is dependent on the application system condition including PCB PAD pattern, trace width and thickness and adjacent components etc. It's suggested to verify the temperature rise of the component under the real operation application conditions.



	www.laird.com	TYS5040 Ser	ies Rev: A
CDECIFICATION	www.iairu.com	1 1 33040 3ei	ies Rev. A
SPECIFICATION			
1.MECHANICAL & DIMENSIONS			(UNIT: mm)
		Α	5.00±0.20
A - C -		В	5.00±0.20
		С	4.0+0.2/-0.3
		D	1.50±0.30
	<u> </u>	E	2.10±0.40
	",	G	4.2 REF
		Н	2.3 REF
		Т	1.4 REF
G-		RE	MARK
. ⊢			
2.PART NUMBER NOMENCLATOR:		I	
	D: Inductance Toleran	ce. (M=±20% ,N=	:±30%)
TYS 5040 6R8 M - 10 A B C D E	E: "X"=0:Standard cata		·
A: Product Series.	"X"=1-9:Controlled		r different
B: Series number, part size		an std catalog par	
C: Inductance code	·	0.	
1			
3.EQUIVALENT CIRCUIT:			
	2.11		
<u> </u>	Ϋ́II		
-	3		
	5		

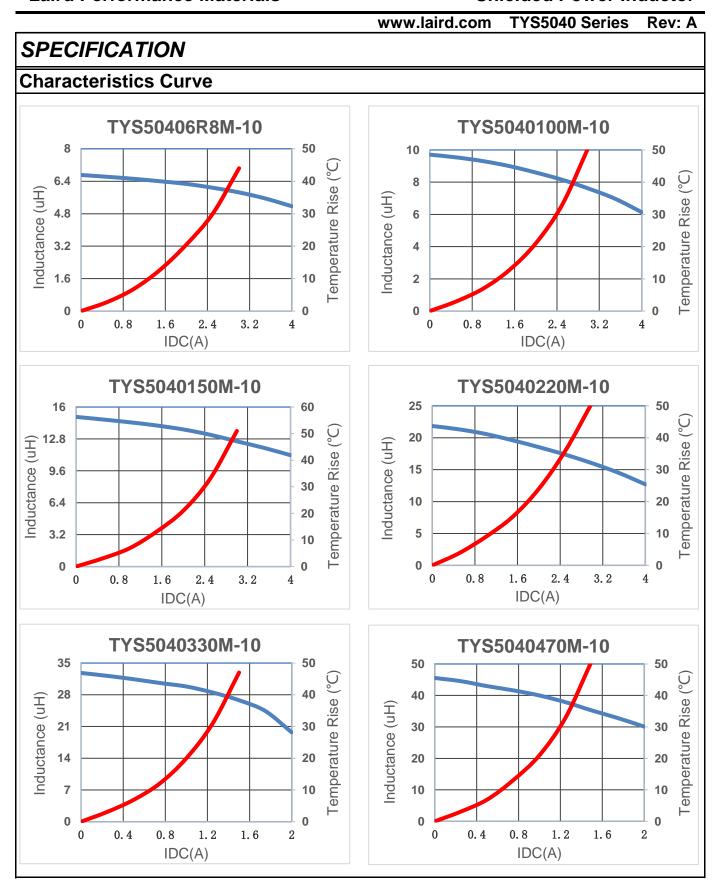


			www.lai	rd.com TY	S5040 Series	Rev: A
SPECIFICAT	ION					
PART NUMBER	INDUCTANCE (uH)	Irms(A) Typ.	Isat(A) Typ.	DCR(mΩ) TYP	DCR(mΩ) Max	SRF MHz
TYS50401R0N-10	1.00	4.90	7.35	12.00	14.4	117
TYS50401R5N-10	1.50	4.30	6.30	15.00	18.0	85
TYS50402R2N-10	2.20	3.80	4.90	19.00	22.8	50
TYS50403R3N-10	3.30	3.40	3.95	24.00	28.8	32
TYS50404R7M-10	4.70	3.00	3.50	30.00	36.0	28
TYS50405R6M-10	5.60	2.80	3.00	38.00	46.0	27
TYS50406R8M-10	6.80	2.50	2.90	43.00	51.6	21
TYS5040100M-10	10.00	2.30	2.70	47.80	62.0	20
TYS5040150M-10	15.00	2.00	2.00	86.00	103.0	13
TYS5040220M-10	22.00	1.50	1.60	129.00	155.0	11
TYS5040330M-10	33.00	1.20	1.30	188.00	226.0	9.1
TYS5040470M-10	47.00	1.00	1.10	272.00	326.0	6.7
TYS5040680M-10	68.00	0.80	0.90	400.00	480.0	5.7
TYS5040101M-10	100.00	0.70	0.75	560.00	672.0	4.7
ENERAL SPEC	FICATION:					
Tolerance: M: ±20%						
Inductance tested	at 100KHz. 1.0V	rms				
Heat Rated Current			nperature rise a	approximate 40°	°C	
(ambient temperat						
Saturation Current		current at whic	h the inductan	ce drops off app	roximately 30%	
from its value with						
Operating tempera	-	•	-	ing temperatur	e rise)	
Storage temperatu					•	
<u> </u>	0 - (15 > 10	<u> </u>	, : : · · · ·	27-1-4	<u>'</u>	
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	www.laird.com TYS5040 Series	Rev: A
SPECIFICATION		
Characteristics Curve		
TYS50401R0N-10 1.5 (Hn) 0.9 0.6 0.0 1.6 3.2 4.8 IDC(A)	TYS50401R5N-10 2.0 1.6 (Hn) 0.8 0.4 0.0 0 1.6 3.2 4.8 6.4 IDC(A)	20 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
TYS50402R2N-10 2.5 (Hn) every 1.0 0.0 1.2 2.4 3.6 IDC(A) 2.5 12 12 12 12 13 15 16 17 17 18 18 19 10 10 10 10 10 10 10 10 10	TYS50403R3N-10 4.0 3.2 (Hn) 0.8 0.0 1 2 3 4 IDC(A)	60 (Section 2) 60 84 84 86 (Section 2) 60 84 86 86 86 86 86 86 86 86 86 86 86 86 86
TYS50404R7M-10 6.0 (Hn) 3.6 2.4 24 224 24 24 12 12 12 1DC(A)	TYS50405R6M-10 8.0 (Hn) 4.8 3.2 1.6 0.0 0.8 1.6 2.4 3.2 IDC(A)	20 Lemperature Rise (°C)





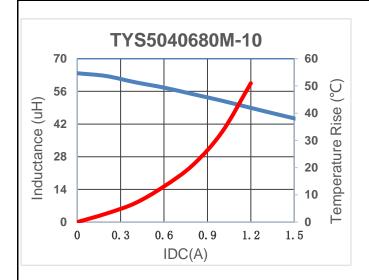


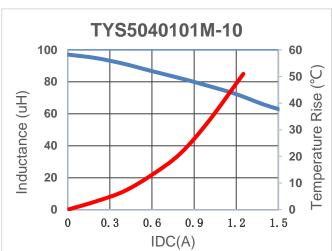
Shielded Power Inductor

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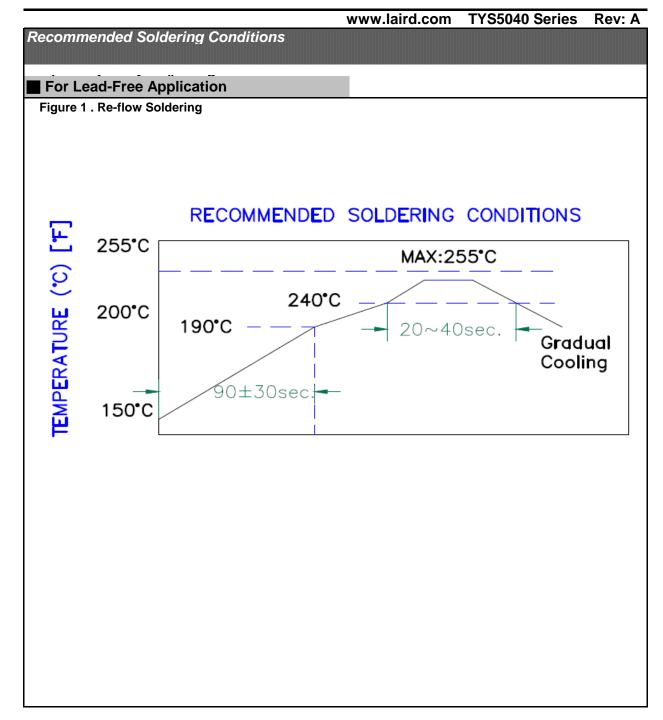
SPECIFICATION

Characteristics Curve











Shielded Power Inductor

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SMD series(Consumer)					
ltem	Reference	Additional Requirements			
Operating temperature range	-40°C ~ +125°C (Including self-temperature ris	e)			
Storage temperature and humidity range	-10°ℂ to +40°ℂ , 70% RH Max				
High Temperature Exposure (Storage)	MIL-STD-202 Method 108	85±2℃, 168+24hours			
Temperature Cycling	JESD22 Method JA-104	-40°C→+85, transforming interval:20s, 100cycles			
Operational Life	MIL-PRF-2	85±℃, 168+24hours Apply maximum rated voltage and current according part drawing			
External Visual	MIL-STD-883 Method 2009	Inspect device construction, marking and workmanship. Electrica Test not required.			
Physical Dimension	JESD22 Method JB-100	Verify physical dimensions to the applicable device detail specification. Note: User(s) and Suppliers spec. Electrical Test not required			
Vibration	MIL-STD-202 Method 204	10~55Hz,1.5mm, 2 hours in each 3mutually perpendicular directions (total of 6 hours)			
Resistance to Soldering Heat	MIL-STD-202 Method 210	1. Max. 260±5℃,10±1s, 2 times 2.Solder Composition: Sn/3Ag/0.5Cu			
Solderability	J-STD-002	245±5℃, 5±1sec, Solder: Sn/3.0Ag/0.5Cu			
Electrical Characterization	Print Spec	Parametrically test per lot and sample size requirements, summary to show Min, Max, Mean and Standard deviation at room as well as Min and Max Operating temperatures			
Board Flex	AEC-Q200-005	2mm,30±1s			
Terminal Strength(SMD)	AEC-Q200-006	10N, 5S, X,Y direct			

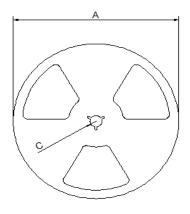


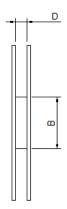
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PACKAGING

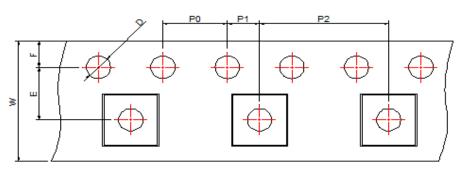
Reel Dimension

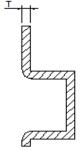




A(mm)	330 REF
B(mm)	100 REF
C(mm)	13 REF
D(mm)	12.4 REF

Tape Dimension



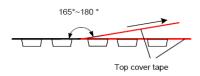


W	E	F	Р	P1	P2	Т	D
12.0±0.3	5.5±0.1	1.75±0.1	4.0±0.1	2.0±0.1	8.0±0.1	0.3±0.05	1.5±0.1

Packaging Quantity

P/N	Chip/Reel
TYS5040 series	1500pcs

Peeling Off Force



The force	The force peeling off cove tape is 10 to 100 grams					
in the arrow direction under the following conditions						
Room	3 -1					
Temp (%) (hPa) (mm/min)						
5~35	45~85	860~1060	300			

- **※Storage Conditions**1. Temperature and humidity conditions: -10-+40℃ and 70% RH.
- 2. Recommended products should be used within 12 months
- from the time of manufacturing.

 3. The packaging material should be kept where no chlorine or sulfur exists in the air.
- 4. Allowable stacking condition of Packaging box: max height 1.5m or 5 boxes stacking

单击下面可查看定价,库存,交付和生命周期等信息

>>Laird(莱尔德)