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Revision: B

**LITE-ON DCC** 

RELEASE

BNS-OD-FC001/A4

## **LITE-ON Technology Corp. / Optoelectronics**

No.90, Chien 1 Road, Chung Ho, New Taipei City 23585, Taiwan, R.O.C. Tel: 886-2-2222-6181 Fax: 886-2-2221-1948 / 886-2-2221-0660



## 1. Description

SMD LEDs from Lite-On are available in miniature sizes and special configurations for automated PC board assembly and space-sensitive applications. These SMD LEDs are suitable for use in a wide variety of electronic equipment, including cordless and cellular phones, notebook computers, network systems, home appliances, and indoor signboard applications..

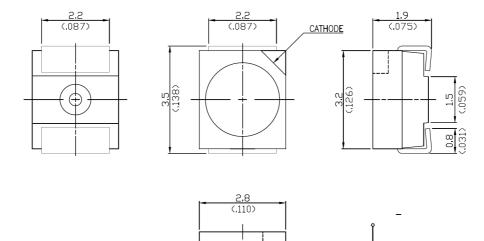
#### 1.1 Features

- Meet ROHS
- Package in 8mm tape on 7" diameter reels
- EIA STD package
- I.C. compatible
- Compatible with automatic placement equipment
- Compatible with infrared reflow solder process
- Preconditioning: accelerate to JEDEC level 3

## **1.2 Applications**

- Indicator and backlight for consumer appliance
- General Signage backlight
- Special lighting
- Industrial lighting

## 2. Package Dimensions



Part No.	Lens Color	Source Color
LTST-T680UWET	Yellow	White

#### Notes:

- 1. All dimensions are in millimeters.
- 2. Tolerance is ±0.2 mm (.008") unless otherwise noted.



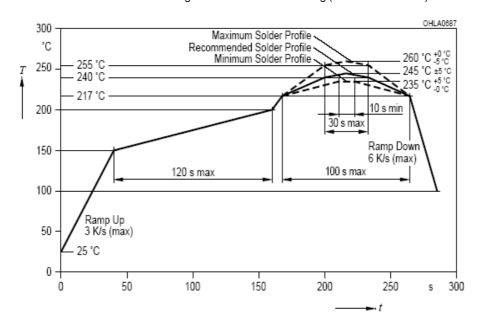
# 3. Rating and Characteristics

## 3.1 Absolute Maximum Ratings at Ta=25℃

Parameter	LTST-T680UWET	Unit
Power Dissipation	108	mW
Peak Forward Current (1/10 Duty Cycle, 0.1ms Pulse Width)	100	mA
DC Forward Current	30	mA
Operating Temperature Range	-40℃ to + 85℃	
Storage Temperature Range	-40℃ to + 100℃	

### 3.2 Suggest IR Reflow Condition for Pb Free Process:

#### IR-Reflow Soldering Profile for lead free soldering (Acc. to J-STD-020B)





### 3.3 Electrical / Optical Characteristics at Ta=25℃

Parameter	Symbol	Part No. LTST-	Min.	Тур.	Max.	Unit.	Test Condition	
Luminous Intensity	IV	T680UWET	Г 2100 - 3300		3300	mcd	IF = 20mA Note 1	
Viewing Angle	2θ <sub>1/2</sub>	T680UWET		120		deg	Note 2 (Fig.5)	
Chromaticity Coordinates	х	T680UWET		0.31			IF = 20mA	
	у	T680UWET		0.31		-	Note 3	
Forward Voltage	VF	T680UWET	2.8	-	3.6	V	IF = 20mA Note 4	
Reverse Current	IR	T680UWET	-	-	10	μΑ	VR = 5V Note 5	

#### Notes:

- 1. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve
- 2.  $\theta$ 1/2 is the off-axis angle at which the luminous intensity is half the axial luminous intensity.
- The chromaticity coordinates (x, y) is derived from the 1931 CIE chromaticity diagram
- Reverse voltage (VR) condition is applied to IR test only. The device is not designed for reverse operation.



## 4. Bin Rank

#### 4.1 VF Rank

Forward	Voltage Unit: V @	20mA
Bin Code	Min.	Max.
D7	2.8	3.0
D8	3.0	3.2
D9	3.2	3.4
D10	3.4	3.6

Tolerance for each forward voltage bin is +/- 0.1 volt

## 4.2 IV Rank

Luminous I	ntensity Unit : mcd	@20mA	
Bin Code	Min.	Max.	
X1	2100	2630	
X2	2630	3300	

Tolerance on each Intensity bin is +/-15%



## 4.3 Color Rank

Color Bin Table							est	@20mA			
Bin Code	Color Bin Limits			Bin Code	Color Bin Limits						
Bill Code	CIE-	Point1	Point2	Point3	Point4	DIII Code	CIE-	Point1	Point2	Point3	Point4
Z1	х	0.2818	0.2853	0.2931	0.2902	V4	х	0.3022	0.3041	0.3116	0.3103
21	у	0.3085	0.3001	0.3101	0.3189	X1	у	0.3335	0.3242	0.3338	0.3434
70	х	0.2853	0.2888	0.2959	0.2931	٧a	х	0.3041	0.3060	0.3128	0.3116
Z2	у	0.3001	0.2918	0.3013	0.3101	X2	у	0.3242	0.3151	0.3245	0.3338
Z3	х	0.2888	0.2920	0.2984	0.2959	X3	х	0.3060	0.3078	0.3141	0.3128
23	у	0.2918	0.2844	0.2935	0.3013	۸۵	у	0.3151	0.3064	0.3150	0.3245
Z4	х	0.2920	0.2948	0.3009	0.2984	X4	х	0.3078	0.3095	0.3152	0.3141
24	у	0.2844	0.2768	0.2859	0.2935	<b>A4</b>	у	0.3064	0.2984	0.3067	0.3150
Y1	х	0.2902	0.2931	0.2985	0.2961	1877	х	0.3103	0.3116	0.3207	0.3201
11	у	0.3189	0.3101	0.3170	0.3261	W1	у	0.3434	0.3338	0.3455	0.3552
Va	х	0.2931	0.2959	0.3008	0.2985	WO	х	0.3116	0.3128	0.3214	0.3207
Y2	у	0.3101	0.3013	0.3080	0.3170	W2	у	0.3338	0.3245	0.3362	0.3455
Y3	х	0.2959	0.2984	0.3030	0.3008	W3	х	0.3128	0.3141	0.3221	0.3214
13	у	0.3013	0.2935	0.2998	0.3080	VVS	у	0.3245	0.3150	0.3260	0.3362
Y4	х	0.2984	0.3009	0.3050	0.3030	W4	х	0.3141	0.3152	0.3226	0.3221
14	у	0.2935	0.2859	0.2919	0.2998	VV <del>4</del>	у	0.3150	0.3067	0.3175	0.3260
VE	х	0.2961	0.2985	0.3041	0.3022	\\/E	х	0.3201	0.3283	0.3285	0.3207
Y5	у	0.3261	0.3170	0.3242	0.3335	W5	у	0.3552	0.3620	0.3520	0.3455
Y6	х	0.2985	0.3008	0.3060	0.3041	W6	х	0.3207	0.3285	0.3287	0.3214
10	у	0.3170	0.3080	0.3151	0.3242	VVO	у	0.3455	0.3520	0.3420	0.3362
V7	х	0.3008	0.3030	0.3078	0.3060	\\/7	х	0.3214	0.3287	0.3289	0.3221
Y7	у	0.3080	0.2998	0.3064	0.3151	W7	у	0.3362	0.3420	0.3320	0.3260
Vo	х	0.3030	0.3050	0.3095	0.3078	\\/O	х	0.3221	0.3289	0.3291	0.3226
Y8	у	0.2998	0.2919	0.2984	0.3064	W8	у	0.3260	0.3320	0.3220	0.3175

Tolerance on each Hue bin (x, y) bin is +/- 0.01.



## 4.4 Chromaticity Coordinate

#### White Bin Structure 0.3900 0.3700 5665K 6020K W5 0.3500 6530K W1 W6 7120K W2 W7 X1 > 0.33007500K Y5 X2 W3 W8 8000K Υ1 Υ6 W4 Х3 9000K 0.3100 Υ2 **Z**1 **Y7** X4 **Z**2 **Y3** Υ8 **Z**3 0.2900 **Z**4 0.2700 0.2700 0.2800 0.2900 0.3000 0.3100 0.3300 0.3200 0.3400 Χ



# 5. Typical Electrical / Optical Characteristics Curves.

### (25℃ Ambient Temperature Unless Otherwise Noted)

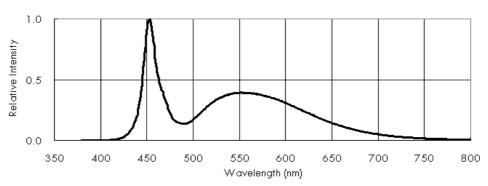


Fig.1 RELATIVE INTENSITY VS. WAVELENGTH

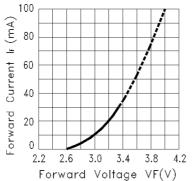


Fig.2 FORWARD CURRENT VS.
FORWARD VOLTAGE

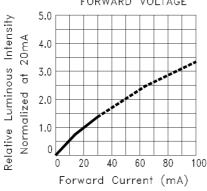


Fig.4 RELATIVE LUMINOUS
INTENSITY VS. FORWARD CURRENT

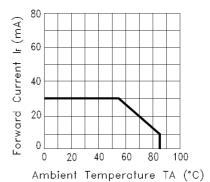


Fig.3 FORWARD CURRENT
DERATING CURVE

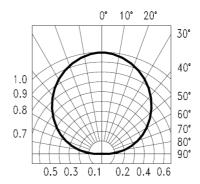


Fig.5 Spatial Distribution



### 6. User Guide

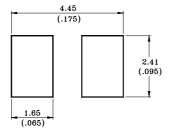
#### 6.1 Cleaning

Do not use unspecified chemical liquid to clean LED they could harm the package. If cleaning is necessary, immerse the LED in ethyl alcohol or isopropyl alcohol at normal temperature for less one minute.

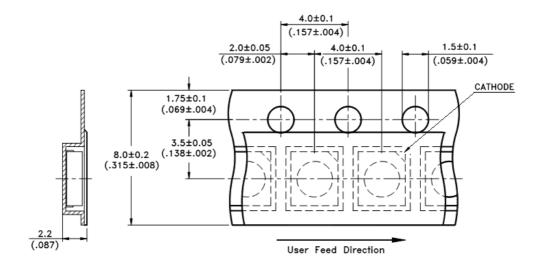
#### **6.2 Recommend Printed Circuit Board Attachment Pad**

Infrared / vapor phase

Reflow Soldering



### 6.3 Package Dimensions of Tape and Reel

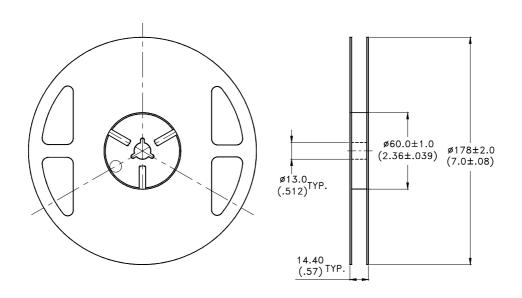


#### Note:

1. All dimensions are in millimeters (inches).



## **6.4 Package Dimensions of Reel**



#### Notes:

- 1. Empty component pockets sealed with top cover tape.
- 2. 7 inch reel 2000 pieces per reel.
- 3. The maximum number of consecutive missing lamps is two.
- 4. In accordance with ANSI/EIA 481 specifications.



#### 7. Cautions

#### 7.1 Application

The LEDs described here are intended to be used for ordinary electronic equipment (such as office equipment, communication equipment and household applications). Consult Liteon's Sales in advance for information on applications in which exceptional reliability is required, particularly when the failure or malfunction of the LEDs may directly jeopardize life or health (such as in aviation, transportation, traffic control equipment, medical and life support systems and safety devices).

#### 7.2 Storage

The package is sealed:

The LEDs should be stored at 30℃ or less and 70%RH or less. And the LEDs are limited to use within one year, while the LEDs is packed in moisture-proof package with the desiccants inside.

The package is opened:

The storage ambient for the LEDs should not exceed 30°C temperature and 60% relative humidity.

It is recommended that LEDs out of their original packaging are IR-reflowed within 168hrs.

For extended storage out of their original packaging, it is recommended that the LEDs be stored in a sealed container with appropriate desiccant, or in a desiccators with nitrogen ambient.

LEDs stored out of their original packaging for more than 168hrs should be baked at about 60  $^{\circ}$ C for at least 48 hours before solder assembly.

#### 7.3 Cleaning

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LED if necessary.

#### 7.4 Soldering

Recommended soldering conditions:

R	eflow soldering	Soldering iron		
Pre-heat 150~200℃ To		Temperature	300℃ Max.	
Pre-heat time 120 sec. Max.		Soldering time	3 sec. Max.	
Peak temperature 260℃ Max.			(one time only)	
Soldering time 10 sec. Max.(Max. two times)				

#### Notes:

Because different board designs use different number and types of devices, solder pastes, reflow ovens, and circuit boards, no single temperature profile works for all possible combinations.

However, you can successfully mount your packages to the PCB by following the proper guidelines and PCB-specific characterization

LITE-ON Runs both component-level verification using in-house **KYRAMX98** reflow chambers and board-level assembly. The results of this testing are verified through post-reflow reliability testing .Profiles used at LITE-ON are based on JEDEC standards to ensure that all packages can be successfully and reliably surface mounted.

Figure on page3 shows a sample temperature profile compliant to JEDEC standards. You can use this example as a generic target to set up your reflow process. You should adhere to the JEDEC profile limits as well as specifications and recommendations from the solder paste manufacturer to avoid damaging the device and create a reliable solder joint.

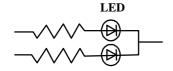


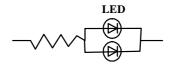
#### 7.5 Drive Method

A LED is a current-operated device. In order to ensure intensity uniformity on multiple LEDs connected in parallel in an application, it is recommended that a current limiting resistor be incorporated in the drive circuit, in series with each LED as shown in Circuit A below.

#### Circuit model A

# Circuit model B





- (A) Recommended circuit.
- (B) The brightness of each LED might appear different due to the differences in the I-V characteristics of those LEDs.

### 7.6 ESD (Electrostatic Discharge)

Static Electricity or power surge will damage the LED.

Suggestions to prevent ESD damage:

- Use of a conductive wrist band or anti-electrostatic glove when handling these LEDs.
- All devices, equipment, and machinery must be properly grounded.
- Work tables, storage racks, etc. should be properly grounded.
- Use ion blower to neutralize the static charge which might have built up on surface of the LED's plastic lens as a result of friction between LEDs during storage and handling.

ESD-damaged LEDs will exhibit abnormal characteristics such as high reverse leakage current, low forward voltage, or "no lightup" at low currents.

To verify for ESD damage, check for " lightup " and Vf of the suspect LEDs at low currents.

The Vf of "good" LEDs should be >2.0V@0.1mA for InGaN product and >1.4V@0.1mA for AllnGaP product.



# 8. Reliability Test

No.	Test item	Test condition	Reference standard
1	Resistance to soldering heat	Tsld = 260℃, 10sec. 3 times	JEITA ED-4701 300 301
2	Solderability	Tsld=245±5℃ (Lead Free Solder, Coverage ≧ 95% of the dipped surface)	JEITA ED-4701 300 303
3	Thermal Shock	85 ± 5℃ ~ -30℃ ± 5℃ 30min 30min 100cycles	JEITA ED-4701 300 307
4	Temperature Cycle	-55℃ ~ 25℃ ~ 100℃ ~ 25℃ 30min 5min 30min 5min 100cycles	JEITA ED-4701 100 105
5	High Temperature Storage	100℃ 1000hrs	JEITA ED- 4701 200 201
6	Low Temperature Storage	-55℃ 1000hrs	JEITA ED-4 701 200 202
7	Temperature Humidity Storage	60℃/90%RH 300hrs	JEIT A ED-4701 100 103
8	Room temp life test	25℃, IF: Max current , 1000hrs	

## 9. Others

The appearance and specifications of the product may be modified for improvement without prior notice.



## 10. Suggested Checking List

#### Training and Certification

- 1. Everyone working in a static-safe area is ESD-certified?
- 2. Training records kept and re-certification dates monitored?

#### Static-Safe Workstation & Work Areas

- 1. Static-safe workstation or work-areas have ESD signs?
- 2. All surfaces and objects at all static-safe workstation and within 1 ft measure less than 100V?
- 3. All ionizer activated, positioned towards the units?
- 4. Each work surface mats grounding is good?

#### Personnel Grounding

- 1. Every person (including visitors) handling ESD sensitive (ESDS) items wears wrist strap, heel strap or conductive shoes with conductive flooring?
- 2. If conductive footwear used, conductive flooring also present where operator stand or walk?
- 3. Garments, hairs or anything closer than 1 ft to ESD items measure less than 100V\*?
- 4. Every wrist strap or heel strap/conductive shoes checked daily and result recorded for all DLs?
- 5. All wrist strap or heel strap checkers calibration up to date?

Note: \*50V for InGaN LED.

#### **Device Handling**

- 1. Every ESDS items identified by EIA-471 labels on item or packaging?
- 2. All ESDS items completely inside properly closed static-shielding containers when not at static-safe workstation?
- 3. No static charge generators (e.g. plastics) inside shielding containers with ESDS items?
- 4. All flexible conductive and dissipative package materials inspected before reuse or recycles?

#### Others

- 1. Audit result reported to entity ESD control coordinator?
- 2. Corrective action from previous audits completed?
- 3. Are audit records complete and on file?

# 单击下面可查看定价,库存,交付和生命周期等信息

>>Lite-On(光宝)