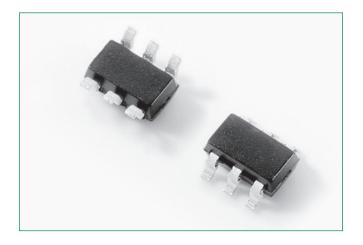


# SP3025, 2.5V, 30A Diode Array



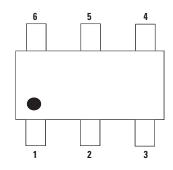


#### **Description**

The SP3025 is a low-capacitance, TVS Diode Array designed to provide protection against ESD (electrostatic discharge), CDE (cable discharge events), EFT (electrical fast transients), and lightning induced surges for highspeed, differential data lines. It's packaged in a SOT23-6L and each device can protect up 4 channels up to 30A (IEC 61000-4- 5  $2^{nd}$  edition,) and up to  $\pm 30kV$  ESD (IEC 61000-4-2).

The SP3025 with its low capacitance and low clamping voltage makes it ideal for high-speed data interfaces such as 1GbE applications found in notebooks, switches, etc.

#### **Pinout**

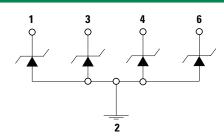


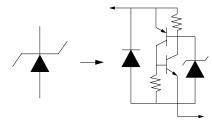
#### **Features**

- ESD, IEC 61000-4-2, ±30kV contact, ±30kV air
- EFT, IEC 61000-4-4, 40A (5/50ns)
- Lightning, IEC 61000-4-5 2<sup>nd</sup> edition, 30A  $(t_p = 8/20 \mu s)$
- Low capacitance of 1.7pF@0V (TYP)
- Low leakage current of 1nA (TYP) at 2.5V

- Low operating and clamping voltage
- Provides protection for two differential data pairs (4 channels) up to 30A
- Halogen free, Lead free and RoHS compliant
- Moisture Sensitivity Level (MSL -1)

#### **Functional Block Diagram**





Life Support Note: Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

#### **Applications**

- •10/100/1000 Ethernet
- WAN/LAN Equipment
- Desktops, Servers and Notebooks
- LVDS Interfaces
- Integrated Magnetics
- Smart TV
- 2.5G/5G/10G Ethernet



#### **Absolute Maximum Ratings**

Symbol	Parameter	Value	Units
l <sub>pp</sub>	Peak Current (t <sub>p</sub> =8/20µs)	30	А
T <sub>OP</sub>	Operating Temperature	-40 to 125	°C
T <sub>STOR</sub>	Storage Temperature	-55 to 150	°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

## Electrical Characteristics (T<sub>OP</sub>=25°C)

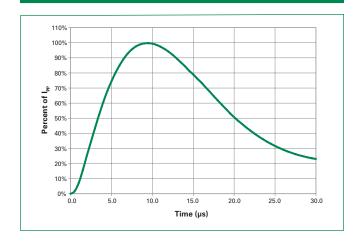
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Reverse Standoff Voltage	V <sub>RWM</sub>	$I_R = 1\mu A$			2.5	V
Breakdown Voltage	V <sub>BR</sub>	I <sub>R</sub> = 1mA	5.5	7.0		V
Reverse Leakage Current	I <sub>LEAK</sub>	V <sub>R</sub> =2.5V		1	100	nA
Holding Voltage	V <sub>HOLD</sub>	I/O to GND		1.6		V
Clamp Voltage <sup>1</sup>	V <sub>C</sub>	$I_{pp}$ =30A, $t_p$ =8/20 $\mu$ s		9	11	V
Dynamic Resistance <sup>2</sup> R <sub>DYN</sub>		TLP, t <sub>p</sub> =100ns		0.14		Ω
ECD With the all Valles and 3	11/2/4-2-13	IEC 61000-4-2 (Contact Discharge)	±30			kV
ESD Withstand Voltage <sup>1,3</sup> V <sub>ES</sub>	V <sub>ESD</sub>	IEC 61000-4-2 (Air Discharge)	±30			kV
Diada Canasitanas	C <sub>I/O-GND</sub>	Reverse Bias=0V. f=1MHz		1.7	2.5	~F
Diode Capacitance <sup>1</sup>	C <sub>I/O-I/O</sub>	neverse bidS=UV, I=TIVIFIZ		0.8	1.2	pF

#### Notes:

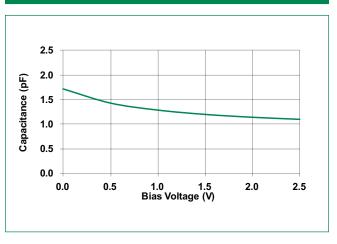
- Parameter is guaranteed by design and/or component characterization.
- 2. Transmission Line Pulse (TLP) test setting: Std.TDR(500),tp=100ns, tr=0.2ns ITLP and VTLP averaging window: start t1=70ns to end t2=90ns

  3. Device stressed with ten non-repetitive ESD pulses.

#### 8/20µs Pulse Waveform

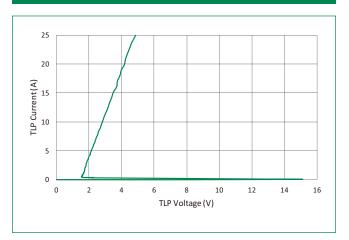


#### Capacitance vs. Reverse Bias

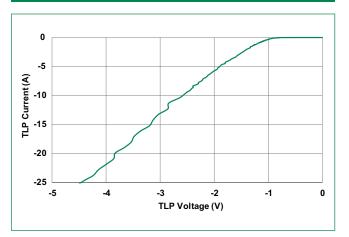




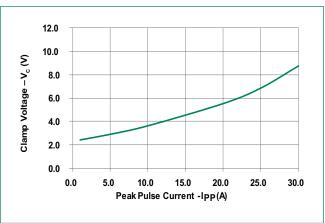
#### Positive Transmission Line Pulsing (TLP) Plot



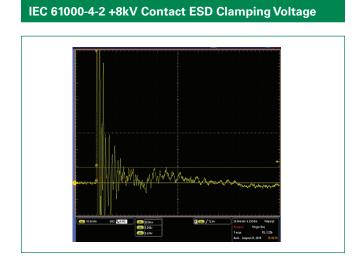
#### **Negative Transmission Line Pulsing (TLP) Plot**

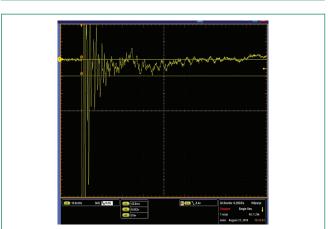


#### Clamping Voltage vs. Peak Pulse Current



# IEC 61000-4-2 -8kV Contact ESD Clamping Voltage

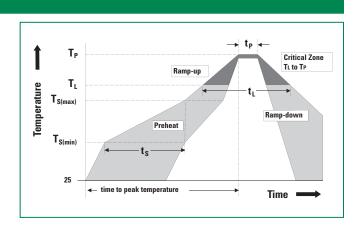






### **Soldering Parameters**

Reflow Condition			
-Temperature Min (T <sub>s(min)</sub> )	150°C		
-Temperature Max (T <sub>s(max)</sub> )	200°C		
-Time (min to max) (t <sub>s</sub> )	60 – 180 secs		
Average ramp up rate (Liquidus) Temp (T <sub>L</sub> ) to peak			
T <sub>S(max)</sub> to T <sub>L</sub> - Ramp-up Rate			
- Temperature (T <sub>L</sub> ) (Liquidus)	217°C		
- Temperature (t <sub>L</sub> )	60 - 150 seconds		
Peak Temperature (T <sub>p</sub> )			
Time within 5°C of actual peak Temperature (t <sub>p</sub> )			
Ramp-down Rate			
Time 25°C to peak Temperature (T <sub>p</sub> )			
Do not exceed			
	- Temperature Min (T <sub>s(min)</sub> ) - Temperature Max (T <sub>s(max)</sub> ) - Time (min to max) (t <sub>s</sub> )  In pup rate (Liquidus) Temp (T <sub>L</sub> ) to peak Ramp-up Rate - Temperature (T <sub>L</sub> ) (Liquidus) - Temperature (t <sub>L</sub> )  sature (T <sub>p</sub> )  5°C of actual peak Temperature (t <sub>p</sub> )  Rate peak Temperature (T <sub>p</sub> )		



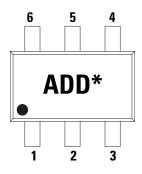
#### **Ordering Information**

Part Number	Package	Min. Order Qty.
SP3025-04HTG	SOT23-6L	3000

#### **Product Characteristics**

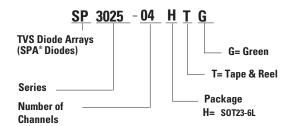
Lead Plating	Matte Tin	
Lead Material	Copper Alloy	
Lead Coplanarity	0.004 inches (0.102mm)	
Substrate Material	Silicon	
Body Material	Molded Compound	
Flammability	UL Recognized compound meeting flammability rating V-0	

## **Part Marking System**



AD : Part code
D : Assembly code
\* : Date code

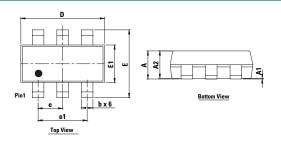
### **Part Numbering System**

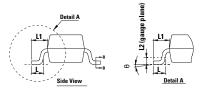




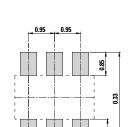
# **Lightning Surge Protection- SP3025**

#### Package Dimensions — SOT23-6L







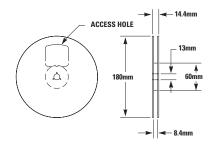


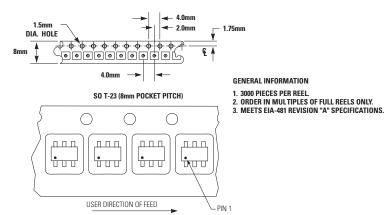
Recommended soldering pad layout (unit :mm)

Complete	Millimeters			
Symbol	Min	Nom	Max	
А	-	-	1.45	
A1	0.00	-	0.15	
A2	0.90	1.15	1.30	
b	0.30	-	0.50	
b1	0.30	0.40	0.45	
С	0.08	-	0.22	
c1	0.08	0.13	0.20	
D	2.75	2.90	3.05	
E	2.60	2.80	3.00	
E1	1.45	1.60	1.75	
е	0.95 BSC			
e1	1.90 BSC			
L	0.30	0.50	0.60	
L1	0.60 REF			
L2	0.25 BSC			
Θ	0°	4°	8°	

# Embossed Carrier Tape & Reel Specification — SOT23-6L

8mm TAPE AND REEL





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