

**CD4148WTP** 

1:6

# Switching Diode CD4148WTP



#### **FEATURES**

- Silicon epitaxial planar diode
- SMD chip pattern, available in various dimension included 1206 & 0805
- Leadfree and RoHS compliance components
- For small signal switching and operating ambient temperature less than 55°C and voltage withstand less than 60V; not suitable for AC switching input as rectified circuit and high reverse voltage location

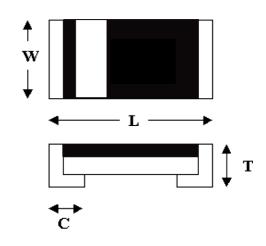
#### **MECHANICAL CHARACTERISTICS**

■ Size: 0603

Weight: approx. 4mgMarking: Cathode terminal

## **DIMENSIONS**

Dimension/mm	0603
L	1.55±0.1
W	0.80±0.1
Т	0.65±0.1
C	0.35±0.1



## THERMAL CHARACTERISTICS<sup>1)</sup>

Parameter at T <sub>amb</sub> =25°C <sup>1)</sup>	Symbol	Value	Unit
Forward Power Dissipation	D	200	mW
Power derating above 25°C	P <sub>tot</sub>	1.6	mW/ °C
Junction Temperature	T <sub>i</sub>	150	°C
Thermal Resistance Junction to Ambient air	$R_{\theta JA}$	375	°C/W
Operating& Storage Temperature range	$T_{stg}$	-55 to 150	°C

<sup>1)</sup> Valid provided that electrodes are kept at ambient temperature.

Ref No: CD4148WTP V3.2 NOV 25, 2009 www.lizgroup.com

## **CD4148WTP**

2.6

## MAXIMUM RATING1)

Parameter at T <sub>amb</sub> =25°C <sup>1)</sup>	Symbol	Value	Unit
Repetitive Peak Reverse Voltage	$V_{RRM}$	75	V
Average rectified current sin half wave rectification with resistive load	I <sub>F(AV)</sub>	100	mA
Repetitive Peak Forward Current at T <sub>amb</sub> =25°C	$I_{FRM}$	200	mA
Non-Repetitive Surge Forward Current at t<1s and T <sub>i</sub> =25°C	I <sub>FSM</sub>	400	mA
at t≦8.3ms and T <sub>i</sub> =25°C		800	mA

<sup>1)</sup> Valid provided that electrodes are kept at ambient temperature.

# **ELECTRICAL CHARACTERISTICS**<sup>1)</sup>

Parameter at T <sub>amb</sub> =25°C <sup>1)</sup>	Symbol	Value	Unit
Forward Voltage at I <sub>F</sub> =10mA	V	1.0 <sub>MAX</sub>	V
at I <sub>F</sub> =100mA	$V_{F}$	1.25 <sub>MAX</sub>	V
Leakage Current at V <sub>R</sub> =20V	т	0.025 <sub>MAX</sub>	uA
Leakage Current at V <sub>R</sub> =75V	$\mathbf{I}_{R}$	5 <sub>MAX</sub>	uA
Capacitance at V <sub>R</sub> =0V, f=1MHz	C <sub>tot</sub>	4 <sub>MAX</sub>	pF
Reverse Recovery Time at $I_F = I_R = 10 \text{mA}$ , $R_L = 100 \Omega$	t <sub>rr</sub>	4 <sub>MAX</sub>	ns

<sup>1)</sup> Valid provided that electrodes are kept at ambient temperature.

### TYPICAL CHARACTERISTICS

Figure 1. Forward Characteristic

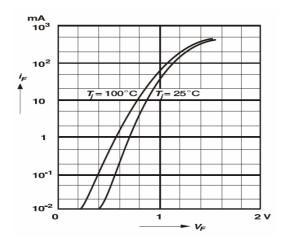
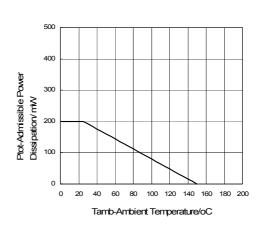


Figure 2. Power De-rating

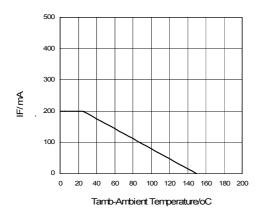


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# **CD4148WTP**

3:6

Figure 3. Forward Current De-rating



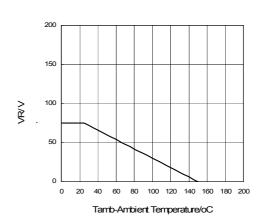


Figure 4. Reverse Voltage De-rating

# **TEST CHARACTERISTICS**

Test Item	Test Condition	Requirement
Solderability	Sn bath at 245±5°C for 2±0.5s	>95% area tin covered
Resistance to Soldering Heat	Sn bath at 260±5°C for 10±2s	V <sub>F</sub> ,V <sub>R</sub> & I <sub>R</sub> within spec; no mechanical damage
Humidity Steady State	At 85°C 85%RH for 168hrs	V <sub>F</sub> ,V <sub>R</sub> & I <sub>R</sub> within spec
Continue Forward Operating Life	At 25°C $I_F = 1.1I_F$ for 1000hrs	V <sub>F</sub> ,V <sub>R</sub> & I <sub>R</sub> within spec
Thermal Shock	-55 ±5°C/5min to 150±5°C/5min for 10cycles	V <sub>F</sub> ,V <sub>R</sub> & I <sub>R</sub> within spec
Bending Strength	Bending up to 2mm for 1cycle	V <sub>F</sub> ,V <sub>R</sub> & I <sub>R</sub> within spec; no mechanical damage

Ref No: CD4148WTP V3.2 NOV 25, 2009

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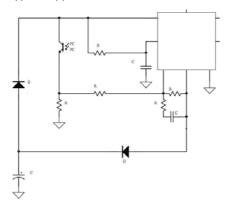


**CD4148WTP** 

4:6

#### **APPLICATIONS**

- Function: suit for small signal switching application
- Typical Application circuit:



- Typical Product field: General application except high reverse voltage location
- Soldering Condition:

## Soldering Condition & Caution

■ Recommended Soldering Condition (Refer to IPC/JEDEC J-STD-020D 4-1&5.2)

Recommended Profile Condition	Sn-Pb Soldering	Leadfree Soldering	Wave Soldering	
Ramp-up rate (from pre-heat stage)	<3°C/s	<3°C/s	∆T<150°C	
Dro host Tomporature 9. Time	100-150 °C	150-200 °C	100-150 °C	
Pre-heat Temperature & Time	60-120s	60-120s	60-120s	
Coldoring Tomporature 9. Time	183 ℃	217 °C	260±5°C	
Soldering Temperature & Time	60-150s	60-150s	5±2s	
Dook Tomporature	230±5°C	245±5°C	260±5°C	
Peak Temperature	<260°C	<260°C	200±3 C	
Time within 5°C of peak temperature	10-20s	20-30s	-	
Ramp-down rate	<6°C/s	<6°C/s	<6°C/s	
Time 25°C to peak temperature	<6min	<8min	-	

Manual Soldering: Approx. 350°C for 3s, avoid solder iron tip direct touch the components body

Ref No: CD4148WTP V3.2 NOV 25, 2009

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CHIP DIODE

## Recommended Soldering Profile

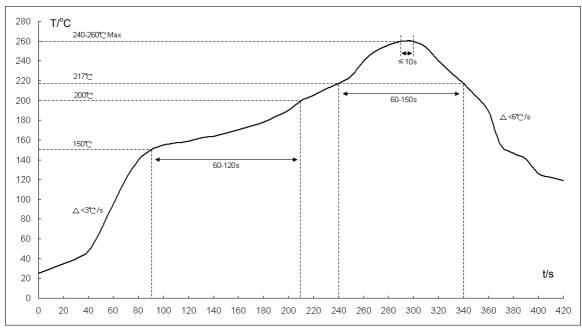


Fig1: Reflow soldering profile for lead-free solder (SnAgCu)

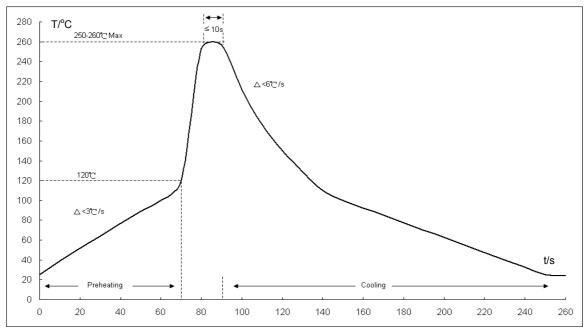


Fig2: Wave soldering profile

- \*1. The recommended profiles are referring to IPC/JEDEC J-STD-020D & IEC-60068-2-58
- \*2. Chip diodes are able to stand maximum soldering temperature up to  $260^{\circ}$ C max for 10s, and the soldering cycles with max 3 times, referring to IEC-60068-2-58

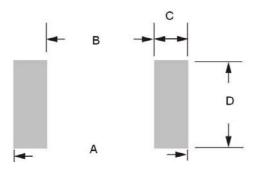
6:6



## **Switching Diode**

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■ Recommended Soldering Footprint:



■ Reflow/Wave Soldering					
Droduct Cito	Dimension/ mm				
Product Size	Α	В	С	D	
0603	1.8-2.6	0.8	0.5-0.9	0.8-1.0	

■ Storage Condition: Product termination solderability can degrade due to high temperature and humidity or chemical environment. Storage condition must be in an ambient temperature of <40°C and ambient humidity of <75%RH, and free from chemical.

### **ENVIRONMENTAL CHARACTERISTICS**

	Hazardous Substance or Element/ppm					
Product	Pb	Cd	Hg	Cr <sup>6+</sup>	PBB	PBDE
	<1000	<100	<1000	<1000	<1000	<1000
	Halogen Substance/ ppm					
Product	F	Cl	E	Br	I	Total
	<900	<900	) <9	000	<900	<1500

#### **PACKING METHOD**

Product	Quality/Reel	Reel Size	Tape
	5,000pcs	7"	Paper

## **DISCLAIMERS**

These products are not designed for use in applications where any failure or malfunction may resulted in personal injury, death or severe property or environmental damage such as medical, military, aircraft, space or life support equipments.

单击下面可查看定价,库存,交付和生命周期等信息

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