1A Single Chip Li-lon and Li-Polymer Charger

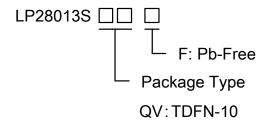
General Description

The LP28013S is a complete constant-current/ constant voltage linear charger for single cell lithium-ion battery. No external sense resistor is needed, and no blocking diode is required due to the internal MOSFET architecture. Thermal feedback regulates the charge current to limit the die temperature during high power operation or high ambient temperature. The charge voltage is fixed at 4.2V, and the charge current can be programmed externally by ISET pin with a single resistor.

The LP28013S automatically terminates the charge cycle when the charge current drops to 1/10 setting current value after the final float voltage is reached.

Other features include charge current monitor, under voltage lockout, automatic recharge, status pins and battery temperature detection.

Order Information



Features

- Input Voltage up to 28V
- ◆ Input Over Voltage Protection: 7V
- Short-circuit protection
- Programmable Charge Current up to 1A
- ◆ 1µA Battery Reverse Current
- Over temperature Sensing Protection
- Protection of Reverse Connection of Battery
- Constant-Current/Constant-Voltage Operation with Thermal Regulation
- ◆ TDFN-10 Package
- ◆ RoHS Compliant and 100% Lead (Pb)-Free

Applications

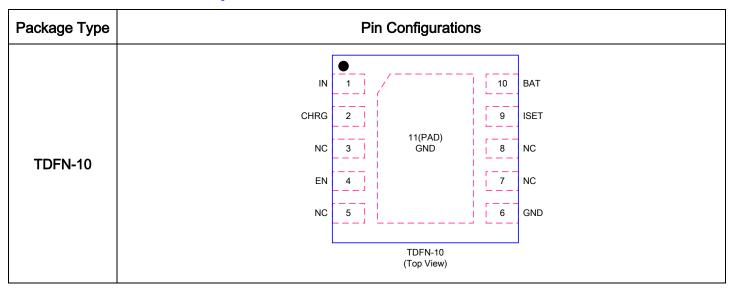
- ♦ Portable Media Players/Game
- ♦ Power Bank
- ♦ Bluetooth Applications
- ♦ PDA/MID

Marking Information

Device	Marking	Package	Shipping		
	LPS				
LP28013SQVF	LP28013S	TDFN-10	5K/REEL		
	YWX				
Marking indication:					
Y:Production year W:Production week X: Series Number					

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Functional Pin Description

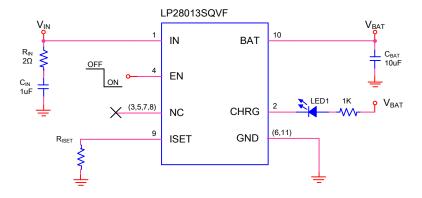


Pin Description

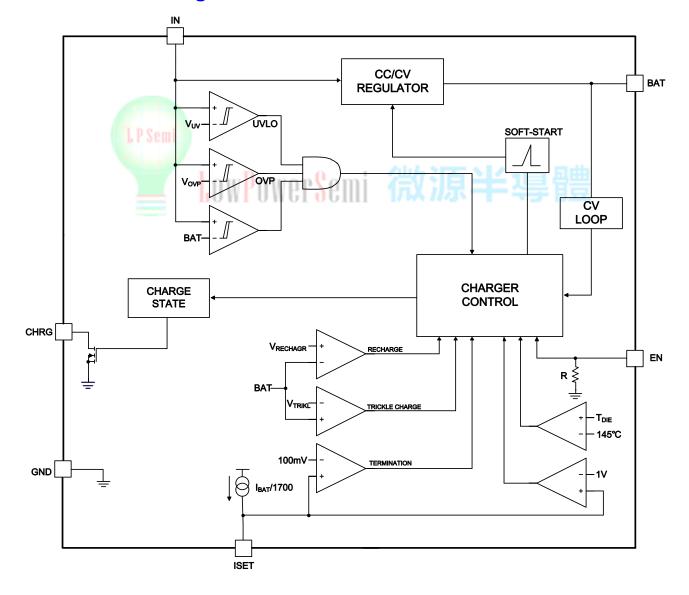
No.	NAME	DESCRIPTION
1	IN P 26 III	IN is the input power source. Connect to a wall adapter.
2	CHRG	Open-Drain Charge Status Output. When the battery is charging, the CHRG pin is pulled low by an internal NMOS. When the charge cycle is completed, the pin could be pulled High by an external pull high resistor.
3/5/7/8	NC	No Connector.
4	EN	Charge Enable Input (active low).
6/11	GND	GND is the connection to system ground.
9	ISET	Charge Current Program. The charge current is programmed by connecting a 1% resistor (RISET) to ground.
10	BAT	BAT is the connection to the battery. Typically a 10µF capacitor is needed.

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Typical Application Circuit



Functional Block Diagram



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Absolute Maximum Ratings Note 1

	•	
	Input Voltage to GND	0.3V to 28V
	BAT Voltage GND	5V to 15V
	Other pin to GND	0.3V to 6.5V
	Maximum Junction Temperature(T _J)	150°C
	Maximum Soldering Temperature (at leads, 10 sec)	260°C
	Operating Junction Temperature Range	20°C to 85°C
	Storage Temperature	
Th	nermal Information	
	Maximum Power Dissipation (P _D , T _A =25°C)	1.5W
	Thermal Resistance (θ _{JA})	65°C/W
ES	SD Susceptibility	
	HBM(Human Body Model)	2KV
\diamond	MM(Machine Model)	200V

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





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Electrical Characteristics

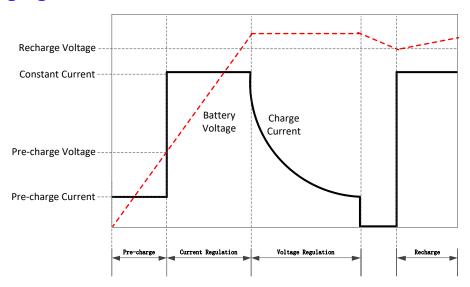
(T_A=25°C, V_{IN} =5V, unless otherwise noted.)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNITS
Vin	Input Voltage		4.5	5	6.5	V
		R _{ISET} =17K,Charge Mode		300		uA
lin	Input Supply Current	V _{BAT} =4.2V, Charge Terminated		160		uA
		R _{ISET} =NC,Shutdown Mode		200		uA
V _{FLOAT}	Regulated Output (Float) Voltage	I _{BAT} =40mA,	4.158	4.2	4.242	V
Vuv	VIN Under Voltage Lockout Threshold	From V _{IN} Low to High		3.5		V
Vuv_Hys	Under Voltage Lockout Hysteresis			110		mV
Vovp	Input Voltage OVP	V _{IN} Rising		7		V
V _{OVP_HYS}	OVP Hysteresis			150		mV
		RISET=3.4k, Current Mode	450	500	550	mA
I _{BAT}	BAT Pin Current	R _{ISET} =17k, Current Mode	90	100	110	mA
		V _{BAT} =4.2V (V _{IN} =float or 0V)		1		uA
I _{TRIKL}	Trickle Charge Current	V _{BAT} <v<sub>TRIKL, R_{ISET}=10k, Charge Mode</v<sub>		10		%I _{BAT}
V _{TRIKL}	Trickle Charge Threshold Voltage	V _{BAT} Rising		2.6		V
VTRIKL-HYS	Trickle Charge Hyste <mark>r</mark> esis Vol <mark>ta</mark> ge	orComi 微源生	息	150		mV
I _{TERM}	Termination Current Threshold	OI COILL IV WAR		10		%I _{BAT}
VISET	ISET Pin Voltage	R _{ISET} =10k, Current Mode	0.9	1	1.1	V
ISTAT	CHRG Pin Sink Current	V _{CHRG} =5V			1	uA
VSTAT	CHRG Pin Output Low Voltage	I _{CHRG} =5mA			0.5	V
ΔV _{RECHRG}	Recharge Battery Threshold Voltage	VFLOAT-VRECHRG		150		mV
V _{ASD}	V _{IN} - V _{BAT} Lockout Threshold Voltage	V _{BAT} =3.5V,V _{IN} Rising		150		mV
V _{EN_ON}	EN Logic-Low Voltage Threshold				0.4	V
V _{EN_OFF}	EN Logic-High Voltage Threshold		1.4			V
T _{LIM}	Junction Temperature in Constant Temperature Mode		145	I	°C	

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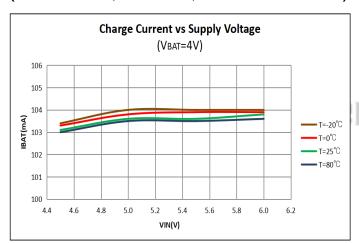
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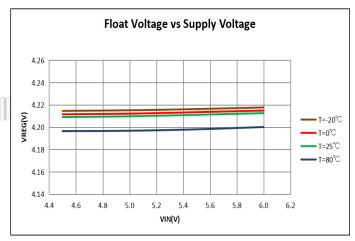
Typical Charging Profile

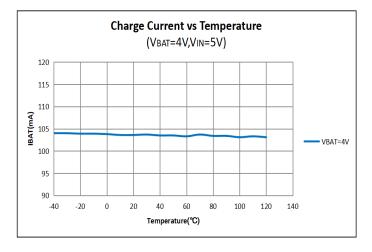


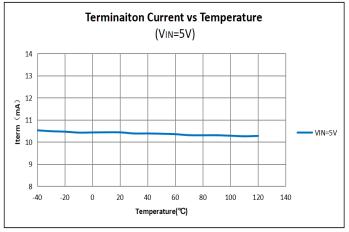
Typical Performance Characteristics

(CIN=COUT=10uF, RISET=16.5K, unless otherwise noted)









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Application Information

The LP28013S has built-in input voltage surge protection as high as +28V. The charger IC will be automatically disabled when the input voltage is higher than 7V. A charge cycle begins when EN is high, the voltage at the IN pin rises above the UVLO threshold level and a program resistor is connected from the ISET pin to ground. If the BAT pin is less than 2.6V, the charger enters trickle charge mode. In this mode, the LP28013S approximately supplies 1/10 programmed charge current to bring the battery voltage up to a safe level for full current charging. When the BAT pin voltage rises above 2.6V, the charger enters constant-current mode(CC), where the ISET programmed charge current is supplied to the battery. When the Battery Voltage approaches the final float voltage, It enters constant-voltage mode(CV) and the charge current begins to decrease, and the battery full indication is set when the charge current in the CV mode is reduced to the programmed full battery current (1/10).

Charge Termination Com

A charge cycle is terminated when the charge current falls to 1/10th the ISET programmed value after the final float voltage is reached. This condition is detected by using an internal, filtered comparator to monitor the ISET pin. When the ISET pin voltage falls below 100mV for longer than t_{TERM} (typically 1ms), charging is terminated.

Charge Current Program

The charge current (I_{BAT}) is set by a resistor (R_{ISET}) connecting from the ISET pin to GND. The relationship of the charge current and the programming resistance is established by the following equations ($V_{ISET}=1V$).

$$I_{BAT} = \frac{1700 \times V_{ISET}}{R_{ISET}}$$

Automatic Recharge

Once the charge cycle is terminated, the LP28013S continuously monitors the voltage on the BAT pin. A charge cycle restarts when the battery voltage falls below 4.05V (which corresponds to approximately 80% to 90% battery capacity). This ensures that the battery is kept at or near a fully charged condition and eliminates the need for periodic charge cycle initiations.

Charge Status Indicator (CHRG)

After application of a 5V source, the input voltage rises above the UVLO and sleep thresholds (V_{IN}>V_{BAT}+V_{ASD}), but is less than OVP (V_{IN}<V_{OVP}), CHRG has two different states: strong pull-down (~5mA) and high impedance. The strong pull-down state indicates that the LP28013S is in a charge cycle. When the charger is entered CV mode and once the charge current has reduced to the battery full charge current threshold (I_{TERM}), the CHRG pin will become high impedance.

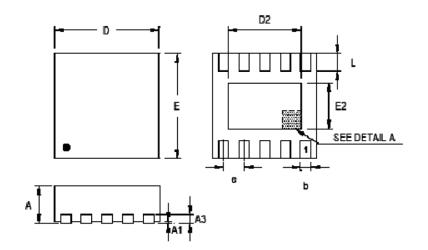
Function	CHRG	
Charging	Low	
Charge Finish	Hi-Z	

Thermal Limit

An internal thermal feedback loop reduces charge current if junction temperature attempts to rise above a preset value of approximately 145°C. This feature protects the device from excessive temperature and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the device. The charge current can be set according to typical (not worst-case) ambient temperature with the assurance that the charger will automatically reduce the current in worst-case conditions.

Packaging Information

TDFN-10(3*3)





<u>DETAIL A</u> Pin #1 ID and Tie Bar Mark Options

Note: The configuration of the Pin#1 identifier is optional, but must be located within the zone indicated.

L P Semi

Symbol	Dimensions in millimeters		Dimensions in inches		
	Min	Max (F)	Min	Max	
Α -	0.700	0.800	0.028	0.031	
A1	0.000	0.050	0.000	0.002	
A3	0.175	0.250	0.007	0.010	
b	0.180	0.300	0.007	0.012	
D	2.950	3.050	0.116	0.120	
D2	2.300	2.650	0.091	0.104	
E	2.950	3.050	0.116	0.120	
E2	1.500	1.750	0.059	0.069	
е	0.500		0.020		
L	0.350	0.450	0.014	0.018	

单击下面可查看定价,库存,交付和生命周期等信息

>>LOW POWER(微源半导体)