

Darlington Transistors

PNP Silicon

We declare that the material of product compliance with RoHS requirements.

LMBTA63LT1G
LMBTA64LT1G

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CES}	-30	Vdc
Collector–Base Voltage	V_{CBO}	-30	Vdc
Emitter–Base Voltage	V_{EBO}	-10	Vdc
Collector Current – Continuous	I_C	-500	mAdc

DEVICE MARKING

LMBTA63LT1G = 2U; LMBTA64LT1G = 2V

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, ⁽¹⁾ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	225	mW
		1.8	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, ⁽²⁾ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300	mW
		2.4	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage ($I_C = -100 \mu\text{Adc}$)	$V_{(BR)CEO}$	-30	-	Vdc
Collector Cutoff Current ($V_{CB} = -30 \text{ Vdc}$)	I_{CBO}	-	-100	nAdc
Emitter Cutoff Current ($V_{EB} = -10 \text{ Vdc}$)	I_{EBO}	-	-100	nAdc

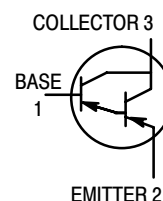
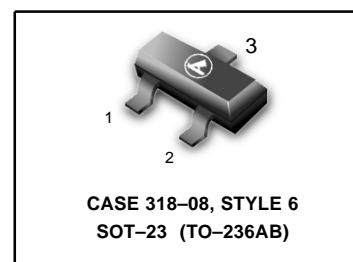
ON CHARACTERISTICS

DC Current Gain ⁽³⁾ ($I_C = -10 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc}$) ($I_C = -10 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc}$) ($I_C = -100 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc}$) ($I_C = -100 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc}$)	LMBTA63LT1G LMBTA64LT1G LMBTA63LT1G LMBTA64LT1G	h_{FE}	5,000 10,000 10,000 20,000	- - - -	- - - -
Collector–Emitter Saturation Voltage ($I_C = -100 \text{ mAdc}, I_B = -0.1 \text{ mAdc}$)		$V_{CE(sat)}$	-	-1.5	Vdc
Base–Emitter On Voltage ($I_C = -100 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc}$)		$V_{BE(on)}$	-	-2.0	Vdc

SMALL-SIGNAL CHARACTERISTICS

Current–Gain – Bandwidth Product ($I_C = -10 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc}, f = 100 \text{ MHz}$)	f_T	125	-	MHz
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- FR-5 = 1.0 x 0.75 x 0.062 in.
- Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina.
- Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.



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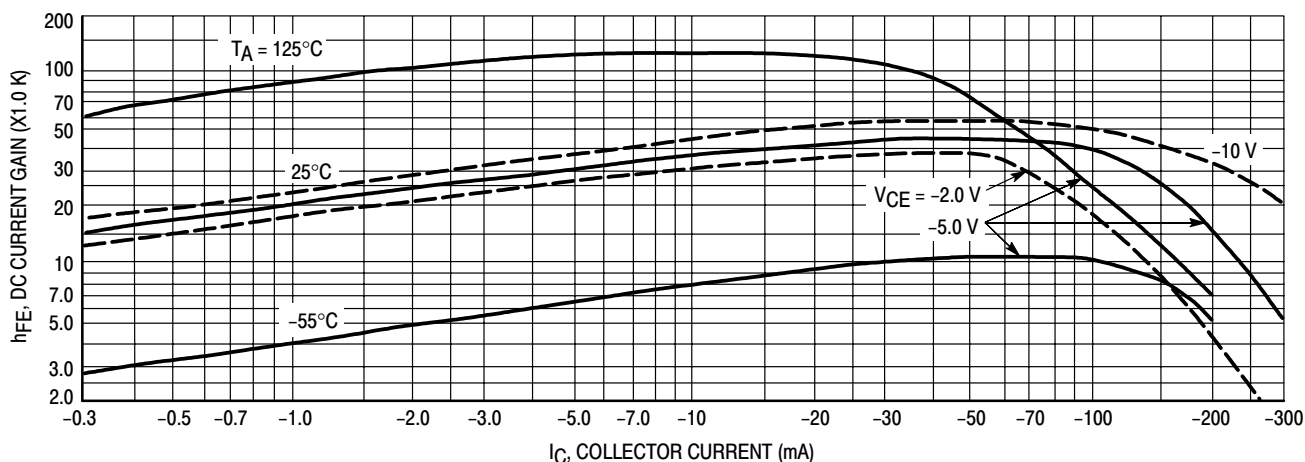


Figure 1. DC Current Gain

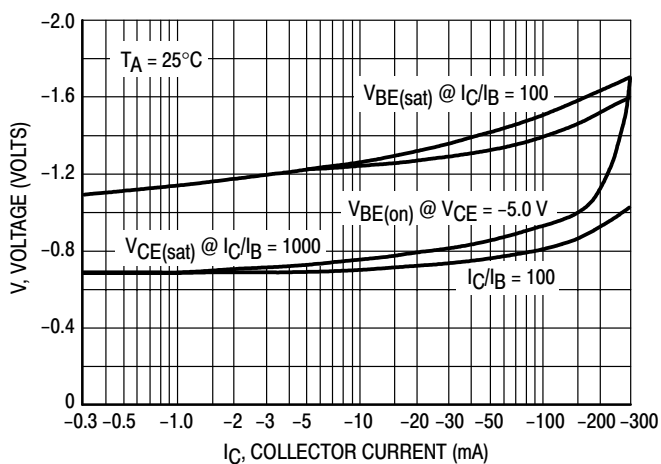


Figure 3. "On" Voltage

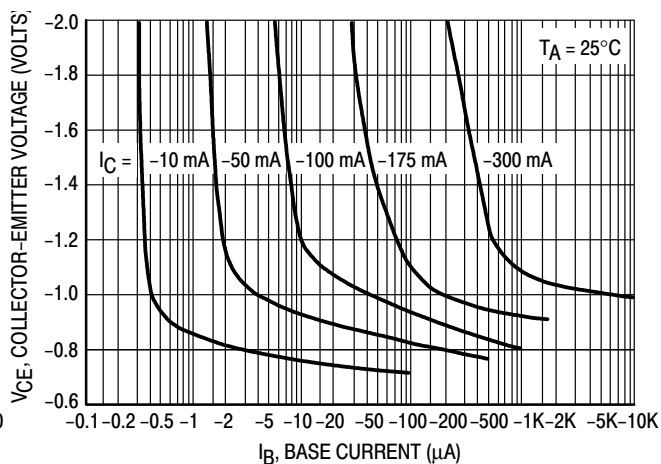


Figure 2. Collector Saturation Region

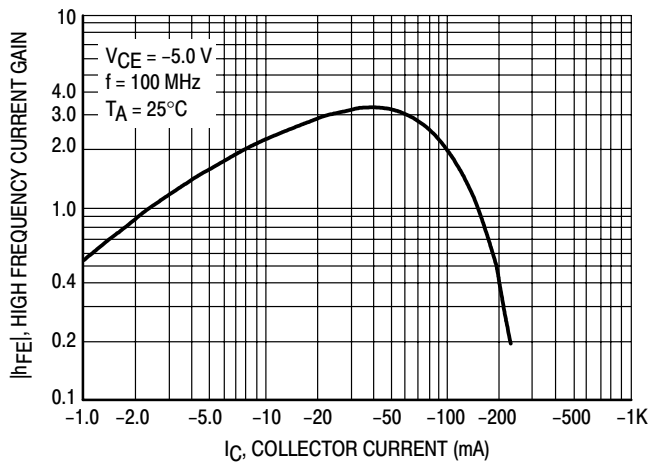


Figure 4. High Frequency Current Gain

单击下面可查看定价，库存，交付和生命周期等信息

[>>LRC\(乐山无线电\)](#)