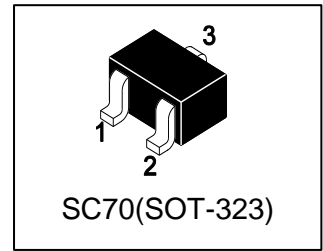


LMUN5214T1G

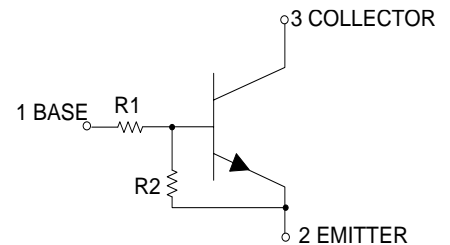
S-LMUN5214T1G

Bias Resistor Transistor
NPN Silicon Surface Mount Transistor
with Monolithic Bias Resistor Network



1. FEATURES

- Simplifies circuit design
- Reduces board space and component count
- The SC-70/SOT-323 package can be soldered using wave or reflow.
- The modified gull-winged leads absorb thermal stress during soldering eliminating the possibility of damage to the die.
- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- S- prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.



2. DEVICE MARKING AND RESISTOR VALUES

Device	Marking	R1(K)	R2(K)	Vin(V)	Shipping
LMUN5214T1G	8D	10	47	-6 ~+40	3000/Tape&Reel
LMUN5214T3G	8D	10	47	-6 ~+40	10000/Tape&Reel

3. MAXIMUM RATINGS(Ta = 25°C)

Parameter	Symbol	Limits	Unit
Collector-Emitter Voltage	V _{CEO}	50	V
Collector-Base Voltage	V _{CBO}	50	V
Collector Current — Continuous	I _C	100	mA

4. THERMAL CHARACTERISTICS

Parameter	Symbol	Limits	Unit
Total Device Dissipation, FR-5 Board (Note 1) @ TA = 25°C Derate above 25°C	PD	202 1.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient(Note 1)	R _{θJA}	618	°C/W
Junction and Storage temperature	T _J , T _{stg}	-55~+150	°C

1. FR-4 @ Minimum Pad.

5. ELECTRICAL CHARACTERISTICS (Ta= 25°C)

OFF CHARACTERISTICS

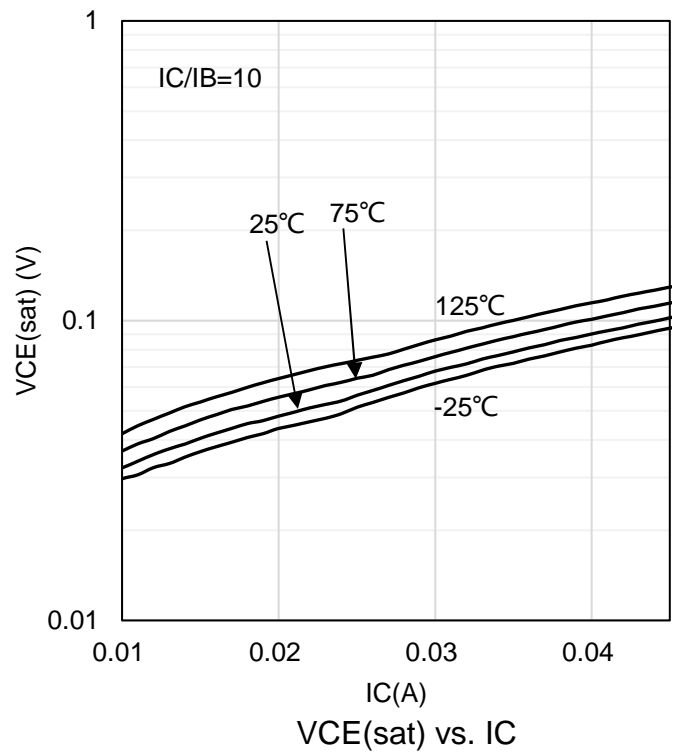
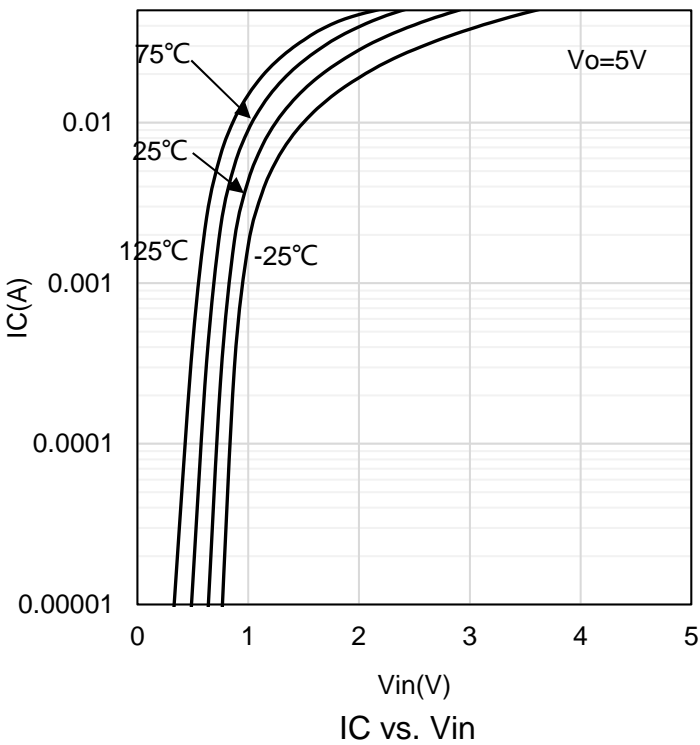
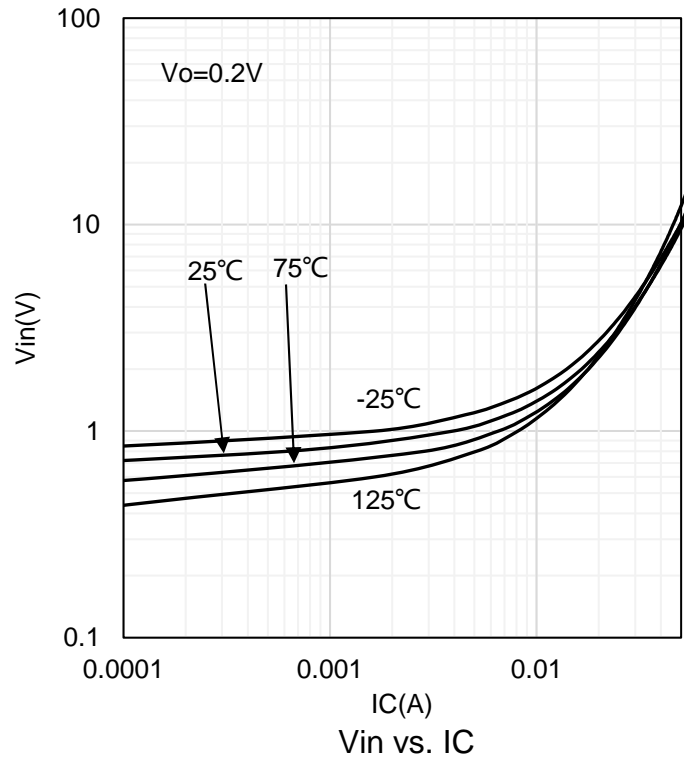
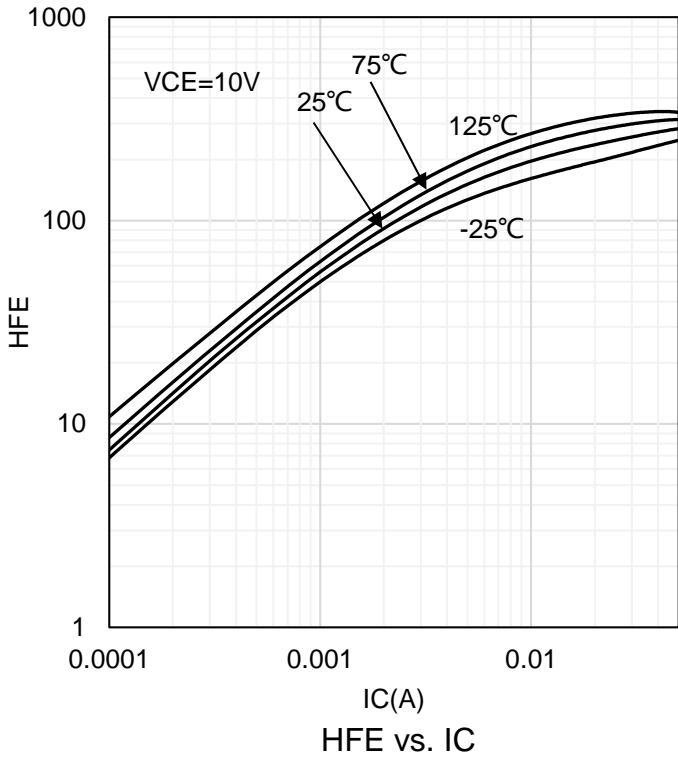
Characteristic	Symbol	Min.	Typ.	Max.	Unit
Collector–Emitter Breakdown Voltage (IC = 2.0 mA, IB = 0)	VBR(CEO)	50	-	-	V
Collector–Base Breakdown Voltage (IC = 10 μA, IE = 0)	VBR(CBO)	50	-	-	V
Collector-Base Cutoff Current (VCB = 50 V, IE = 0)	ICBO	-	-	100	nA
Collector-Emitter Cutoff Current (VCE = 50 V, IB = 0)	ICEO	-	-	500	nA
Emitter-Base Cutoff Current (VEB = 6.0 V, IC = 0)	IEBO	-	-	0.2	mA

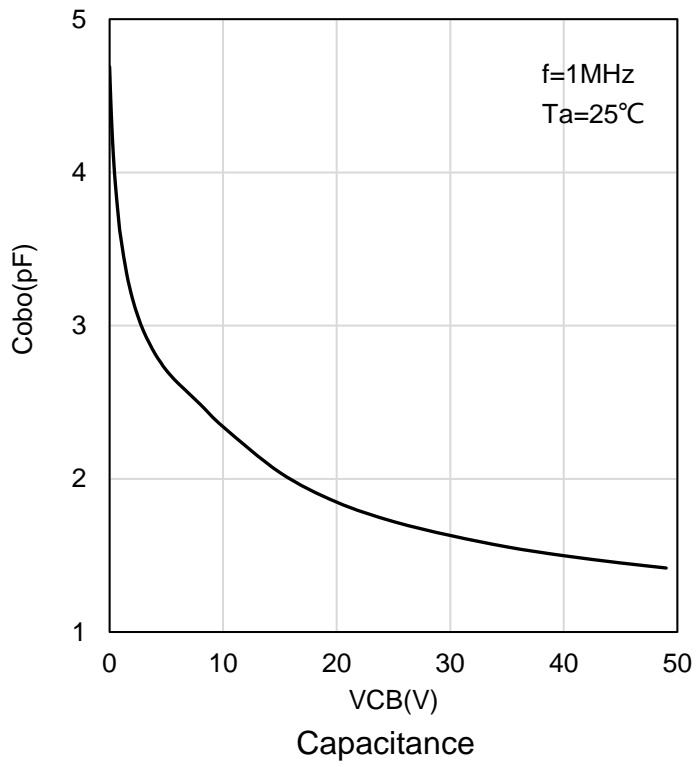
ON CHARACTERISTICS (Note 2.)

DC Current Gain (IC = 5.0 mA, VCE = 10 V)	HFE	80	140	-	
Collector–Emitter Saturation Voltage (IC = 10 mA, IB = 0.3 mA)	VCE(sat)	-	-	0.25	V
Output Voltage (on) (VCC = 5.0 V, VB = 2.5 V, RL = 1.0KΩ)	VOL	-	-	0.2	V
Output Voltage (off) (VCC = 5.0 V, VB = 0.5 V, RL = 1.0KΩ)	VOH	4.9	-	-	V
Input Voltage(off) (VCE=5.0V,IC=100μA)	Vi(off)	-	-	0.5	V
Input Voltage(on) (VCE=0.2V,IC=5.0mA)	Vi(on)	2	-	-	V
Input Resistor	R1	7	10	13	KΩ
Resistor Ratio	R1/R2	0.17	0.21	0.25	

2. Pulse Test: Pulse Width < 300 μs, Duty Cycle < 2.0%

6.ELECTRICAL CHARACTERISTICS CURVES

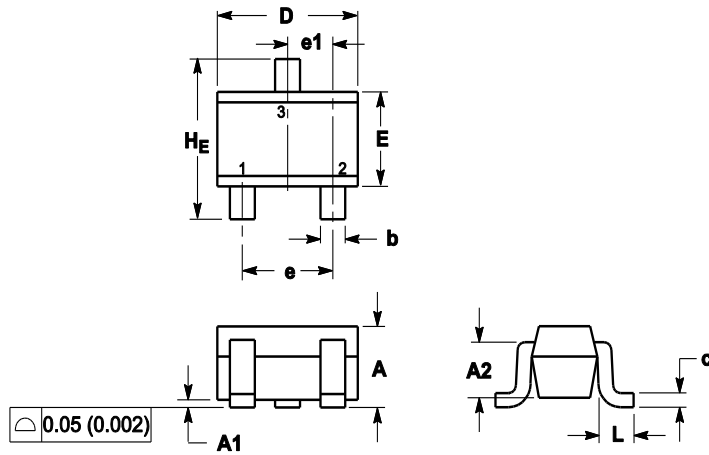


6.ELECTRICAL CHARACTERISTICS CURVES(Con.)

7. OUTLINE AND DIMENSIONS

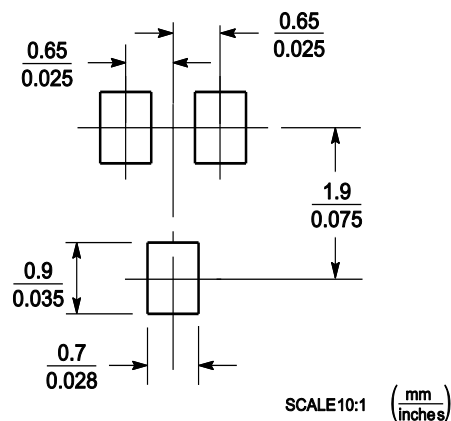
Notes:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.



DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.90	1.00	0.032	0.035	0.0394
A1	0.00	0.05	0.10	0	0.002	0.004
A2	0.70REF			0.028REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.10	0.18	0.25	0.004	0.007	0.01
D	1.80	2.10	2.20	0.071	0.083	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65REF			0.026REF		
L	0.20	0.38	0.56	0.008	0.015	0.022
H _E	2.00	2.10	2.40	0.079	0.083	0.095

8. SOLDERING FOOTPRINT



DISCLAIMER

- Curve guarantee in the specification. The curve of test items with electric parameter is used as quality guarantee. The curve of test items without electric parameter is used as reference only.
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