

General Purpose Transistors PNP Silicon

These transistors are designed for general purpose amplifier applications. They are housed in the SOT–323/SC–70 which is designed for low power surface mount applications.

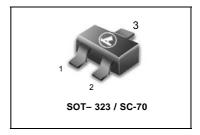
Features

We declare that the material of product compliance with RoHS requirements.

MAXIMUM RATINGS

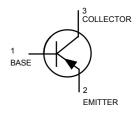
Rating	Symbol	BC856	BC857	BC858	Unit
Collector–Emitter Voltage	V_{CEO}	-65	-4 5	-30	V
Collector-Base Voltage	V _{CBO}	-80	– 50	-30	V
Emitter-Base Voltage	V _{EBO}	-5.0	-5.0	-5.0	V
Collector Current — Continuous	I _c	-100	-100	-100	mAdc

LBC856AWT1G, BWT1G
LBC857AWT1G, BWT1G
CWT1G
LBC858AWT1G, BWT1G
CWT1G
S-LBC856AWT1G, BWT1G
S-LBC857AWT1G, BWT1G
CWT1G
S-LBC858AWT1G, BWT1G
CWT1G
CWT1G



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR– 5 Board, (1) T _A = 25°C	P _D	150	mW
Thermal Resistance, Junction to Ambient	R _{θJA}	833	°C/W
Junction and Storage Temperature	T_J , T_stg	–55 to +150	°C



DEVICE MARKING

S-LBC856AWT1G= 3A; S-LBC856BWT1G= 3B;S-LBC857AWT1G= 3E; S-LBC857BWT1G = 3F; S-LBC857CWT1G= 3G;S-LBC858AWT1G= 3J; S-LBC858BWT1G= 3K;S-LBC858CWT1G= 3L

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit			
OFF CHARACTERISTICS								
Collector–Emitter Breakdown Voltage	LBC856 Series	856 Series		_	_			
$(I_c = -10 \text{ mA})$	LBC857 Series	$V_{(BR)CEO}$	- 45	_	_	V		
	LBC858 Series		- 30	_	_			
Collector–Emitter Breakdown Voltage	LBC856 Series		- 80	_	_			
$(I_C = -10 \mu A, V_{EB} = 0)$	LBC857B Only	$V_{(BR)CES}$	- 50	_	_	٧		
	LBC858 Series		- 30	_	_			
Collector–Base Breakdown Voltage	LBC856 Series LBC857 Series V (BR)CBC		- 80	_	_			
$(I_{\rm C} = -10 \mu\text{A})$		$V_{(BR)CBO}$	- 50	_	_	V		
	LBC858 Series		- 30	_	_			
Emitter–Base Breakdown Voltage	LBC856 Series		- 5.0	_	_			
(I _E = -1.0 μA)	LBC857 Series	$V_{(BR)EBO}$	- 5.0	_	_	V		
	LBC858 Series		- 5.0	_	_			
Collector Cutoff Current (V _{CB} = -30	I _{CBO}	_	_	– 15	nA			
$(V_{CB} = -30)$	· CBO	_	_	- 4.0	μΑ			

^{1.}FR-5=1.0 x 0.75 x 0.062in



LBC856AWT1G, BWT1G LBC857AWT1G, BWT1G, CWT1G LBC858AWT1G, BWT1G, CWT1G S-LBC856AWT1G, BWT1G S-LBC857AWT1G, BWT1G, CWT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

	0					
	Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS	5					
DC Current Gain	DC Current Gain					_
$(I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V})$	LBC856A, LBC857A, LBC858A		125	180	250	
	LBC856B,LBC857B, LBC858B		220	290	475	
	LBC857C, LBC858C		420	520	800	
Collector–Emitter Saturati	Collector–Emitter Saturation Voltage (I _C = –10 mA, I _B = – 0.5 mA)		_	_	- 0.3	
	$(I_{C} = -100 \text{ mA}, I_{B} = -5.0 \text{ mA})$	V _{CE(sat)}	_	_	-0.65	V
Base–Emitter Saturation Voltage (I _C = -10 mA, I _B = -0.5 mA)		\/	_	- 0.7	_	V
	$(I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA})$	$V_{BE(sat)}$	_	-0.9	_	V
Base–Emitter Voltage (I c	= -2.0 mA, V _{CE} = -5.0 V)	\/	- 0.6	_	- 0.75	V
(I _c :	$V_{BE(on)}$	_	_	- 0.82	V	
SMALL-SIGNAL CHA	ARACTERISTICS					
Current-Gain — Bandwidth Product		f _T	100	_	_	MHz
$(I_{C} = -10 \text{ mA}, V_{CE} = -5.0 \text{ mA})$	•					
Output Capacitance (V $_{CB}$ = -10 V, f = 1.0 MHz)			_	_	4.5	pF
Noise Figure					10	dB
$(I_C = -0.2 \text{ mA}, V_{CE} = -5.0)$	NF			.0	u.b	

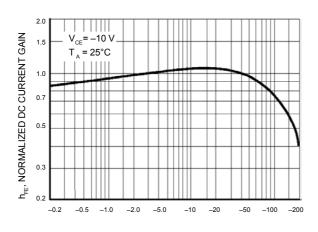
ORDERING INFORMATION (Pb-Free)

Device	Package	Shipping	
LBC856AWT1G series	SOT-23	3000/Tape & Reel	
LBC856AWT3G series	SOT-23	10000/Tape & Reel	

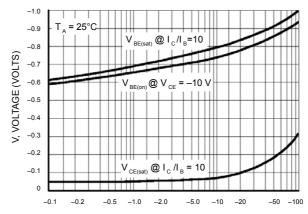


LBC856AWT1G, BWT1G LBC857AWT1G, BWT1G, CWT1G LBC858AWT1G, BWT1G, CWT1G S-LBC856AWT1G, BWT1G S-LBC857AWT1G, BWT1G, CWT1G S-LBC858AWT1G, BWT1G, CWT1G

LBC857/LBC858

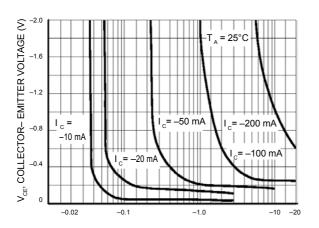


I_C, COLLECTOR CURRENT (mAdc)
Figure 1. Normalized DC Current Gain



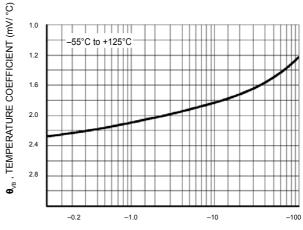
I_c, COLLECTOR CURRENT (mAdc)

Figure 2. "Saturation" and "On" Voltages



I_B, BASE CURRENT (mA)

Figure 3. Collector Saturation Region



I _c , COLLECTOR CURRENT (mA)

Figure 4. Base–Emitter Temperature Coefficient

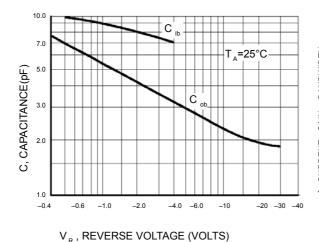
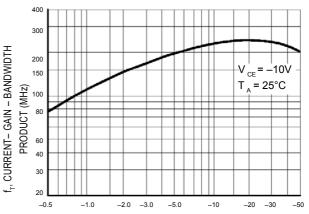


Figure 5. Capacitances



I_c, COLLECTOR CURRENT (mAdc)

Figure 6. Current-Gain - Bandwidth Product



LBC856AWT1G, BWT1G LBC857AWT1G, BWT1G, CWT1G LBC858AWT1G, BWT1G, CWT1G S-LBC856AWT1G, BWT1G S-LBC857AWT1G, BWT1G, CWT1G S-LBC858AWT1G, BWT1G, CWT1G

-1.0

LBC856

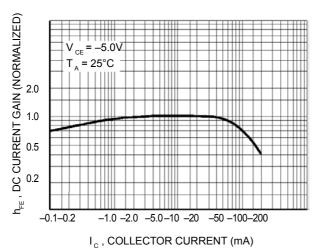


Figure 7. DC Current Gain

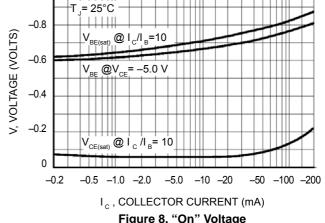


Figure 8. "On" Voltage

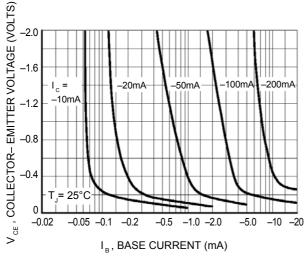


Figure 9. Collector Saturation Region

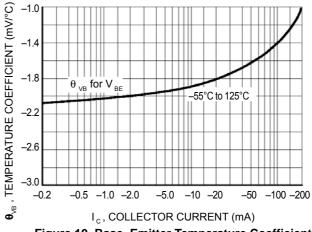


Figure 10. Base-Emitter Temperature Coefficient

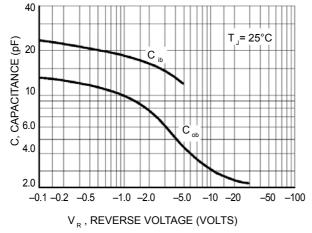


Figure 11. Capacitance

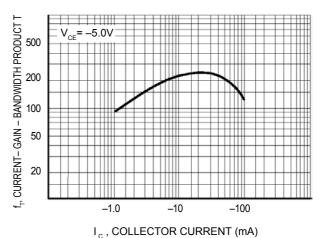


Figure 12. Current-Gain - Bandwidth Product



LBC856AWT1G, BWT1G LBC857AWT1G, BWT1G, CWT1G LBC858AWT1G, BWT1G, CWT1G S-LBC856AWT1G, BWT1G S-LBC857AWT1G, BWT1G, CWT1G S-LBC858AWT1G, BWT1G, CWT1G

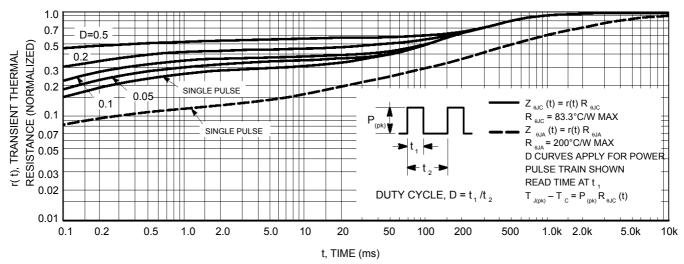
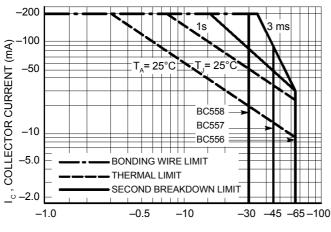


Figure 13. Thermal Response



 V_{CE} , COLLECTOR-EMITTER VOLTAGE (V)

Figure 14. Active Region Safe Operating Area

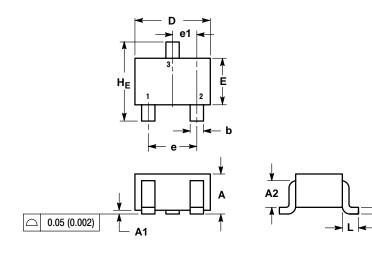
The safe operating area curves indicate I $_{\rm C}$ –V $_{\rm CE}$ limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 14 is based upon T $_{J(pk)}$ = 150°C; T $_{C}$ or T $_{A}$ is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided T $_{J(pk)} \leq 150$ °C. T $_{J(pk)}$ may be calculated from the data in Figure 13. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.



LBC856AWT1G, BWT1G LBC857AWT1G, BWT1G, CWT1G LBC858AWT1G, BWT1G, CWT1G S-LBC856AWT1G, BWT1G S-LBC857AWT1G, BWT1G, CWT1G S-LBC858AWT1G, BWT1G, CWT1G

SC-70 / SOT-323



- VOIEs.

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.80	0.90	1.00	0.032	0.035	0.040	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
A2	0.7 REF			0.028 REF			
b	0.30	0.35	0.40	0.012	0.014	0.016	
C	0.10	0.18	0.25	0.004	0.007	0.010	
D	1.80	2.10	2.20	0.071	0.083	0.087	
E	1.15	1.24	1.35	0.045	0.049	0.053	
е	1.20	1.30	1.40	0.047	0.051	0.055	
e1	0.65 BSC			0.026 BSC			
Ĺ	0.425 REF			0.017 REF			
HE	2.00	2.10	2.40	0.079	0.083	0.095	

GENERIC MARKING DIAGRAM

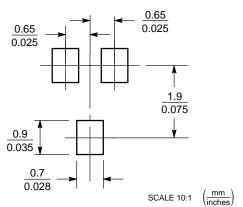


XX= Specific Device Code Μ = Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

SOLDERING FOOTPRINT*



单击下面可查看定价,库存,交付和生命周期等信息

>>LRC(乐山无线电)