

LESD8D12T5G ESD PROTECTION DIODE

Discription

The LESD8D12T5G is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

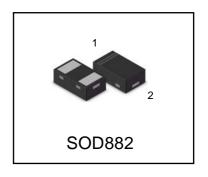
Applications

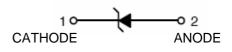
- I Cellular phones audio
- I MP3 players
- I Digital cameras
- I Portable applicationss
- I mobile telephone

Features

- Small Body Outline Dimensions:
 - 0.039" x 0.024"(1.0 mm x 0.60 mm)
- Low Body Height: 0.020" (0.50 mm) Max
- Stand-off Voltage: 3.3 V 12 V
- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- We declare that the material of product compliance with RoHS requirements.

LESD8D12T5G





Ordering information

Device	Marking	Shipping
LESD8D12T5G	Н	10000/Tape&Reel

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Air Contact Contact discharge		±15 ±8	kV kV
ESD Voltage Per Human Body Model		16	kV
Total Power Dissipation on FR-5 Board (Note 1)	PD	150	Mw
@ T _A =25℃			
Junction and Storage Temperature Range	TJ,TSTG	-55 to 150	°C
Lead Solder Temperature – Maximum (10	TL	260	°C
Second Duration)			

Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-5 = 1.0*0.75*0.62 in.

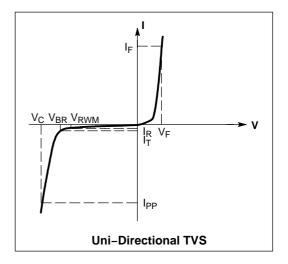


LESD8D12T5G

ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted)

Symbol	Parameter	
I _{PP}	Maximum Reverse Peak Pulse Current	
V _C	Clamping Voltage @ IPP	
V _{RWM}	Working Peak Reverse Voltage	
I _R	Maximum Reverse Leakage Current @ V_{RWM}	
V _{BR}	Breakdown Voltage @ I _T	
Ι _Τ	Test Current	
١ _F	Forward Current	
V _F	Forward Voltage @ I _F	
P _{pk}	Peak Power Dissipation	
С	Capacitance @ $V_R = 0$ and f = 1.0 MHz	



ELECTRICAL CHARACTERISTICS (TA=25 °C unless otherwise noted, VF=0.9V Max. @ IF=10Ma for all type	es)
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	V _{RWM}	I _R	V_{BR}	Ι _Τ	I _{PP}	Vc	P _{PK}	С
	(V)	(μA)	(V)	(mA)	(A)	(V)	(W)	(pF)
Device		@	@ I _T			@ Max I_{PP}	(8*20 µs)	
		V _{RWM}	(Note 2)		(Note 3)	(Note 3)		
	Max	Max	Min		Max	Max	Тур	Тур
LESD8D12T5G	12	1.0	13.3	1.0	5.9	23.7	140	30

Other voltage available upon request.

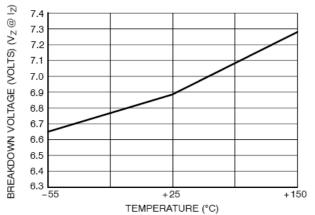
2. V_{BR} is measured with a pulse test current IT at an ambient temperature of $25^\circ\!\!{\rm C}$

3. Surge current waveform per Figure 3.



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TYPICAL CHARACTERISTICS



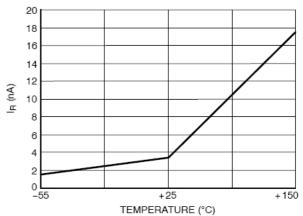


Figure 1. Typical Breakdown Voltage versus Temperature

Fig 2. Typical Leakage Current versus Temperature

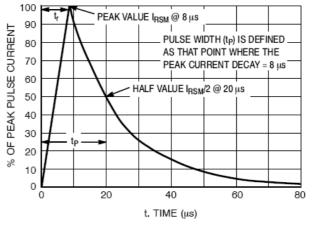
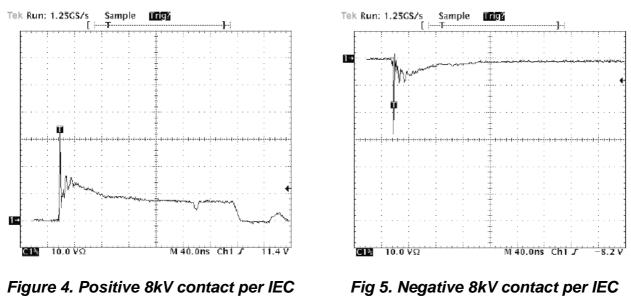


Figure 3. 8*20 µs Pulse Waveform



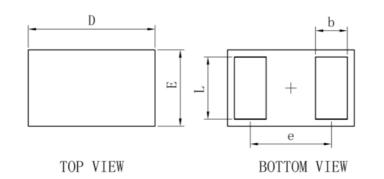
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OUTLINE AND DIMENSIONS



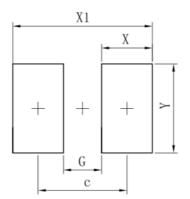
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Dim	Min				
D	0.95	1.00	1.05		
Е	0.55	0.60	0.65		
е	-	0.64	-		
L	0.44	0.49	0.54		
b	0.20 0.25 0.3				
А	0.43 0.48 0.5		0.53		
A1	0 - 0.0		0.05		
A3 0. 127REF.					
All Dimensions in mm					



SIDE VIEW

SOLDERING FOOTPRINT

S0D882



Dimensions	(mm)
с	0.70
G	0.30
Х	0.40
X1	1.10
Y	0.70

S0D882



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>>LRC(乐山无线电)

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