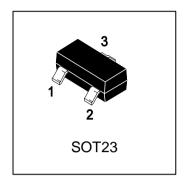


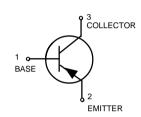
L2SA1037AKRLT1G S-L2SA1037AKRLT1G

General Purpose Transistors PNP Silicon

1. FEATURES

- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- S- prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.





2. DEVICE MARKING AND ORDERING INFORMATION

Device	Marking	Shipping		
L2SA1037AKRLT1G	FR	3000/Tape&Reel		
L2SA1037AKRLT3G	FR	10000/Tape&Reel		

3. MAXIMUM RATINGS(Ta = 25°C)

Parameter	Symbol	Limits	Unit
Collector-Emitter Voltage	VCEO	-50	V
Collector-Base voltage	VCBO	-60	V
Emitter-Base Voltage	VEBO	-6	V
Collector current-continuous	IC	-150	mΑ
Collector power dissipation	Pc	0.2	W
Junction temperature	Tj	150	$^{\circ}\!\mathbb{C}$
Storage temperature	Tstg	-55~+150	$^{\circ}$



L2SA1037AKRLT1G,S-L2SA1037AKRLT1G General Purpose Transistors PNP Silicon

4. ELECTRICAL CHARACTERISTICS (Ta= 25°C)

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Collector-Emitter Breakdown Voltage	V(BR)CEO	-50	-	-	V
(IC =-1.0mA)	V(BIX)OLO				
Emitter-Base Breakdown Voltage	V(BR)EBO	-6	-	-	V
$(IE = -50\mu A)$	V(BR)EBO				
Collector-Base Breakdown voltage	V(BR)CBO	-60	ı	-	V
$(IC = -50\mu A)$	V(BR)OBO				
Collector Cutoff Current	ICBO	-	1	-0.1	μΑ
(VCB = -60 V)	ЮВО				
Emitter Cutoff Current	IEBO	-	1	-0.1	μΑ
(VEB = -6V)	ILDO				
Collector-Emitter Saturation Voltage	VCE(Sat)	1	-	-0.5	V
(IC =-50mA,IB =-5mA)	VOL(Gat)				
Base–Emitter Saturation Voltage	VBE(Sat)	-	-	-1.1	V
(IC = -100mA, IB = -10mA)	VDL(Gat)				
DC current transfer ratio	HFE	180	-	390	
(VCE = -6 V, IC = -1mA)	1111 -				
Transition frequency	fT	1	140	-	MHz
(VCE = - 12 V, IE = 2mA, f=30MHz)					
Output capacitance	Cob	ı	4	5	pF
(VCB = - 12 V, IE = 0A, f =1MHz)	COD				

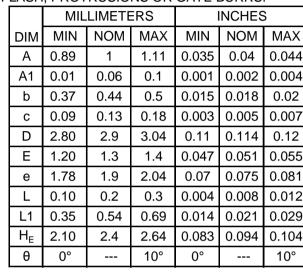


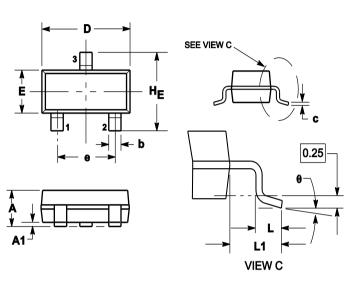
L2SA1037AKRLT1G,S-L2SA1037AKRLT1G General Purpose Transistors PNP Silicon

5.OUTLINE AND DIMENSIONS

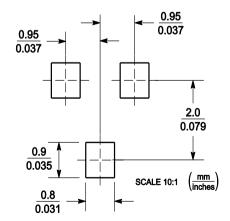
Notes:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.





6.SOLDERING FOOTPRINT





DISCLAIMER

- Curve guarantee in the specification. The curve of test items with electric parameter is used as quality guarantee. The curve of test items without electric parameter is used as reference only.
- Before you use our Products for new Project, you are requested to carefully read this document and fully understand its contents. LRC shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of any LRC's Products against warning, caution or note contained in this document.
- All information contained in this document is current as of the issuing date and subject to change without any prior notice. Before purchasing or using LRC's Products, please confirm the latest information with a LRC sales represe--ntative.

单击下面可查看定价,库存,交付和生命周期等信息

>>LRC(乐山无线电)