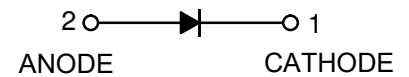
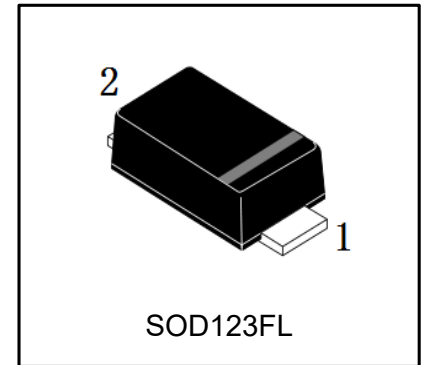


SODZ***A-SH Series

Surface Mount Zener Voltage Regulator Diodes
1.0 Watt Steady State

1. FEATURES

- 1.0 Watt SOD123FL zener voltage regulator diodes
- Low profile package for surface mounted applications in order to optimize board spaces
- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- High temperature soldering guaranteed: 260°C / 10 seconds at terminals
- We declare that the material of product is compliant with RoHS and Halogen free requirements.
- MSL: Level 1



2. MECHANICAL DATA

- Case: JEDEC SOD123FL / MINI SMA molded plastic over glass die
- Terminals: Plated terminals, solderable per MIL-STD-750, Method 2026
- Polarity: With color bands that denote cathode end
- Mounting Position: Any
- Weight: Approximately 0.0155 gram
- Quantity: 3000 Units / Tape&Reel

3. MAXIMUM RATINGS (T_A = 25°C unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

PARAMETERS	SYMBOLS	VALUE	UNITS
Steady State Power Dissipation at T _L = 75°C (Note1)	P _{M(AV)}	1.0	Watt
Z-current	I _Z	P _V /V _Z	mA
Operating Junction Temperature Range	T _J	-55 to +150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

Notes:

1. 8.0mm² (0.013mm thick) land areas.
2. 8.3ms single half sine-wave, duty cycle= 4 pulses per minutes maximum.

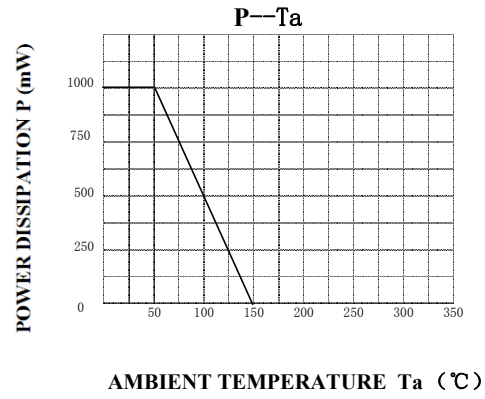
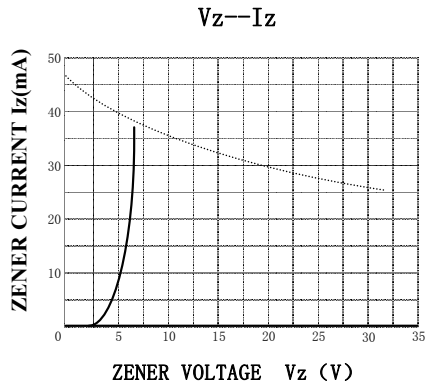
4. ELECTRICAL CHARACTERISTICS (TA = 25°C unless otherwise specified.)

Vz tolerance : ±5%; Tested with pulse tp = 40ms; Vfmax = 1.2V@IF = 200mA; P = 1 W

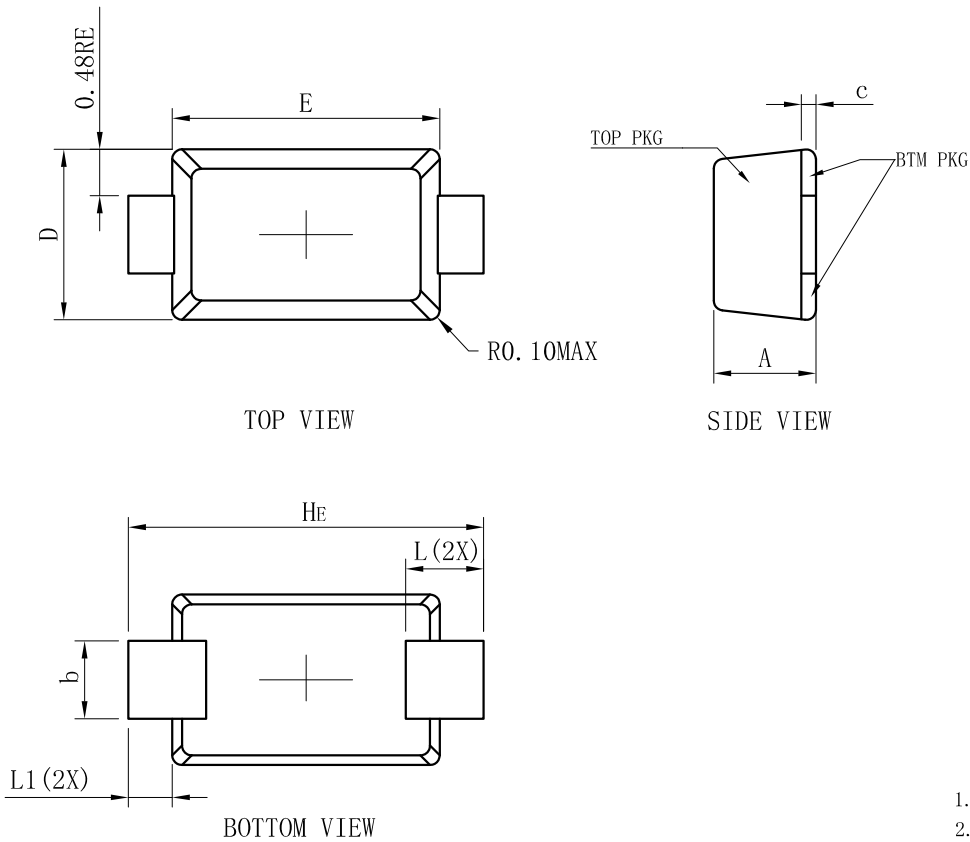
Type	Device Marking Code	Zener Voltage	Zener Current	Dynamic Impedance			Leakage Current		Maximum Regulator Current
		Vz @Izt	Izt	Zzt @Izt	Zzk @Izk	Izk	IR	VR	IzM @ TA=50°C
		Volts	mA	Ohms	Ohms	mA	uA Max	Volts	mA
SODZ2.4A-SH	Z2.4	2.4	5	95	500	1	600	1.0	178
SODZ2.7A-SH	Z2.7	2.7	5	95	500	1		1.0	164
SODZ3.0A-SH	Z3.0	3.0	5	95	500	1		1.0	146
SODZ3.3A-SH	Z3.3	3.3	5	95	500	1		1.0	133
SODZ3.6A-SH	Z3.6	3.6	5	95	500	1	100	1.0	121
SODZ3.9A-SH	Z3.9	3.9	5	95	500	1		1.0	110
SODZ4.3A-SH	Z4.3	4.3	5	95	500	1		1.0	100
SODZ4.7A-SH	Z4.7	4.7	5	95	500	1		1.0	100

5. ELECTRICAL CHARACTERISTIC CURVES

3.Characteristic Curves



6. PACKAGE DIMENSIONS

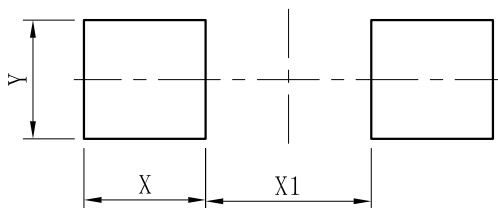


SOD123FL			
DIM	MIN	NOR	MAX
A	0.90	1.05	1.15
b	0.75	0.80	0.95
L	0.50	0.80	1.10
E	2.60	2.75	2.90
D	1.60	1.75	1.90
HE	3.50	3.65	3.80
c	0.12	0.17	0.22
L1	0.25	0.45	0.65
All Dimensions in mm			

GENERAL NOTES

1. Top package surface finish $Ra0.4 \pm 0.2\mu m$
2. Bottom package surface finish $Ra0.7 \pm 0.2\mu m$
3. Side package surface finish $Ra0.4 \pm 0.2\mu m$

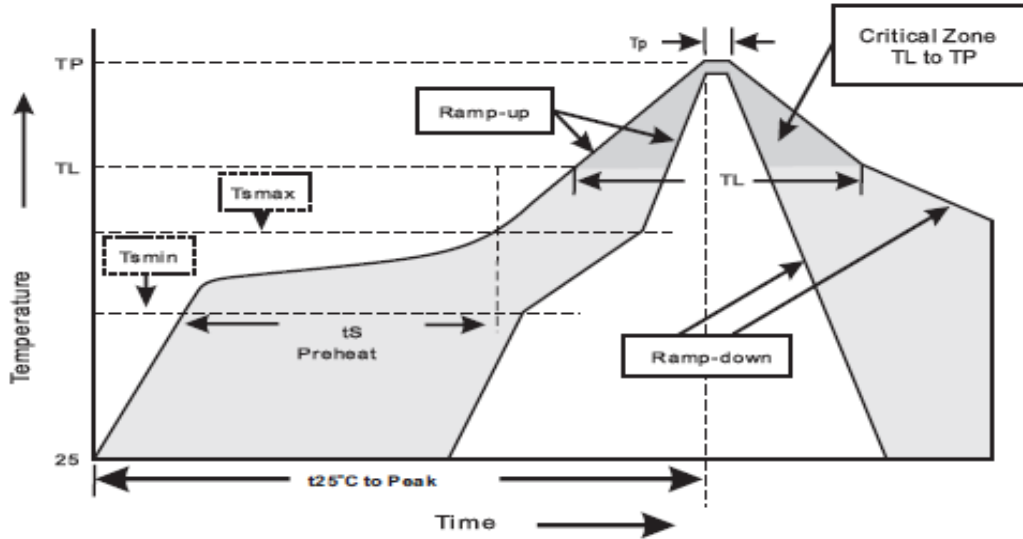
7. SOLDERING FOOTPRINT



DIM	(mm)
X	1.20
Y	1.10
X1	2.00

8. SUGGESTED THERMAL PROFILE FOR SOLDERING PROCESS

1. Storage environment : Temperature=5~40°C Humidity=55±25%
2. Reflow soldering of surface-mount device



3. Reflow soldering conditions

Profile Feature	Soldering Condition
Average ramp-up rate(T_L to T_P)	<3°C/sec
Preheat - Temperature Min(T_{smin}) - Temperature Max(T_{smax}) - Time(min to max)(t_s)	150°C 200°C 60~120sec
T_{smax} to T_L - Ramp-up Rate	<3sec
Time maintained above: - Temperature (T_L) - Time(t_L)	217°C 60-260sec
Peak Temperature(T_P)	255 -0/+5°C
Time within 5°C of actual Peak Temperature(T_P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

9. HIGH RELIABILITY TEST CAPABILITIES

Item Test	Condition	Reference
Solder Resistance	at 260±5°C for 10±2sec immerse body into solder 1/16" ± 1/32"	MIL-STD-750D METHOD-2031
Solderability	at 245±5°C for 5 sec	MIL-STD-202F METHOD-208
High Temperature Reverse Bias	V _R =80% rate at T _j =150°C for 168hrs	MIL-STD-750D METHOD-1038
Forward Operation Life	Rated average rectifier current T _A =25°C for 500hrs	MIL-STD-750D METHOD-1027
Intermittent Operation Life	T _A =25°C , I _F =I _O On state:power on for 5 min. Off state:power off for 5 min. on and off for 500 cycles	MIL-STD-750D METHOD-1036
Pressure Cooker	15P _{SIG} at T _A =121°C for 4hrs	JESD22-A102
Temperature Cycling	-55°C to +125°C dwelled for 30 min. and transferred for 5min. Total 10 cycles	MIL-STD-750D METHOD-1051
Thermal Shock	0°C for 5min. Rise to 100°C for 5min. Total 10 cycles	MIL-STD-750D METHOD-1056
Forward Surge	8.3ms single half sine-wave superimposed on rated load,one surge	MIL-STD-750D METHOD-4066-2
Humidity	at T _A =85°C , RH=85% for 1000hrs	MIL-STD-750D METHOD-1021
High Temperature Storage Life	at 175°C for 1000hrs	MIL-STD-750D METHOD-1031

DISCLAIMER

- Curve guarantee in the specification. The curve of test items with electric parameter is used as quality guarantee. The curve of test items without electric parameter is used as reference only.
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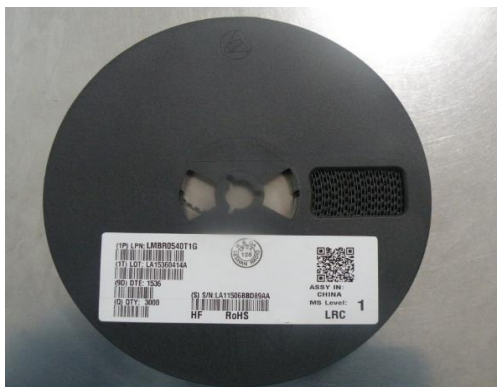
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8.1.2 Label position and QA stamp position.(Empty area) 标签张贴位置及QA印章位置。(印章盖 标签空白区)



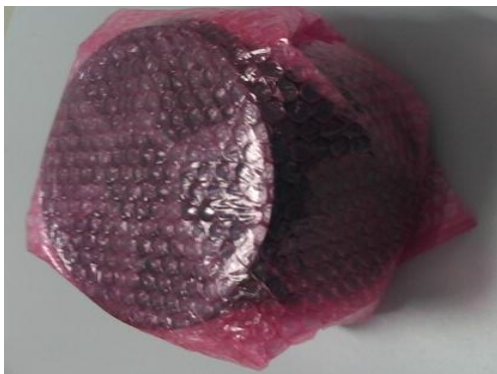
7英寸卷盘标签张贴及QA印章位置



13英寸卷盘标签张贴及QA印章位置

8.1.3 Ensure direction In the same reel. The same steel coil plate direction, With antistatic bubble to package reel. Refer to the below picture.

同一箱内的卷盘方向一致,用防静电泡沫对卷盘进行包裹。



7英寸卷盘防静电泡沫包裹



13英寸卷盘防静电泡沫包裹

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8.1.4 Put in the antistatic packing box after packaged reels. And QA stamp on the box label .

将包装好的卷盘放入防静电纸箱中，并 盒标签上盖章。



7 英寸卷盘内盒及标签



13 英寸卷盘内盒及标签

8.1.5 Product use printing inner box. 产品使用LRC印字内箱。



7英寸卷盘内箱印字（侧面）



13英寸卷盘内箱印字（正面）

8.1.6 Inner box packing quantity requirement. 内盒包装数量要求。

Product Description	QTY
SOD123-FL	1-10Reels
SOD323-HE	1-10Reels
SMA-FL	1-7Reels
SMB-FL	1-4Reels

8.1.7 With transparent tape sealing. 透明胶带封箱。

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7英寸内箱封盒



13英寸内箱封盒

8.1.8 Outer box size and packing quantity requirement, 外箱尺寸及包装数量要求。

Product Description	卷盘尺寸	Height (H)	Width (W)	Length (L)	Max. Qty
Power Device	7 英寸	410mm	400mm	445mm	12
Power Device	13 英寸	410mm	400mm	445mm	5



7 英寸卷盘产品装箱



13 英寸卷盘产品装箱

统一方向

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Title: Power Packages Marking & Taping Specification

功率封装字模和编带规范

Document Number: APS-QA-QS-010

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8.2 Standard Products Taping Specification

标准产品编带规范

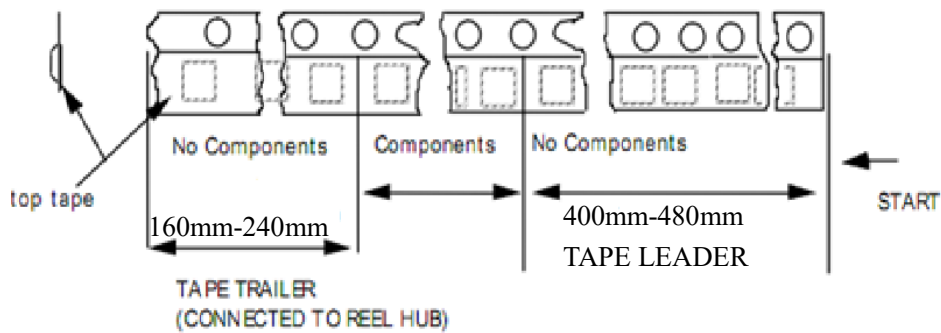
8.2.1 Tape length of no component

空带长度说明

Taping leader length 引导部分: $440\text{mm} \pm 40\text{mm}$, Tape trailer 尾部: $200\text{mm} \pm 40\text{mm}$

Figure 4

Tape Ends For Finished Goods Reel



8.2.2 Component packaging orientation: The cathode lead is close to the carrier tape's index hole.

产品放置方向: 印阴极带引脚邻近载带索引孔



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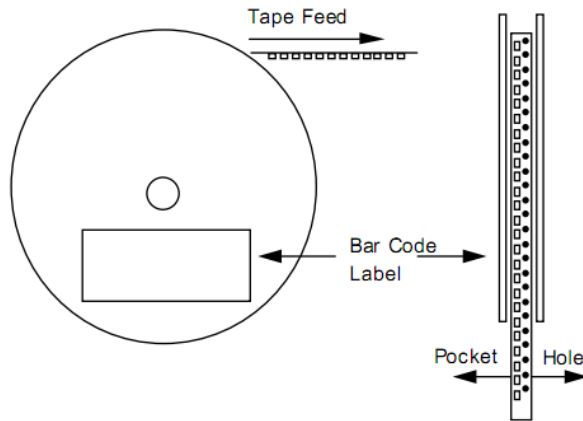
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8.2.3 Tape enwind orientation

编带缠绕方向要求



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