

LESD8LV12CT5G

Transient Voltage Suppressors

ESD Protection Diodes with Ultra-Low Capacitance

The LESD8LV is designed to protect voltage sensitive components that require ultra-low capacitance from ESD and transient voltage events. Excellent clamping capability, low capacitance, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its low capacitance, it is suited for use in high frequency designs such as USB 2.0 high speed and antenna line applications.

Specification Features:

- Ultra Low Capacitance 1.5 pF
- Low Clamping Voltage
- Stand-off Voltage: 10 V
- Low Leakage
- Response Time is Typically < 1.0 ns
- IEC61000-4-2 Level 4 ESD Protection
- This is a Pb-Free Device
- S- Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements;AEC-Q101 Qualified and PPAP Capable.

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic

Epoxy Meets UL 94 V-0

LEAD FINISH: 100% Matte Sn (Tin)

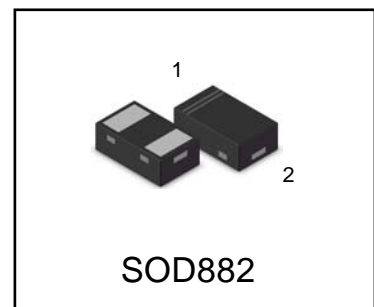
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Contact Air		±10 ±15	kV
Total Power Dissipation on FR-5 Board (Note 1) @ T _A = 25°C	P _D	200	mW
Storage Temperature Range	T _{stg}	-55 to +150	°C
Junction Temperature Range	T _J	-55 to +150	°C
Lead Solder Temperature - Maximum (10 Second Duration)	T _L	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-5 = 1.0 x 0.75 x 0.62 in.

LESD8LV12CT5G
S-LESD8LV12CT5G



ORDERING INFORMATION

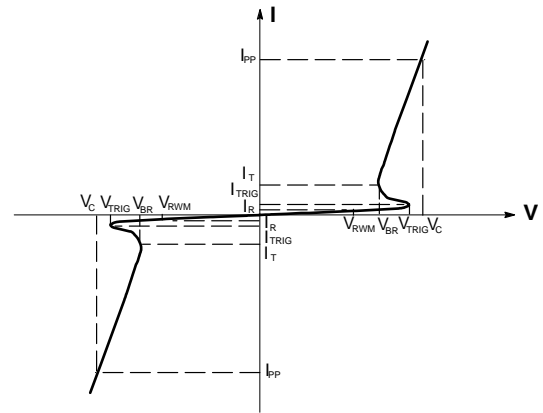
Device	Marking	Shipping
LESD8LV12CT5G	M1	10000/Tape&Reel

LESD8LV12CT5G,S-LESD8LV12CT5G

ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted)

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Reverse standoff voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
I _T	Test Current
V _{TRIG}	Reverse trigger voltage
I _{TRIG}	Reverse trigger current



Bi-Directional TVS

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Device	Device Marking	V _{RWM} (V)	I _R (μA) @ V _{RWM}	V _{BR} (V) @ I _T (Note 2)	I _T	C (pF)	V _C (V) @ I _{PP} = 1.65 A (Note 3)	V _C
		Max	Max	Min	mA	Max	Max	Per IEC61000-4-2 (Note 4)
LESD8LV12CT5G	M1	10	1.0	12	1.0	1.8	24.5	Figures 1 and 2 See Below

- V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C.
- Surge current waveform per Figure 4.
- For test procedure see Figures 3.

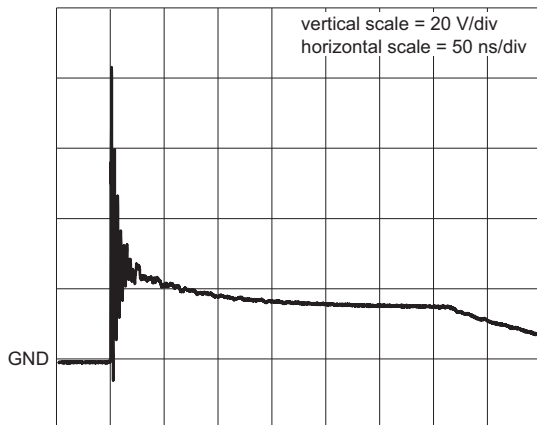


Figure 1. ESD Clamping Voltage Screenshot Positive 8 kV Contact per IEC61000-4-2

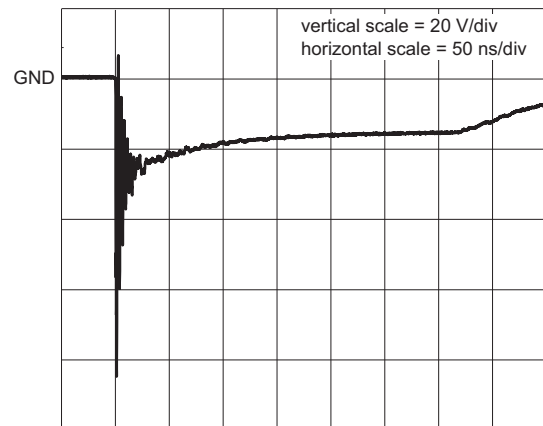


Figure 2. ESD Clamping Voltage Screenshot Negative 8 kV Contact per IEC61000-4-2

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IEC 61000-4-2 Spec.

Level	Test Voltage (kV)	First Peak Current (A)	Current at 30 ns (A)	Current at 60 ns (A)
1	2	7.5	4	2
2	4	15	8	4
3	6	22.5	12	6
4	8	30	16	8

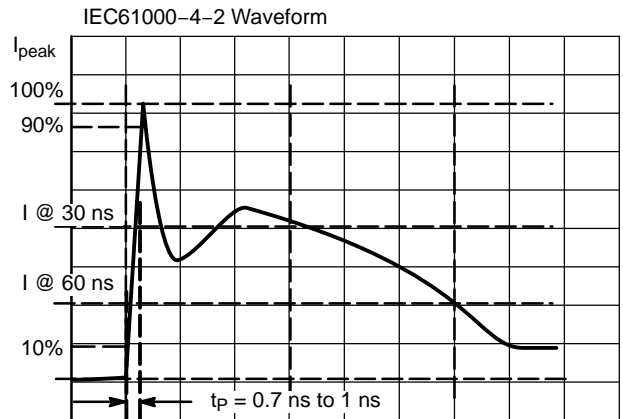


Figure 3. IEC61000-4-2 Spec

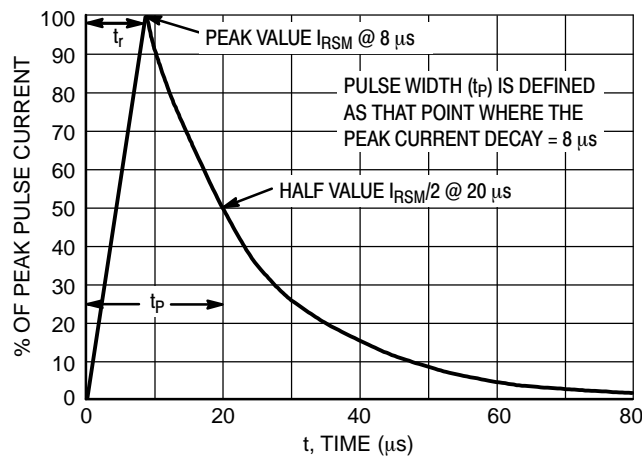
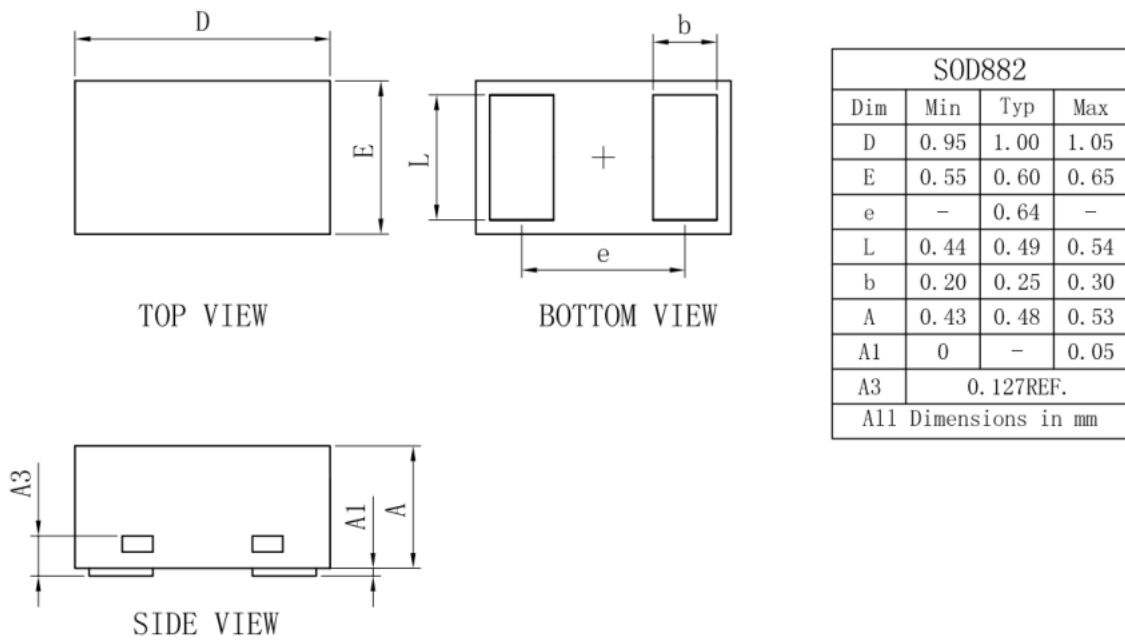


Figure 4. 8 X 20 μs Pulse Waveform

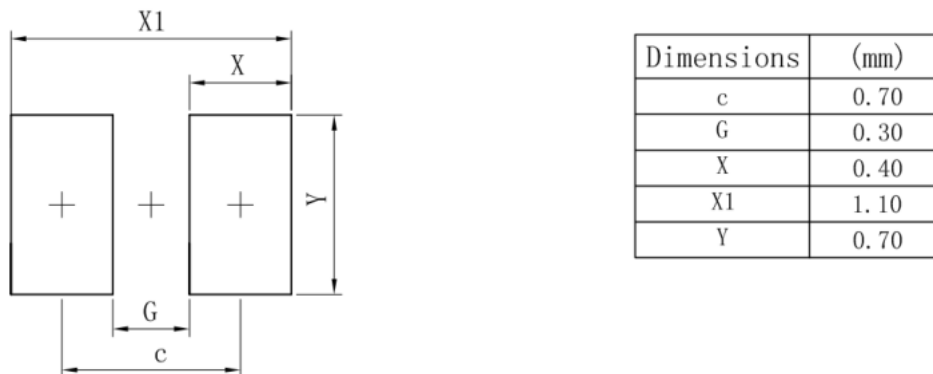
LESD8LV12CT5G,S-LESD8LV12CT5G

Package Outline Dimension



Suggested Pad layout

SOD882



单击下面可查看定价，库存，交付和生命周期等信息

[>>LRC\(乐山无线电\)](#)