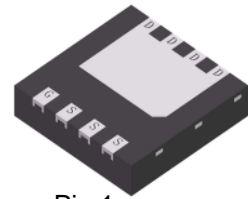


# S-LPB85260DT0AG

P-Channel 60-V Power MOSFET



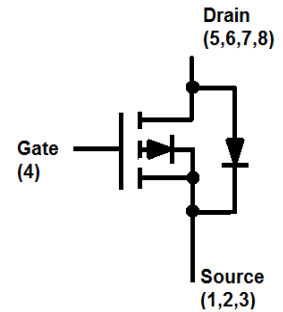
Pin 1  
DFN3333-8A

## 1. FEATURES

- Low thermal impedance.
- Fast switching speed.
- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- S- prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.

## 2. APPLICATIONS

- Power Routing
- DC/DC Conversion
- Motor Drives



## 3. DEVICE MARKING AND ORDERING INFORMATION

Device	Marking	Shipping
S-LPB85260DT0AG	S85	2000/Tape&Reel

## 4. MAXIMUM RATINGS(Ta = 25°C)

Parameter		Symbol	Limits	Unit
Drain-to-Source Voltage		VDS	-60	V
Gate-to-Source Voltage		VGS	± 20	V
Continuous Drain Current	TA =25°C	ID	-3	A
	TA =75°C		-2.6	
	TC =25°C		-7	
	TC =75°C		-6	
Pulsed Drain Current(Note 2)		IDM	-12	A
Avalanche Current		IAS	8	A
Avalanche energy(L=0.1mH)		EAS	3.2	mJ
Power Dissipation (Note 1)	TA =25°C	PD	2.5	W
	TC =25°C		20	
Operating Junction and Storage Temperature Range		TJ , TSTG	-55~+150	°C

## 5. THERMAL CHARACTERISTICS

Parameter	Symbol	Limits	Unit
Maximum Junction-to-Ambient(Note 1)	RθJA	70	°C/W
Maximum Junction-to-Ambient(Note 3)	RθJA	190	
Maximum Junction-to-Case	RθJC	10	

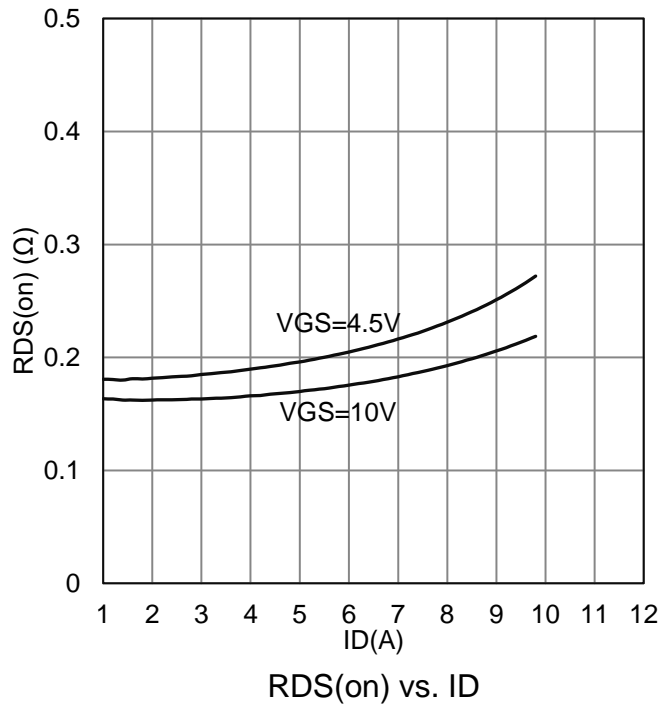
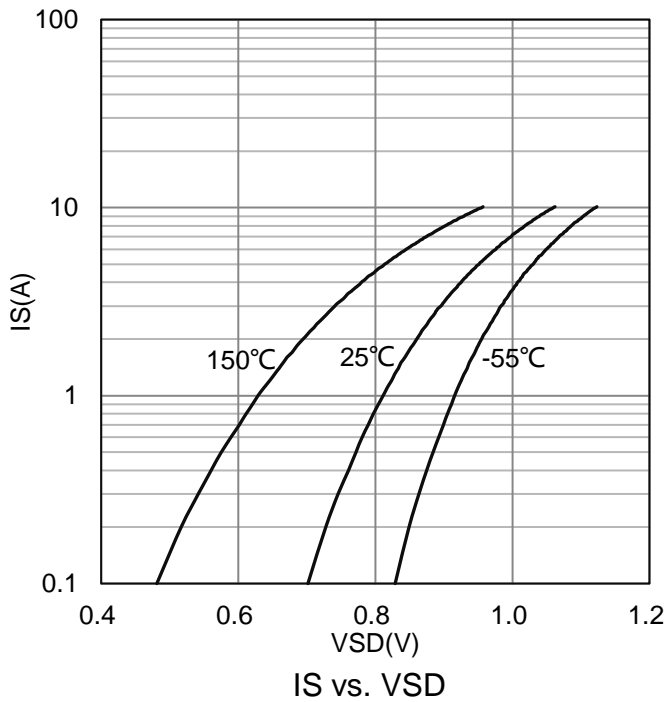
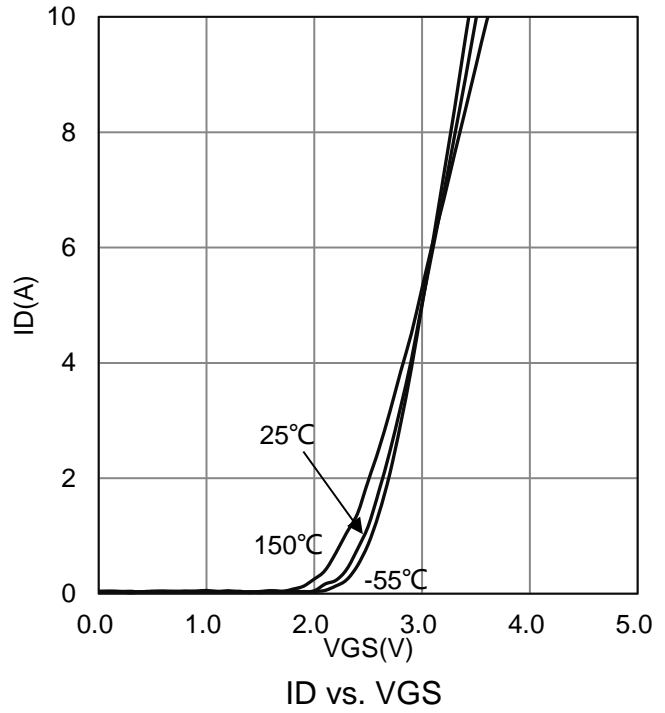
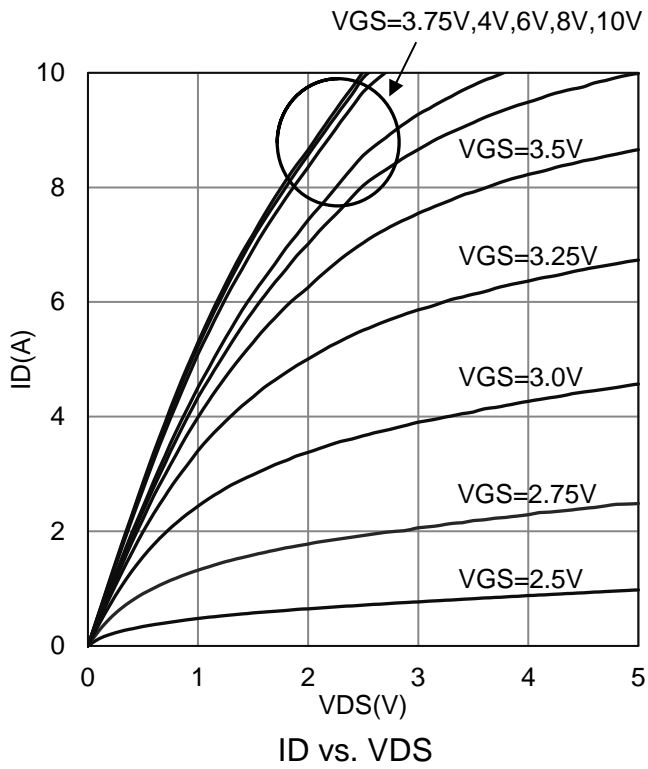
- 1.Surface mounted on "1.5 x 1.5" FR4 board using 1 sq in pad, 2 oz Cu.
- 2.Pulse width limited by maximum junction temperature.
- 3.Surface mounted on FR4 board using the minimum recommended pad size.

**6. ELECTRICAL CHARACTERISTICS (Ta= 25°C)**

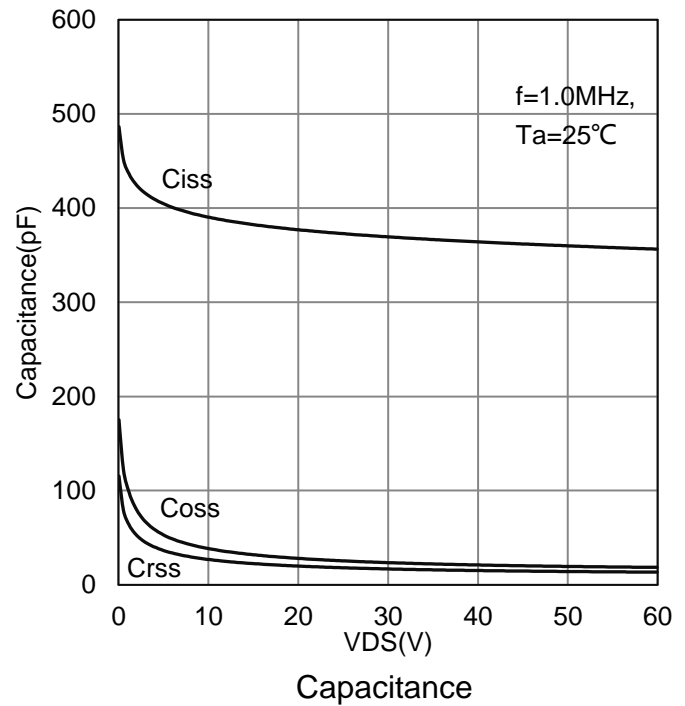
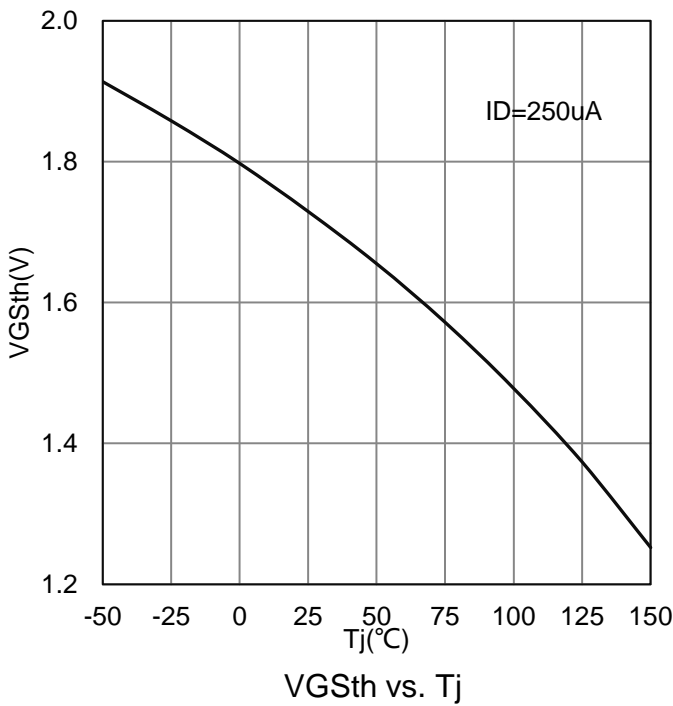
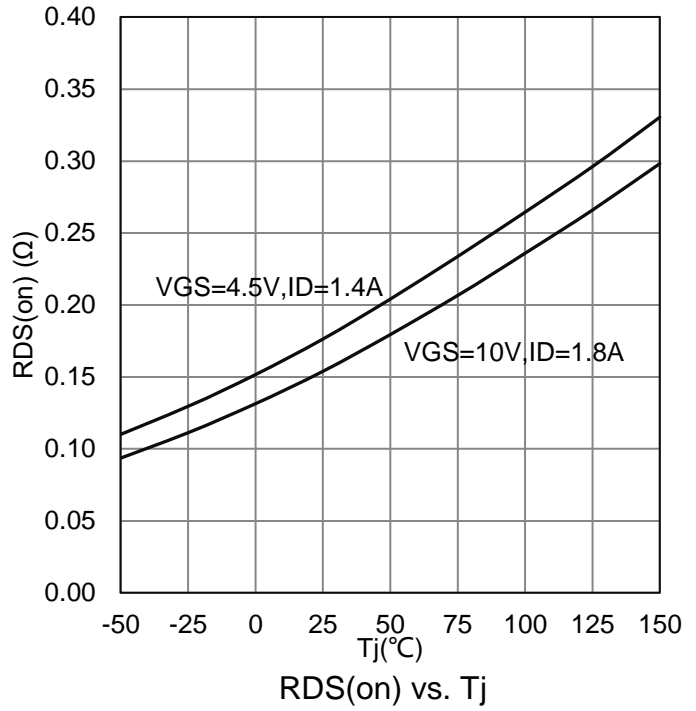
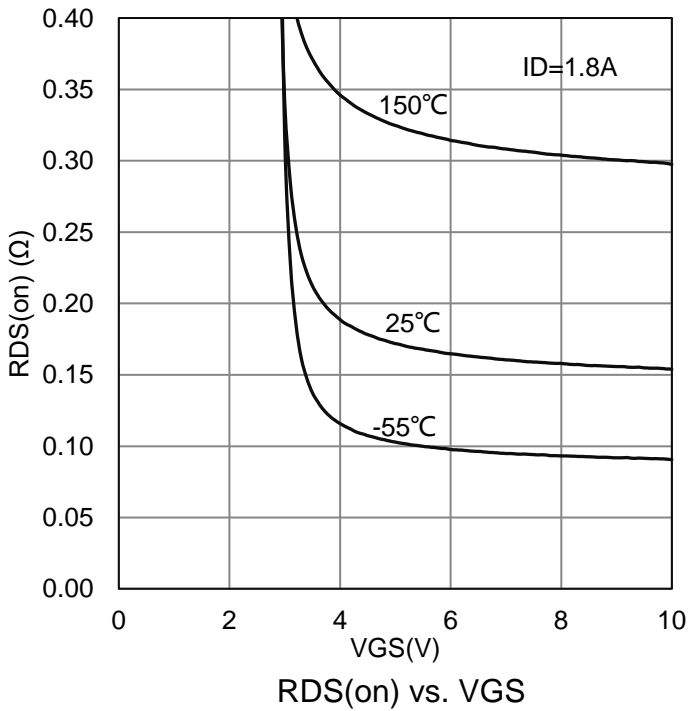
Characteristic	Symbol	Min.	Typ.	Max.	Unit
<b>Static</b>					
Drain-Source Breakdown Voltage (VGS = 0 V, ID = -250 uA)	V(BR)DSS	-60	-	-	V
Gate-Source Threshold Voltage (VDS = VGS, ID = -250 uA)	VGS(th)	1	2	3	V
Gate-Body Leakage (VDS = 0 V, VGS = ±20 V)	IGSS	-	-	± 100	nA
Zero Gate Voltage Drain Current (VDS = -60 V, VGS = 0 V)	IDSS	-	-	-10	μA
Drain-Source On-Resistance(Note 4) (VGS = -10 V, ID = -1.8 A) (VGS = -4.5 V, ID = -1.4 A)	RDS(on)	-	170 190	215 260	mΩ
Diode Forward Voltage (IS = -1.2 A, VGS = 0 V)	VSD	-	-	-1.2	V
<b>Dynamic</b>					
Total Gate Charge	(VDS = -48 V, VGS = -4.5 V, ID = -1 A)	Qg	-	4.2	nC
Gate-Source Charge		Qgs	-	0.7	
Gate-Drain Charge		Qgd	-	2.4	
Input Capacitance	(VDS = -30 V, VGS = 0 V, f = 1 MHz)	Ciss	-	370	pF
Output Capacitance		Coss	-	24	
Reverse Transfer Capacitance		Crss	-	17	
Turn-On Delay Time	(VDS = -30 V, RL= 30Ω, ID = -1 A, VGS = -10 V, RG = 3.1Ω)	td(on)	-	4.4	ns
Rise Time		tr	-	3	
Turn-Off Delay Time		td(off)	-	20.5	
Fall Time		tf	-	4.2	

4.Pulse test: PW ≤ 300μs duty cycle ≤ 2%.

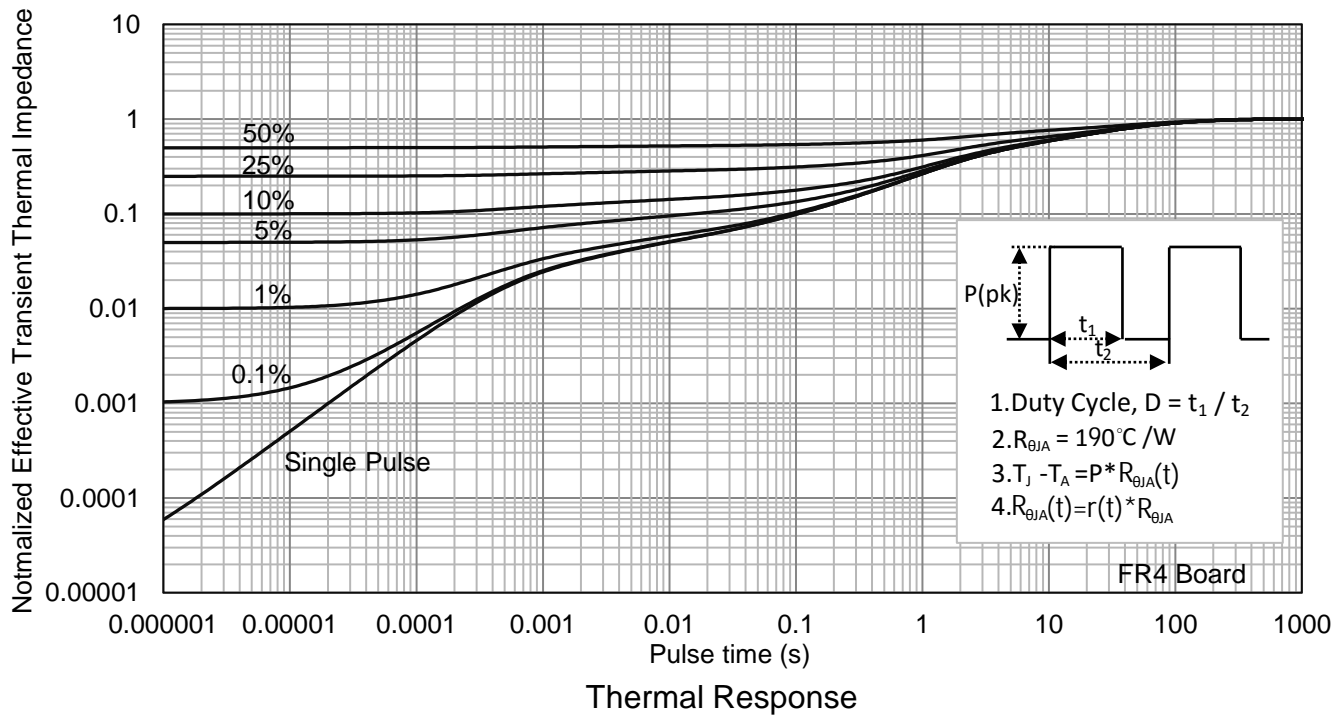
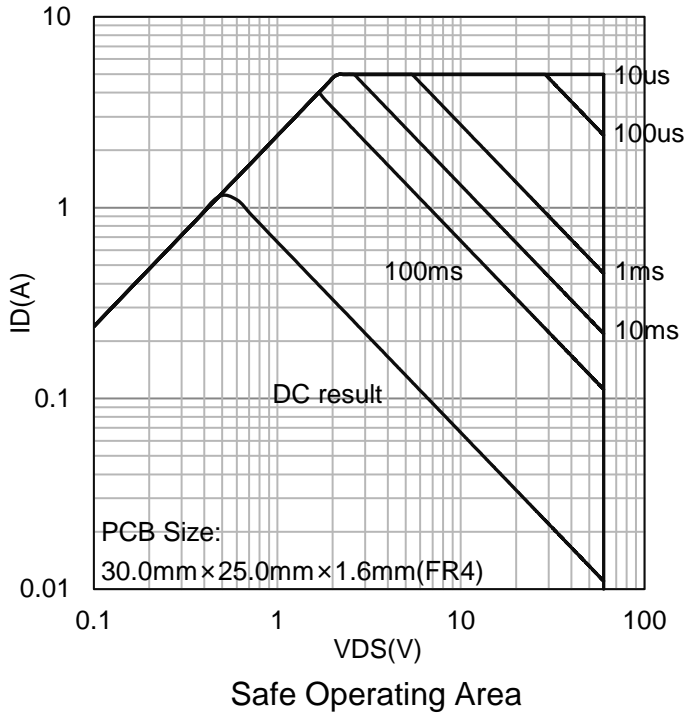
**7. ELECTRICAL CHARACTERISTICS CURVES**



**7. ELECTRICAL CHARACTERISTICS CURVES(Con.)**

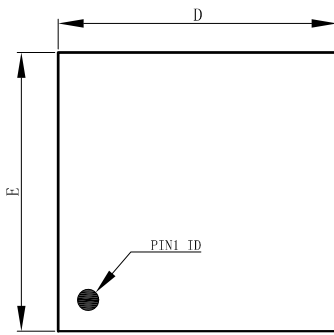


7. ELECTRICAL CHARACTERISTICS CURVES(Con.)

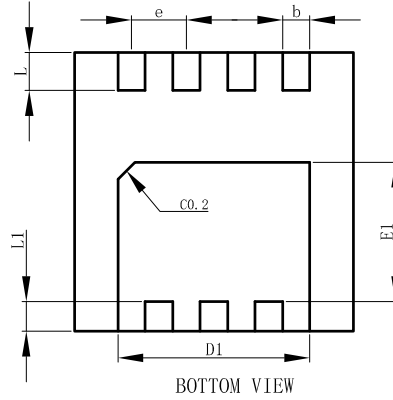


**8.OUTLINE AND DIMENSIONS**

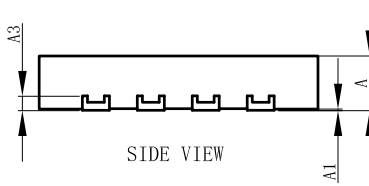
DFN3333-8A



TOP VIEW



BOTTOM VIEW

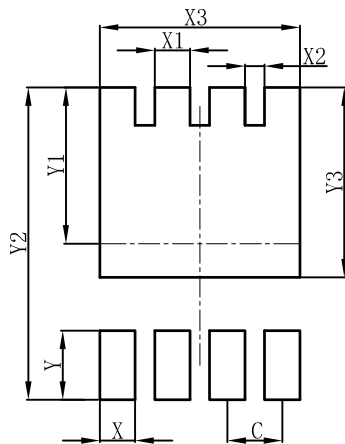


SIDE VIEW

DFN3333-8A			
DIM	MIN	NOR	MAX
A	0.60	0.65	0.70
A1	0.00	0.03	0.05
b	0.27	0.32	0.37
D	3.25	3.30	3.35
E	3.25	3.30	3.35
D1	2.22	2.27	2.32
E1	1.60	1.65	1.70
e	0.65BSC		
L	0.40	0.45	0.50
L1	0.30	0.35	0.40
A3	0.152REF.		
All Dimensions in mm			

**9.SOLDERING FOOTPRINT**

DFN3333-8A



DFN3333-8A	
DIM	(mm)
C	0.65
X	0.42
X1	0.42
X2	0.23
X3	2.37
Y	0.70
Y1	1.85
Y2	3.70
Y3	2.25

## **DISCLAIMER**

- Curve guarantee in the specification. The curve of test items with electric parameter is used as quality guarantee. The curve of test items without electric parameter is used as reference only.
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