Mag Layers USA, INC

Specification Sheet

P/N : MCM-7060M-Series-RU

Products:

Molded Power Chokes

Multilayer Chip Inductors

Lan Transformer

RF Passive / Antennas

Automotive

Certifications:

ISO9001

IATF16949

ISO14001

QC080000

US Office

5406 Bolsa Ave.,
Huntington Beach, CA 92649
(714) 898-8377

Contact Us

www.maglayersusa.com
info@maglayersusa.com

Ⅰ. SCOPE：

This specification applies to the Pb Free high current type SMD Common mode filter for MCM-7060M-SERIES

PRODUCT INDENTIFICATION

MCM - 7060M - 301-RU

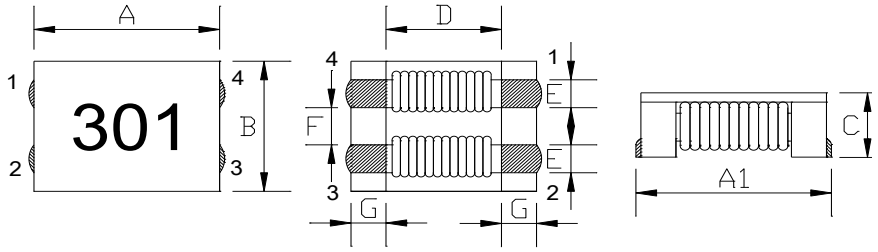
① ② ③

① Product Code

② Dimensions Code

③ Impedance Code

(1) SHAPES AND DIMENSIONS



A:	**7.0±0.5**	**mm**
A1:	**7.5±0.5**	**mm**
B:	**6.0±0.5**	**mm**
C:	**3.8Max.**	**mm**
D:	**3.5Typ.**	**mm**
E:	**1.5±0.2**	**mm**
F:	**1.5±0.2**	**mm**
G:	**1.75±0.2**	**mm**

(2) ELECTRICAL SPECIFICATIONS

SEE TABLE 1

TEST INSTRUMENTS

Z : HP 4291B IMPEDANCE ANALYZER (or equivalent)

RDC : CHROMA MODEL 16502 MILLIOHMMETER (or equivalent)

(3) CHARACTERISTICS

(3)-1 Operate temperature range ..... －40℃～＋125℃

(Including self temp. rise)

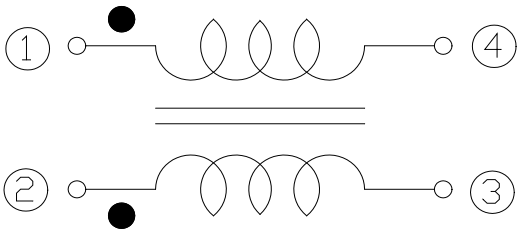
(3)-2 Storage temperature range ..... －40℃～＋125℃

TABLE 1

MAGLAYERS PT/NO.	Impedance(Ω) at 100MHz		Resistance RDC(Ω) Max.(1 line)	Rated Current (A) Max.	Insulation Resistance (MΩ) Min.	Rated Voltage (V)Max.
	Min.	Typ.				
MCM-7060M-400-RU	40	70	5m	15	10	125
MCM-7060M-101-RU	100	140	10m	9.0	10	125
MCM-7060M-301-RU	225	300	10m	5.0	10	125
MCM-7060M-501-RU	275	350	10m	5.0	10	125
MCM-7060M-601-RU	500	700	15m	4.0	10	125
MCM-7060M-701-RU	500	700	15m	4.0	10	125
MCM-7060M-102-RU	800	1020	17m	3.0	10	125
MCM-7060M-132-RU	910	1300	21m	2.5	10	125
MCM-7060M-272-RU	2000	2700	63m	1.0	10	125
MCM-7060M-302-RU	2500	3000	75m	0.9	10	125

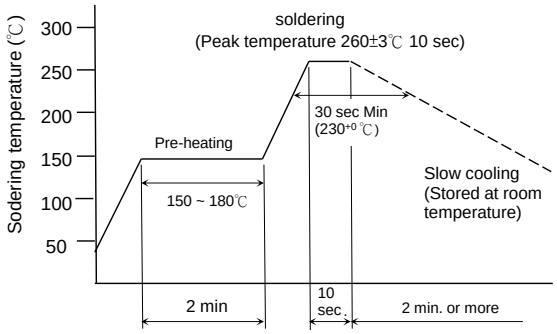
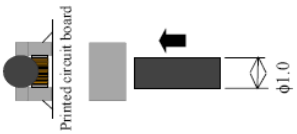
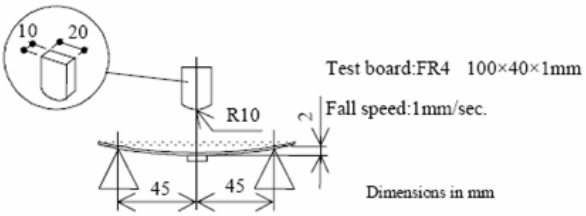
Rated Current ： Based on temperature rise (△T： 40℃ TYP.)

CIRCUIT DIAGRAM



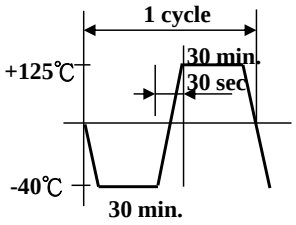
(4) RELIABILITY TEST METHOD

MECHANICAL

TEST ITEM	SPECIFICATION	TEST DETAILS
Solder ability	The product shall be connected to the test circuit board by the fillet (the height is 0.2mm).	Apply cream solder to the printed circuit board . Refer to clause 8 for Reflow profile.
Resistance to Soldering heat (reflow soldering)	There shall be no damage or problems.	Temperature profile of reflow soldering  The specimen shall be passed through the reflow oven with the condition shown in the above profile for 1 time. The specimen shall be stored at standard atmospheric eric conditions for 1 hour, after which the measurement shall be made.
Terminal strength	The terminal electrode and the ferrite must not damaged.	Solder a chip to test substrate , and then laterally apply a load 9.8N in the arrow direction. 
Strength on PC board bending	The terminal electrode and the ferrite must not damaged.	Solder a chip to test substrate and then apply a load. 
High temperature resistance	Impedance:Within±20% of the initial value. Insulation resistance and DC resistance on the specification(refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not damaged.	After the samples shall be soldered onto the test circuit board,the test shall be done. Measurement : After placing for 24 hours min. Temperature : +125±2℃ Applied voltage : Rated voltage Applied current : Rated current Testing time : 500±12 hours

(4) RELIABILITY TEST METHOD

MECHANICAL

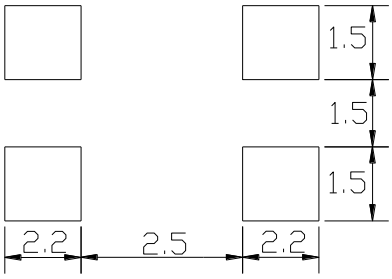
TEST ITEM	SPECIFICATION	TEST DETAILS
Humidity resistance	Impedance:Within±20% of the initial value. Insulation resistance and DC resistance on the specification(refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not damaged.	After the samples shall be soldered onto the test circuit board,the test shall be done. Measurement : After placing for 24 hours min. Temperature : +60±2℃ , Humidity : 90 to 95 %RH Applied voltage : Rated voltage Applied current : Rated current Testing time : 500±12 hours
Thermal shock	Impedance:Within±20% of the initial value. Insulation resistance and DC resistance on the specification(refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not damaged.	
Low temperature storage	Impedance:Within±20% of the initial value. Insulation resistance and DC resistance on the specification(refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not damaged.	After the samples shall be soldered onto the test circuit board,the test shall be done. Measurement : After placing for 24 hours min. Temperature : -40±2℃ Testing time : 500±12 hours
Vibration	Impedance:Within±20% of the initial value. Insulation resistance and DC resistance on the specification(refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not damaged.	After the samples shall be soldered onto the test circuit board,the test shall be done. Frequency : 10 to 55 Hz Amplitude : 1.52 mm Dimension and times : X ,Y and Z directions for 2 hours each.
Solderability	New solder More than 75%	Flux (rosin, isopropyl alcohol{JIS-K-1522}) shall be coated over the whole of the sample before hard, the sample shall then be preheated for about 2 minutes in a temperature of 130～150℃ and after it has been immersed to a depth 0.5mm below for 3±0.2 seconds fully in molten solder M705 with a temperature of 245±2℃. More than 75% of the electrode sections shall be couered with new solder smoothly when the sample is taken out of the solder bath.

(5) LAND DIMENSION (Ref.)

PCB: GLASS EPOXY　t＝1.6mm

(5)-1 LAND PATTERN DIMENSIONS

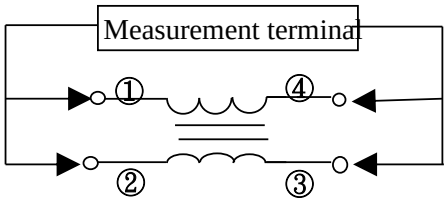
(STANDARD PATTERN)　Unit:mm



(6) TEST EQUIPMENT

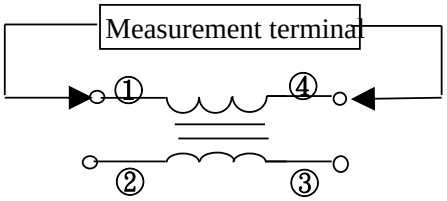
(6)-1 Impedance

Measured by using HP4291B RF Impedance Analyzer.



(6)-2 DC Resistance

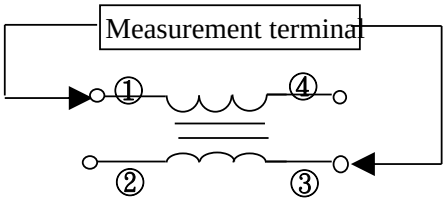
Measured by using Chroma 16502 milliohm meter.



(6)-3 Insulation Resistance

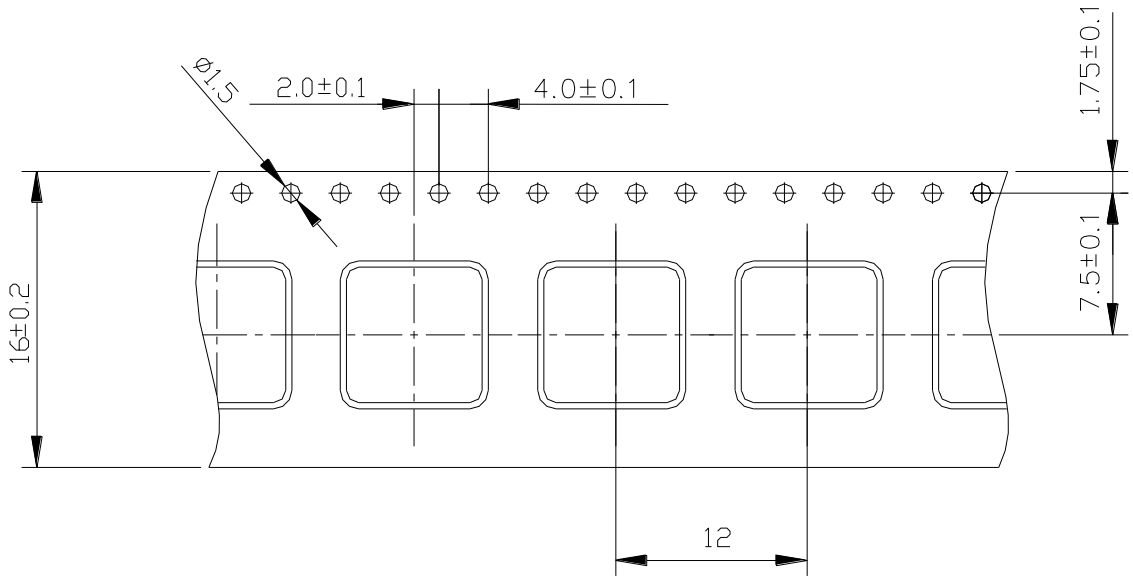
Measured by using Chroma 19073

Measurement voltage : 50v ,Measurement time : 60 sec.

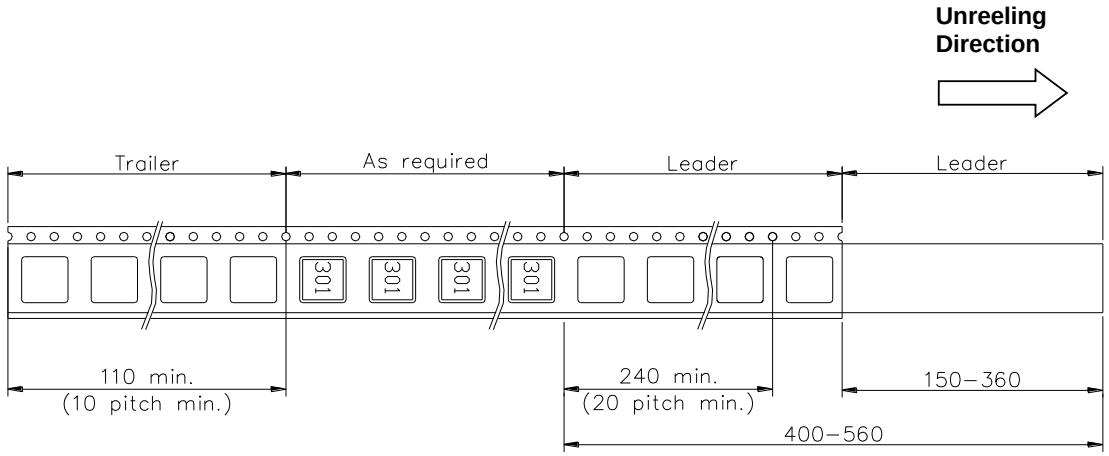


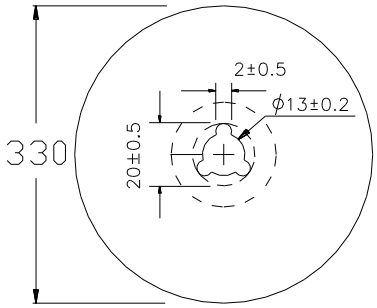
(6) PACKAGING

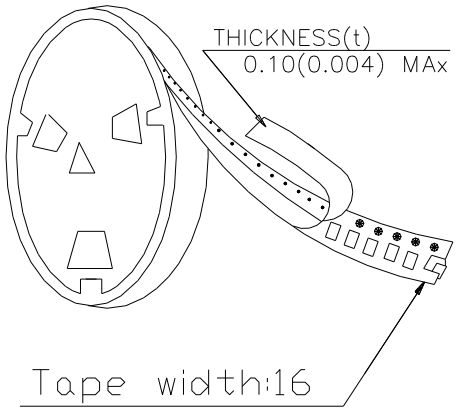
(6)-1 CARRIER TAPE DIMENSIONS (mm)



(6)-2 TAPING DIMENSIONS (mm)







(6)-4 QUANTITY

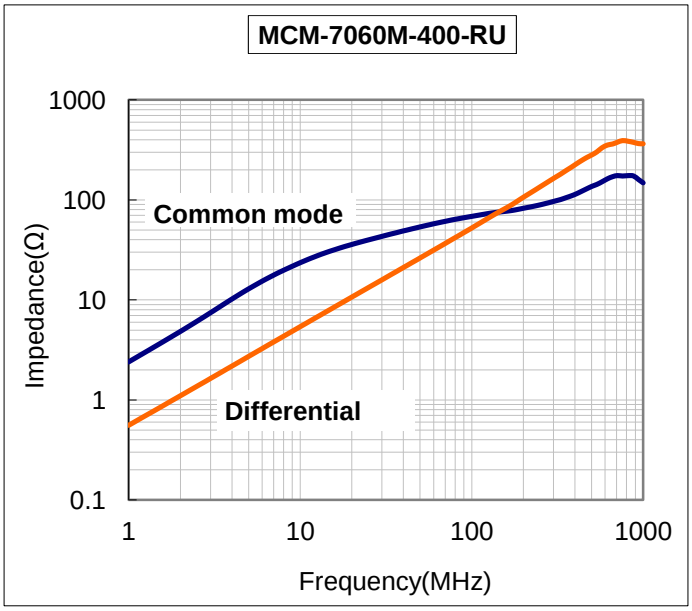
1500 pcs/Reel

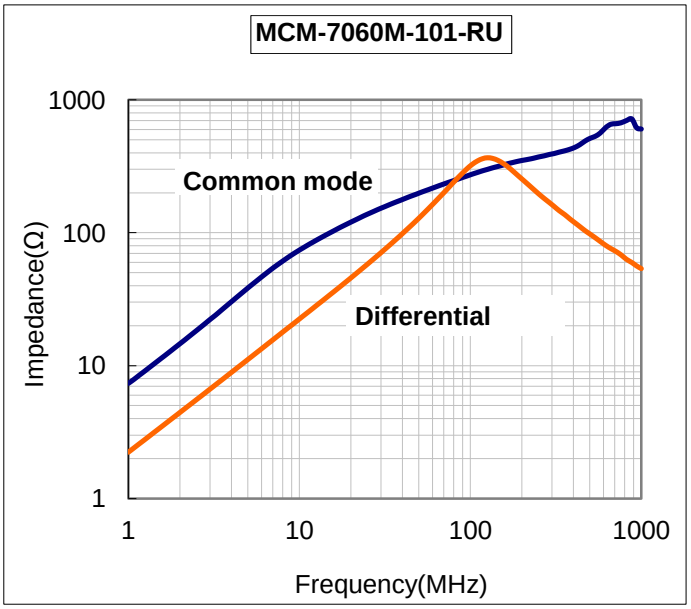
The products are packaged so that no damage will be sustained.

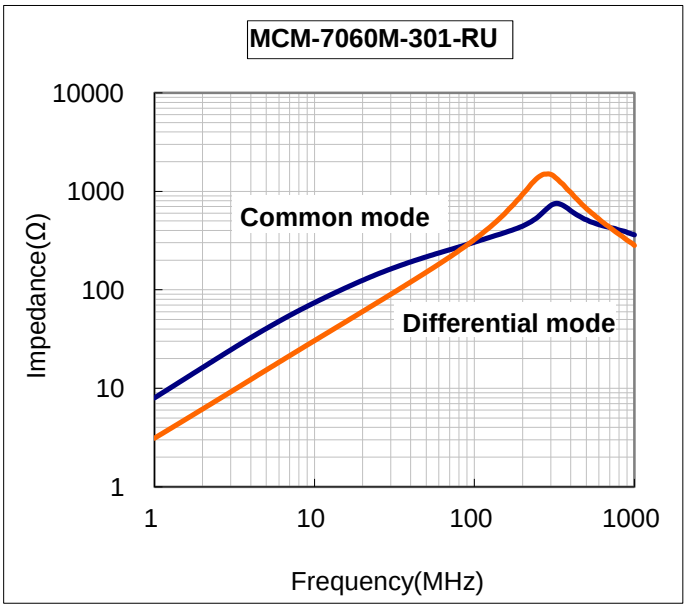
Please note that the contents may change without any prior notice due to reasons such as upgrading.

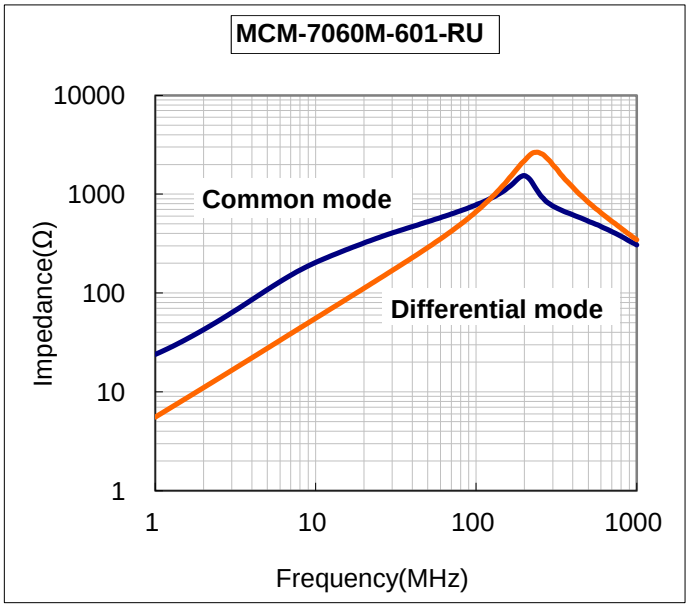
TYPICAL ELECTRICAL CHARACTERISTICS

Impedance VS. Frequency



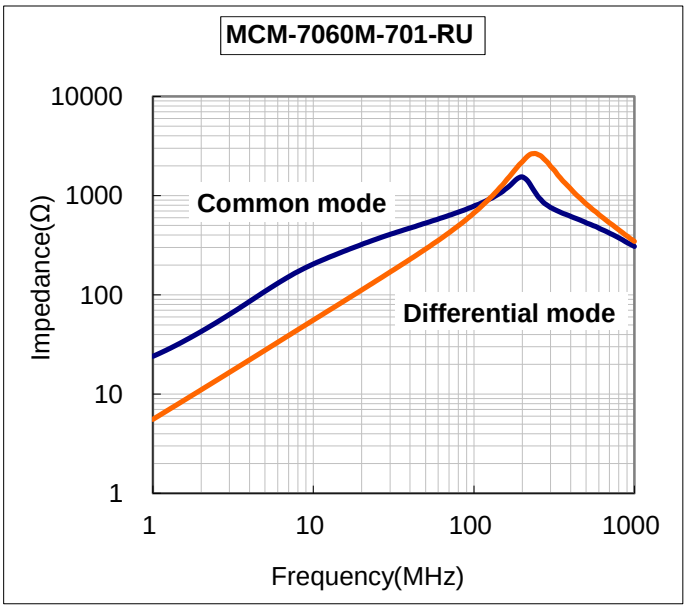


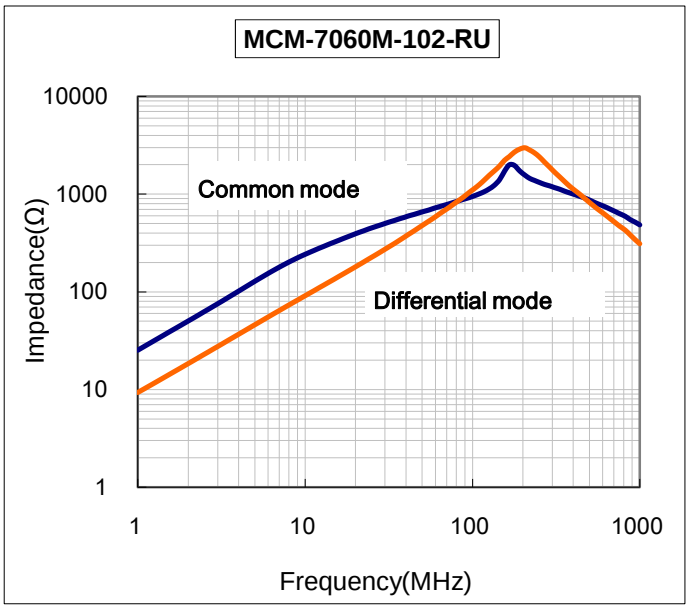


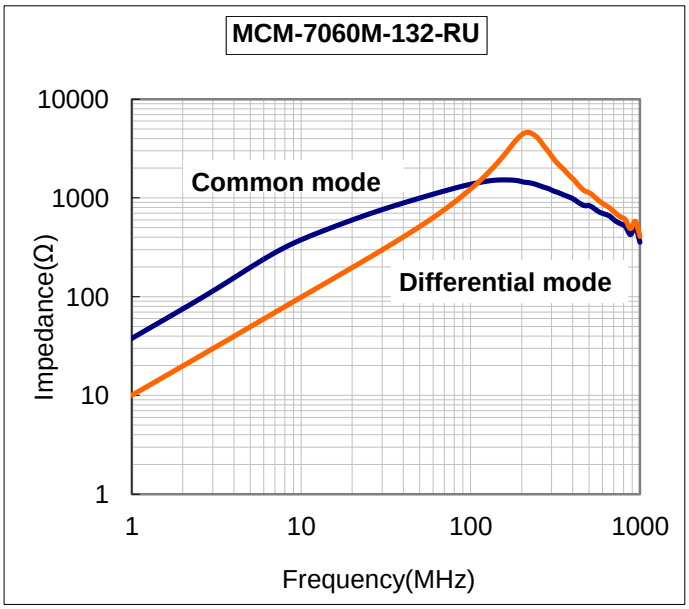


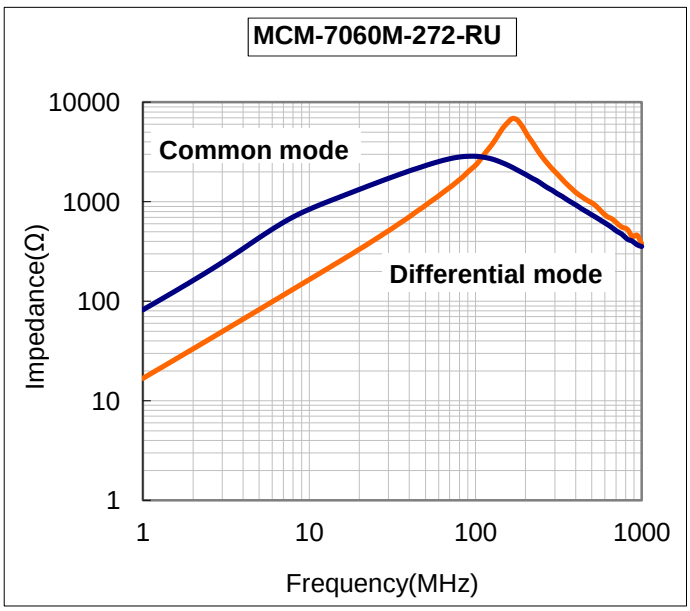
TYPICAL ELECTRICAL CHARACTERISTICS

Impedance VS. Frequency



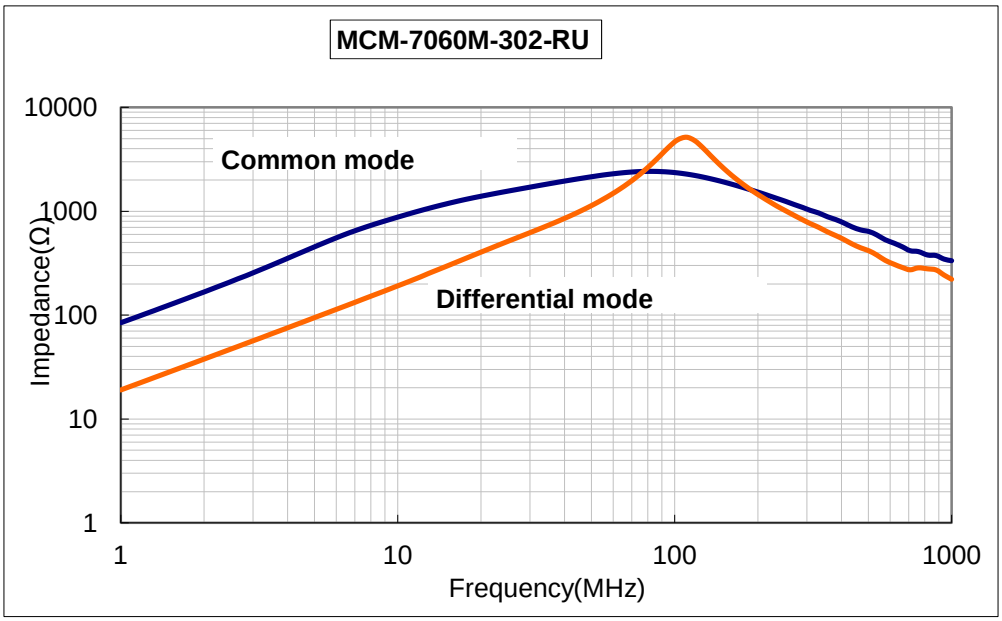






TYPICAL ELECTRICAL CHARACTERISTICS

Impedance VS. Frequency



单击下面可查看定价，库存，交付和生命周期等信息

>>MAG.LAYERS