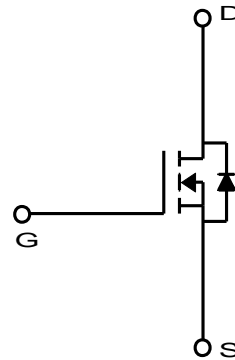
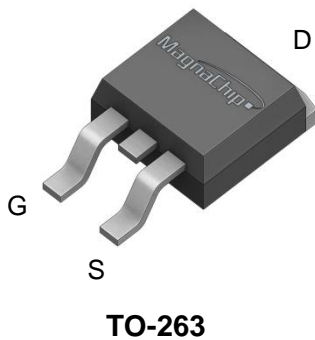


General Description

The MDE1932 uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDE1932 is suitable device for Synchronous Rectification For Server and general purpose applications.

Features

- $V_{DS} = 80V$
- $I_D = 120A @ V_{GS} = 10V$
- $R_{DS(ON)} < 3.4 m\Omega @ V_{GS} = 10V$
- 100% UIL Tested
- 100% Rg Tested



Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	80	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current ⁽¹⁾	$T_C=25^\circ C$ (Silicon Limited)	I_D	175	A
	$T_C=25^\circ C$ (Package Limited)		120	
	$T_C=100^\circ C$		110	
Pulsed Drain Current		I_{DM}	480	
Power Dissipation	$T_C=25^\circ C$	P_D	209	W
	$T_C=100^\circ C$		84	
Single Pulse Avalanche Energy ⁽²⁾		E_{AS}	312.5	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾	$R_{\theta JA}$	62.5	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.6	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDE1932RH	-55~150°C	TO-263	Reel	Halogen Free

Electrical Characteristics (T_J = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = 250μA, V _{GS} = 0V	80	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.0	-	4.0	
Drain Cut-Off Current	I _{DSS}	V _{DS} = 64V, V _{GS} = 0V	-	-	1.0	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	-	-	±0.1	
Drain-Source ON Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 50A	-	3.0	3.4	mΩ
Forward Transconductance	g _{fs}	V _{DS} = 10V, I _D = 50A	-	90	-	S
Dynamic Characteristics						
Total Gate Charge	Q _g	V _{DS} = 40V, I _D = 50A, V _{GS} = 10V	-	116	-	nC
Gate-Source Charge	Q _{gs}		-	27	-	
Gate-Drain Charge	Q _{gd}		-	33	-	
Input Capacitance	C _{iss}	V _{DS} = 40V, V _{GS} = 0V, f = 1.0MHz	-	7,200	-	pF
Reverse Transfer Capacitance	C _{rss}		-	50	-	
Output Capacitance	C _{oss}		-	1,540	-	
Turn-On Delay Time	t _{d(on)}	V _{GS} = 10V, V _{DS} = 40V, I _D = 50A, R _G = 3.0Ω	-	31	-	ns
Rise Time	t _r		-	27	-	
Turn-Off Delay Time	t _{d(off)}		-	91	-	
Fall Time	t _f		-	51	-	
Gate Resistance	R _g	f=1 MHz	-	3.0	-	Ω
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V _{SD}	I _S = 50A, V _{GS} = 0V	-	0.9	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 50A, di/dt = 100A/μs	-	73	-	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	161	-	nC

Note :

- Surface mounted FR-4 board by JEDEC (jesd51-7). Continuous current at T_c=25°C is silicon limited
- E_{AS} is tested at starting T_j = 25°C, L = 1.0mH, I_{AS} = 25.0A, V_{GS} = 10V.

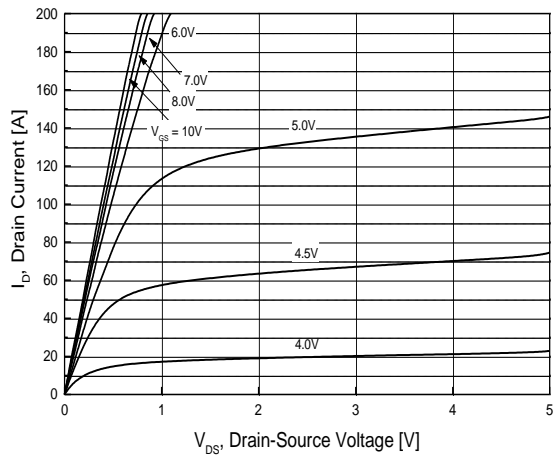


Fig.1 On-Region Characteristics

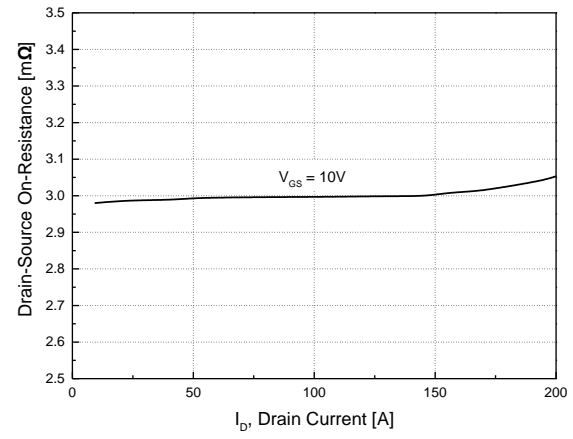


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

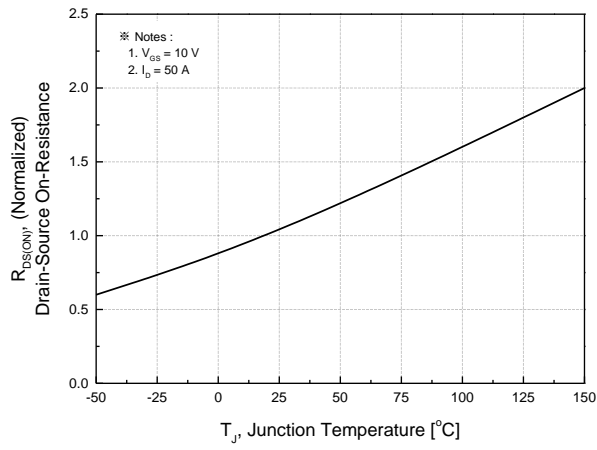


Fig.3 On-Resistance Variation with Temperature

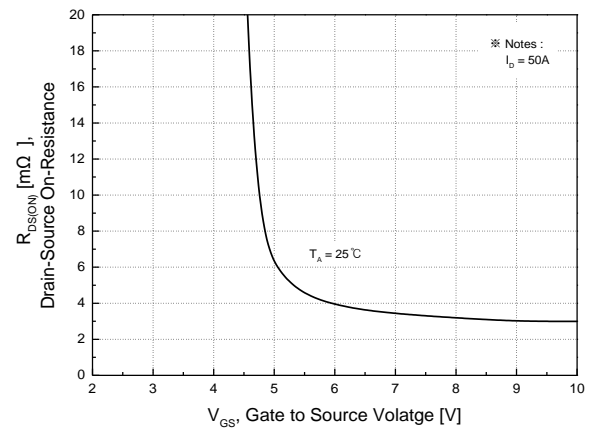


Fig.4 On-Resistance Variation with Gate to Source Voltage

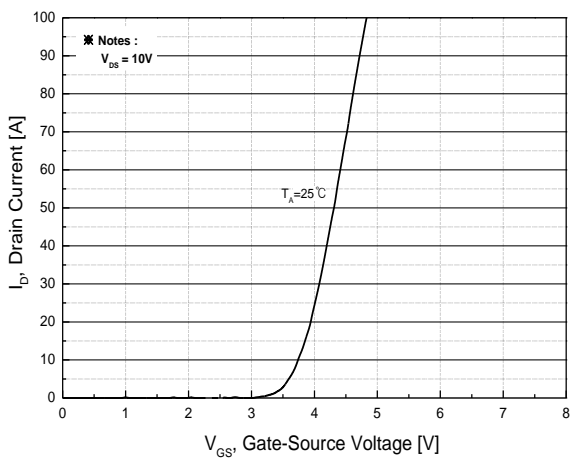


Fig.5 Transfer Characteristics

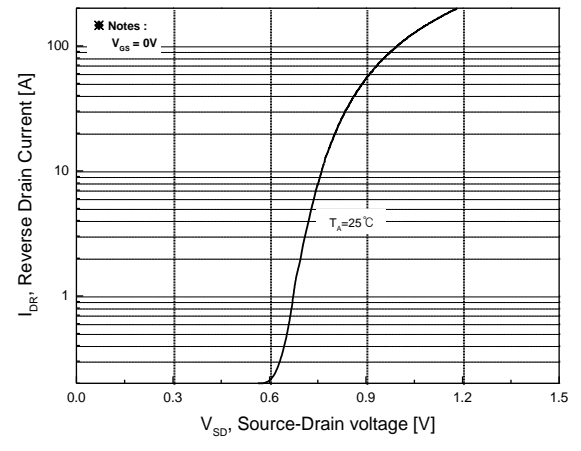


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

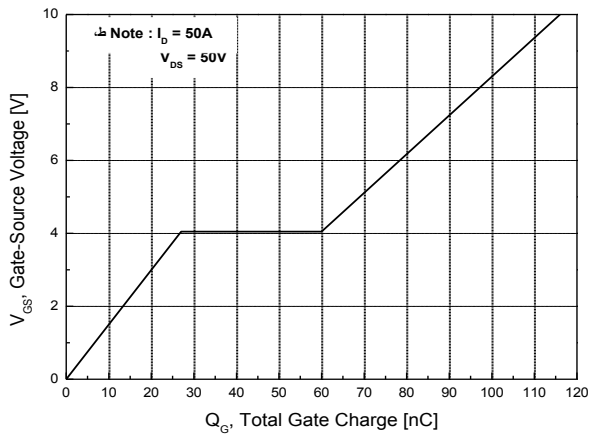


Fig.7 Gate Charge Characteristics

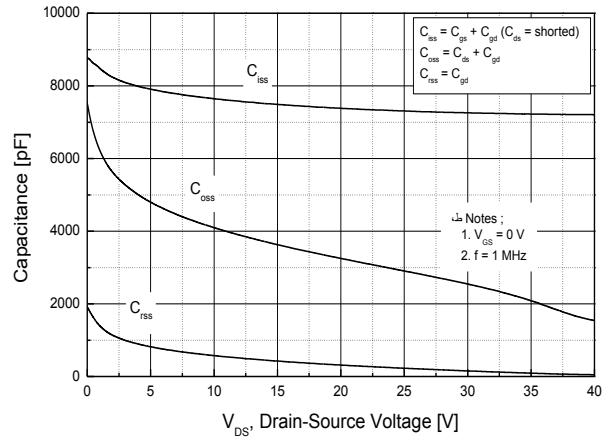


Fig.8 Capacitance Characteristics

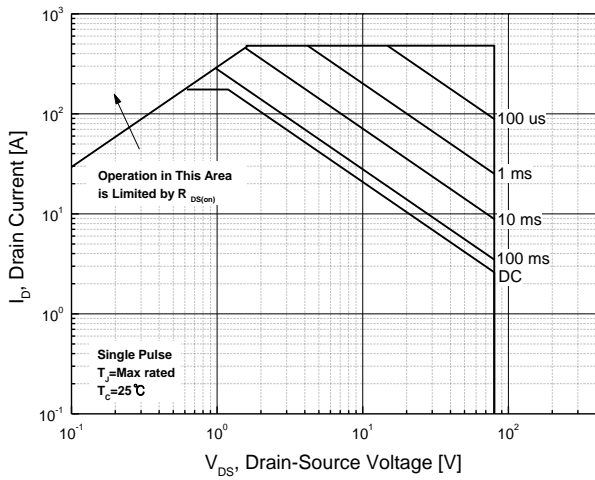


Fig.9 Maximum Safe Operating Area

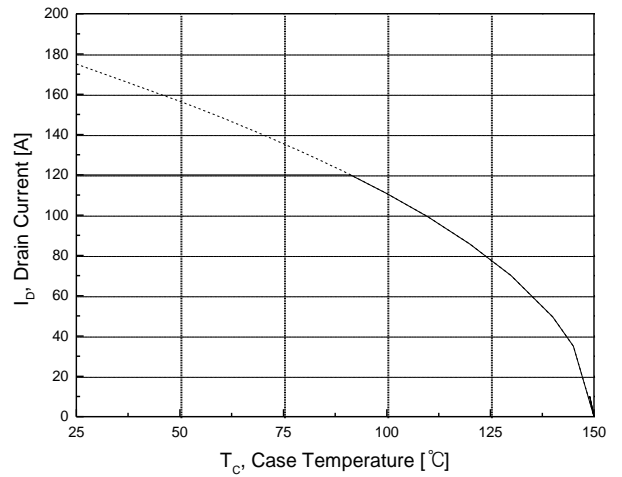


Fig.10 Maximum Drain Current vs. Case Temperature

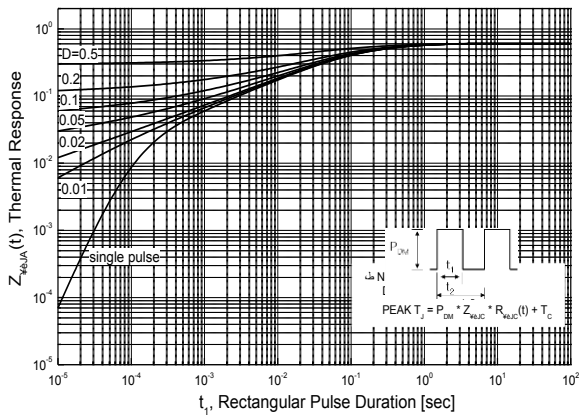
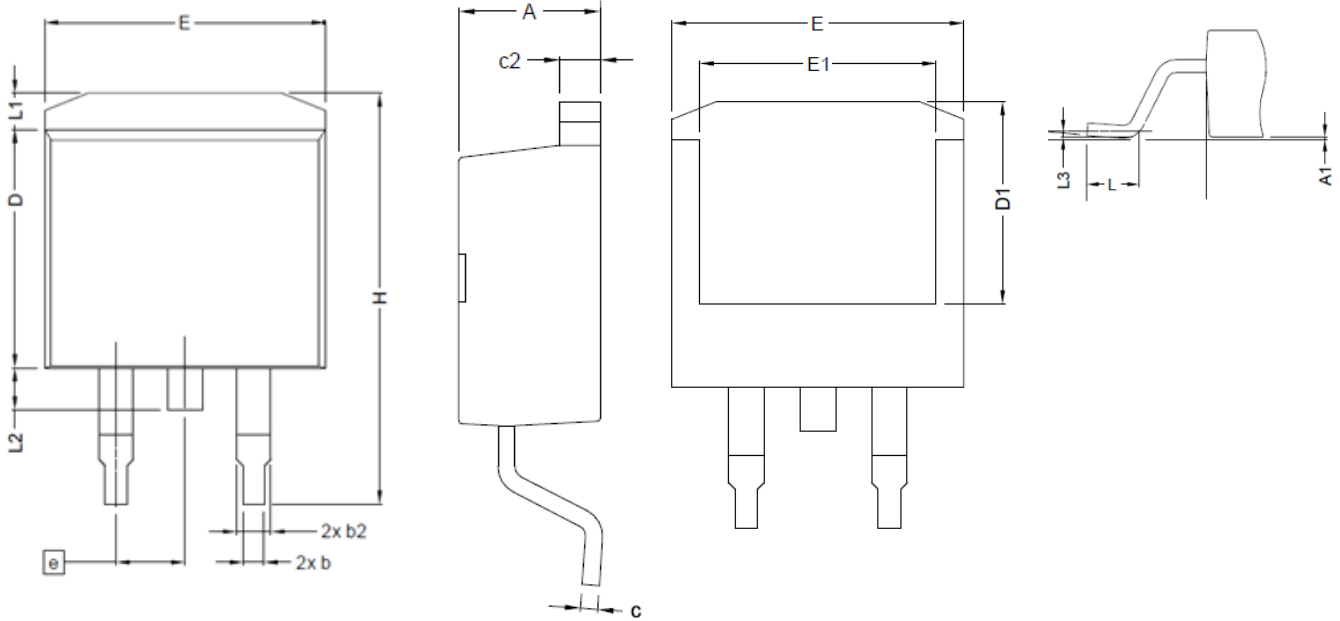


Fig.11 Transient Thermal Response Curve

Package Dimension

3 Leads, TO-263

Dimensions are in millimeters unless otherwise specified



Symbol	Millimeters(mm)	
	Min	Max
A	4.064	4.826
A1	-	0.254
b	0.508	0.99
b2	1.140	1.778
c	0.310	0.736
c2	1.140	1.650
D	8.382	9.652
D1	6.6	-
E	9.652	10.668
E1	6.223	-
e	BSC 2.54	
H	14.605	15.875
L	1.778	2.794
L1	-	1.676
L2	-	1.778
L3	BSC 0.254	

DISCLAIMER:

The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.

MagnaChip reserves the right to change the specifications and circuitry without notice at any time. MagnaChip does not consider responsibility for use of any circuitry other than circuitry entirely included in a MagnaChip product. [MagnaChip](#) is a registered trademark of MagnaChip Semiconductor Ltd.

单击下面可查看定价，库存，交付和生命周期等信息

[>>MAGNACHIP\(美格纳\)](#)