

AMDW1723F

Single N-channel Trench MOSFET 40V $1.95m\Omega$

FEATURES

- Ultra Low On-Resistance
- Bare die
- 100% Tested at Probe
- 175°C operating temperature
- AEC-Q101 qualified



PRODUCT SUMMARY		
V _{DS}	40	V
R _{DS(on)}	0.00195	Ω
Die size	2.74 x 4.17	mm ²
Thickness	180	μm





ORDERING INFORMATION				
Type / Ordering Code	Package	Marking	Packing	RoHS Status
AMDW1723F	Bare Die	not defined	Sawn on Film	Halogen Free

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ABSOLUTE MAXIMUM RATINGS, at $T_c = 25^{\circ}C$, unless otherwise specif

PARAMETER	SYMBOL	RATING	UNIT
Drain-source Voltage	V _{DS}	40	V
Gate-source Voltage	V _{GS}	± 20	V
Operating junction and storage temperature	T _j , T _{stg}	- 55 to +175	°C

WAFER LEVEL ELECTRICAL TEST

at $T_J = 25^{\circ}C$, unless otherwise specified

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain-source breakdown voltage	V _{(BR)DSS}	40	-	-	V	V_{GS} =0 V, I _D =250 µA
Gate threshold voltage	$V_{GS(th)}$	2.0	3.0	4.0	V	$V_{DS}=V_{GS}$, $I_{D}=250 \ \mu A$
Zero gate voltage drain current	I _{DSS}	-	-	1	μA	V _{DS} =32 V, V _{GS} =0 V
		-	-	1	μA	V _{DS} =40 V, V _{GS} =0 V
Gate-source leakage current	I _{GSS}	-	-	± 100	nA	V_{GS} =±20 V, V_{DS} =0 V
¹⁾ Drain-source on-state resistance	R _{DS(on)}	-	1.60	1.95	mΩ	V _{GS} =10 V, I _D =18 A
		-	1.80	2.54		V _{GS} =8 V, I _D =10 A
Diode forward voltage	V _{SD}	-	0.8	1.2	V	V _{GS} =0 V, I _S =18 A

PHYSICAL DATA

CONTENTS	CONFIGURATION
Passivation	Nitride (6,000 Å) / PSPI (5.5 μm)
Back Metal Composition (Thickness)	Ti (1,000 Å) – NiV (5,000 Å) – Ag (2,200 Å)
Front Metal Composition (Thickness)	AI (40,000 Å)
Die Dimension (with S/L)	2,740 µm x 4,170 µm
Gate Pad Dimension	515 µm x 354 µm
Wafer Diameter	200 mm, with 100 flat
Wafer Thickness	180 µm
Scribe lane width	80 µm

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Die information

Bare Die



(All labels are in dimension of μm)

DISCLAIMER :

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