



RoHS Compliant



MDWC0152ERH

Common-Drain Dual N-Channel Trench MOSFET 12V, 15A, 5.1mΩ

General Description

The MDWC0152ERH uses advanced Magnachip's MOSFET Technology, which provides high performance in on-state resistance and excellent reliability. Excellent low $R_{SS(ON)}$, low gate charge operation and operation for Battery Application.

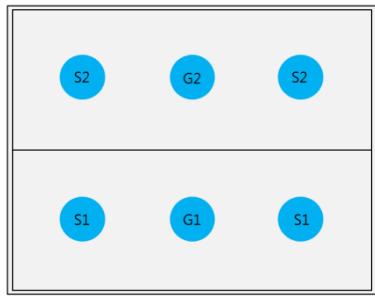
Features

- $V_{SS} = 12V$
- Source-Source ON Resistance;
 $R_{SS(ON)}$ typ. $4.0m\Omega$ @ $V_{GS} = 4.5V$
 $R_{SS(ON)}$ typ. $4.3m\Omega$ @ $V_{GS} = 3.8V$
 $R_{SS(ON)}$ typ. $4.8m\Omega$ @ $V_{GS} = 3.1V$
 $R_{SS(ON)}$ typ. $5.9m\Omega$ @ $V_{GS} = 2.5V$

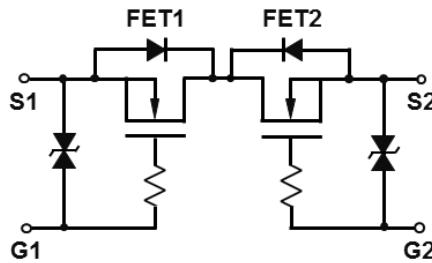
Applications

- Portable Battery Protection

Bottom View



2.14mm*1.67mm WLCSP



Absolute Maximum Ratings

Characteristics	Symbol	Rating	Units
Source-Source Voltage	V_{SSS}	12	V
Gate-Source Voltage	V_{GSS}	± 8	V
Source Current	I_S	15	A
	I_{SP}	60	A
Total Power Dissipation	P_D	1.85	W
Channel Temperature	T_{ch}	150	°C
Junction and Storage Temperature Range	T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance	$R_{θJA}$	67.4	°C/W

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDWC0152ERH	-55~150°C	WLCSP	Tape and Reel	Halogen Free

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Units
Static Characteristics						
Source-Source Breakdown Voltage	BV_{SSS}	$I_S = 1\text{mA}, V_{GS} = 0\text{V}$	12	-	-	V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{SS} = V_{GS}, I_S = 0.84\text{mA}$	-	0.9	1.4	
Cut-Off Current	I_{SSS}	$V_{SS} = 12\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 8\text{V}, V_{SS} = 0\text{V}$	-	-	10	μA
Source-Source Resistance	$R_{SS(\text{ON})}$	$V_{GS} = 4.5\text{V}, I_S = 4.0\text{A}$	-	4.0	5.1	$\text{m}\Omega$
		$V_{GS} = 3.8\text{V}, I_S = 4.0\text{A}$	-	4.3	5.5	
		$V_{GS} = 3.1\text{V}, I_S = 4.0\text{A}$	-	4.8	6.8	
		$V_{GS} = 2.5\text{V}, I_S = 4.0\text{A}$	-	5.9	10	
Dynamic Characteristics						
Total Gate Charge	Q_g	$V_{DD} = 10\text{V}, I_S = 4.0\text{A}, V_{GS} = 4\text{V}$	-	32.4	-	nC
Gate-Source Charge	Q_{gs}		-	7.5	-	
Gate-Drain Charge	Q_{gd}		-	12.6	-	
Input Capacitance	C_{iss}	$V_{SS} = 10\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	-	2,023	-	pF
Reverse Transfer Capacitance	C_{rss}		-	553	-	
Output Capacitance	C_{oss}		-	482	-	
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{GS} = 4\text{V}, V_{DD} = 10\text{V}, I_S = 4.0\text{A}, R_{\text{GEN}} = 3\Omega$	-	0.2	-	μs
Rise Time	t_r		-	1.6	-	
Turn-Off Delay Time	$t_{d(\text{off})}$		-	2.7	-	
Fall Time	t_f		-	9.8	-	
Drain-Source Body Diode Characteristics						
Source-Source Diode Forward Voltage	$VF_{(S-S)}$	$I_F = 4.0\text{A}, V_{GS} = 0\text{V}$	-	0.8	1.2	V

Note *1. Mounted on PCB Board (25.4mm x 25.4mm)

Characteristic Graph

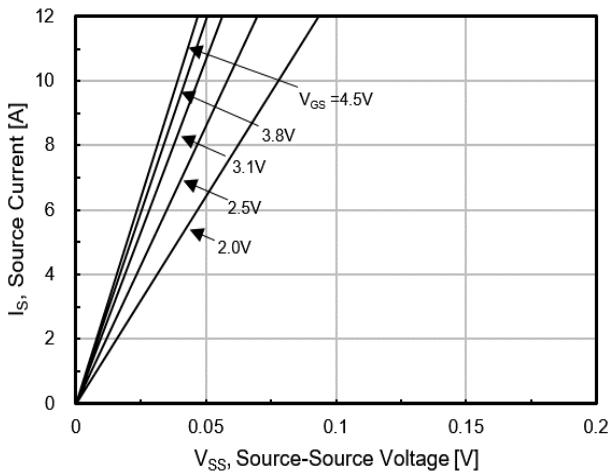


Fig.1 On-Region Characteristics

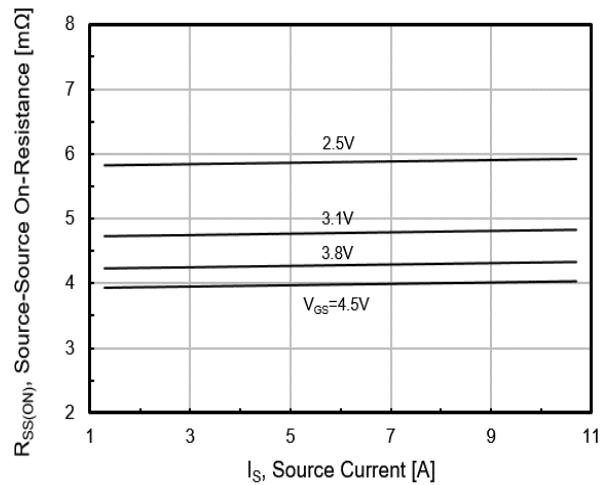


Fig.2 On-Resistance Variation with Source Current and Gate Voltage

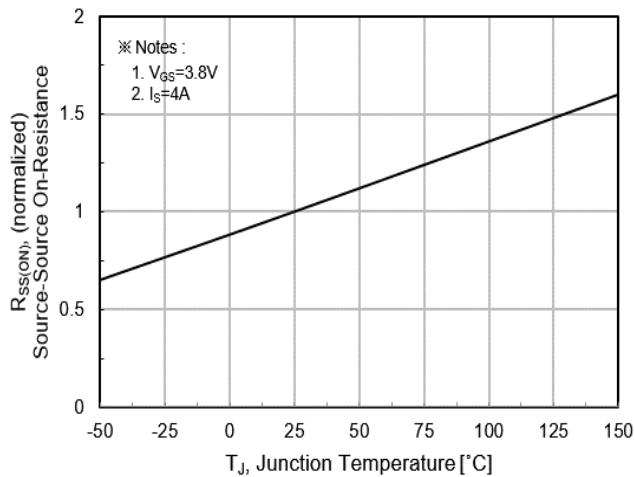


Fig.3 On-Resistance Variation with Temperature

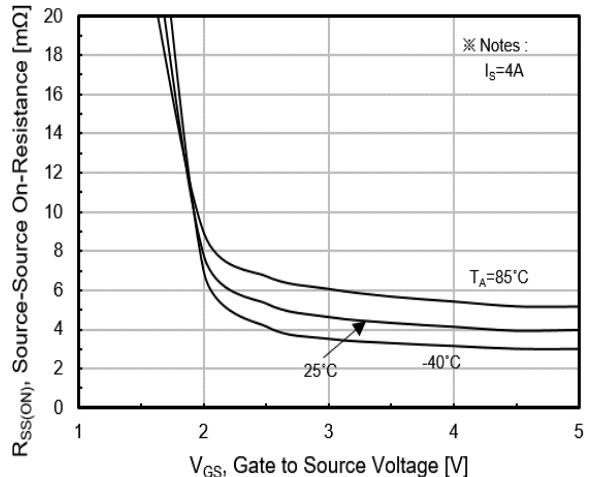


Fig.4 On-Resistance Variation with Gate to Source Voltage

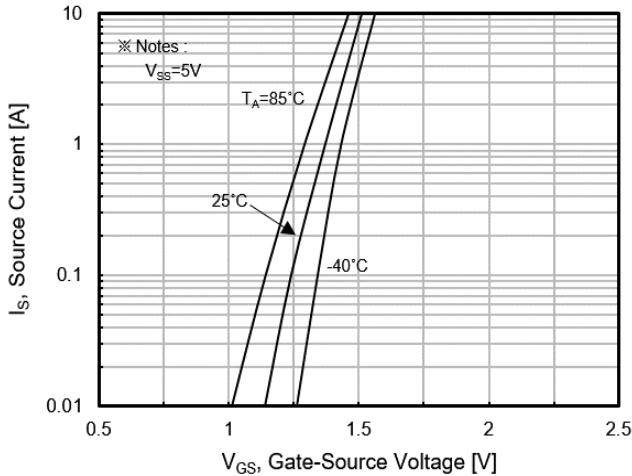


Fig.5 Transfer Characteristics

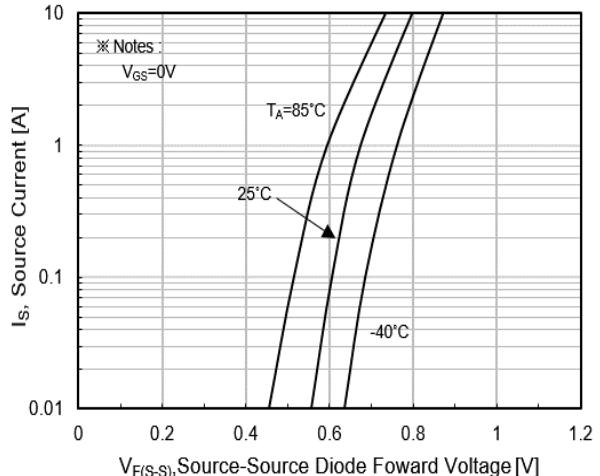


Fig.6 Body Diode Forward Voltage

Characteristic Graph

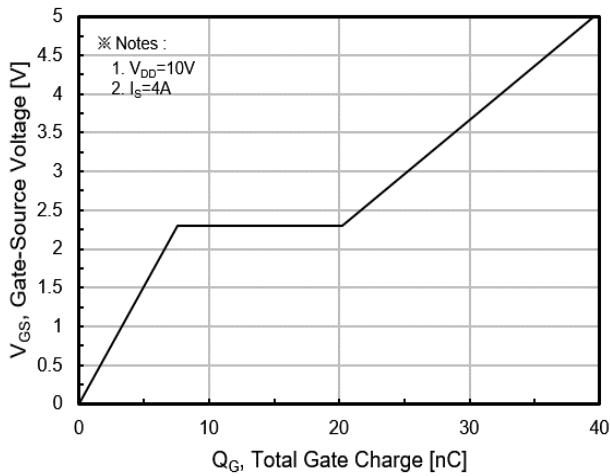


Fig.7 Gate Charge Characteristics

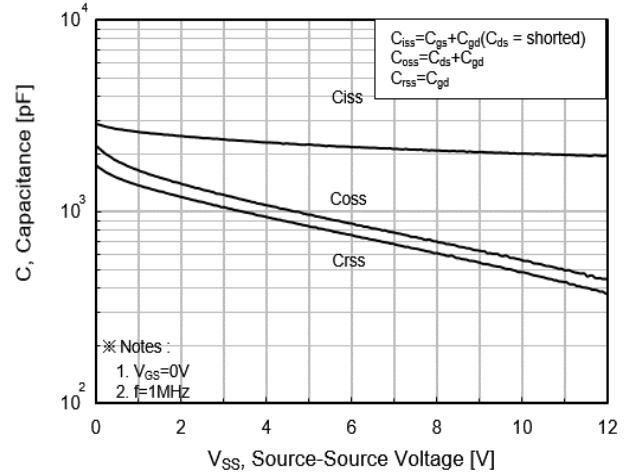


Fig.8 Capacitance Characteristics

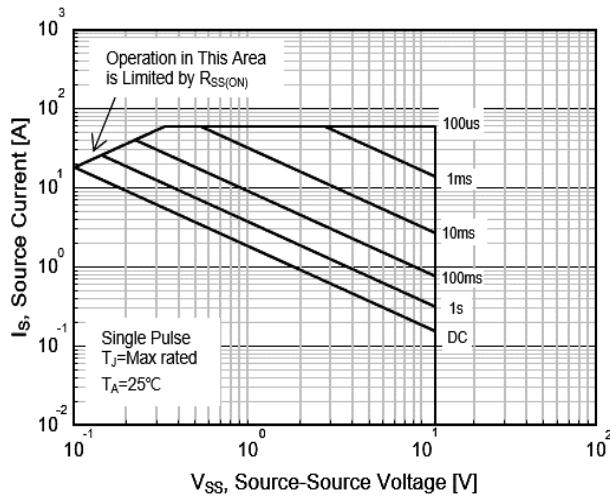


Fig.9 Maximum Safe Operating Area

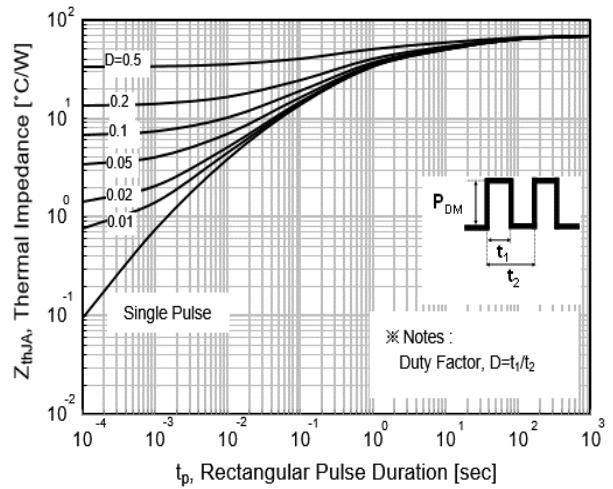
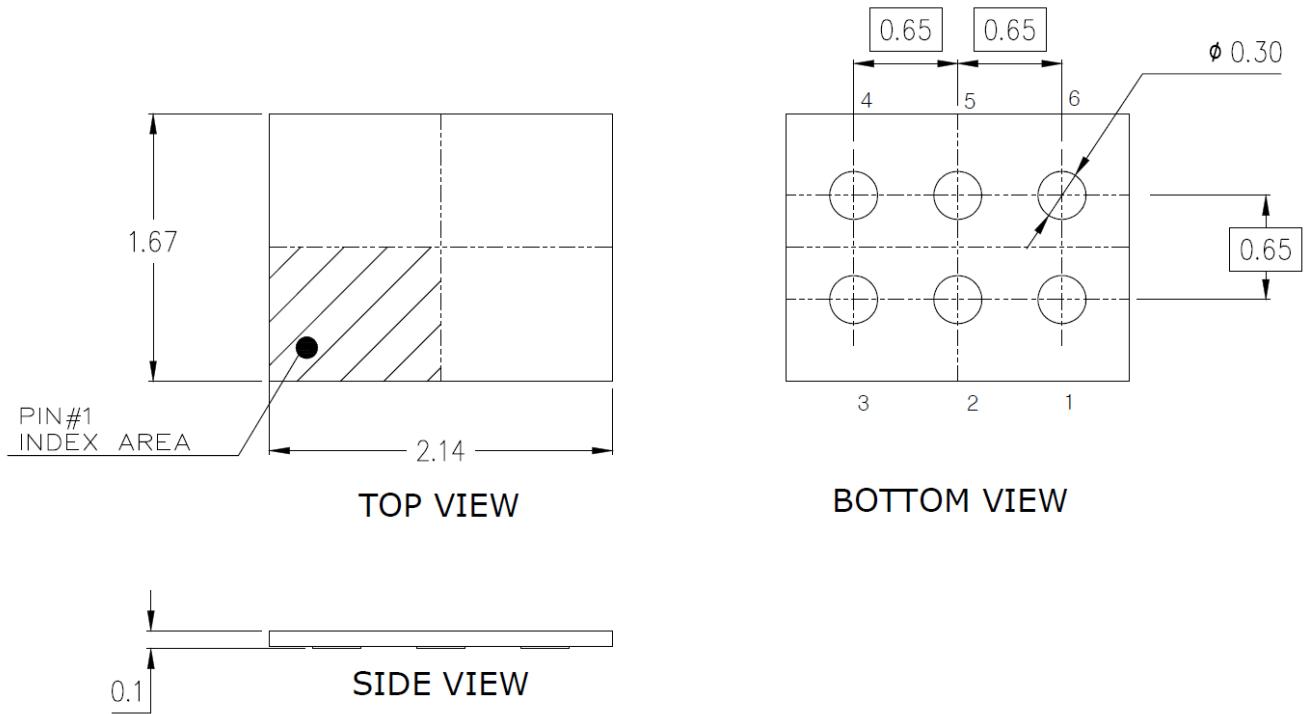


Fig.10 Transient Thermal Impedance Curve

PACKAGE OUTLINE



Note :

- 1) ALL DIMENSIONS ARE IN MILLIMETERS.
- 2) GENERAL TOLERANCE : ± 0.03 mm
- 3) PACKAGE BODY SIZES EXCLUDE FLASH & BURRS

DISCLAIMER:

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