

Fully Integrated, Hall Effect-Based Linear Current Sensor IC with 4.8 kV High-Isolation SOP-16 Wide Body Package



1 Product Description

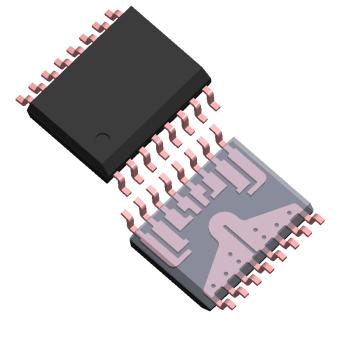
The MagnTek® MT9222 product series is a fully integrated Hall Effect-based isolated linear current sensor device family designed for AC or DC current sensing in industrial, commercial, and communications systems. Each of the MT9222 consists of a precise, lowoffset, linear Hall circuit with a low-resistance current conductor path located near the surface of the die. Applied current flowing through this low-resistance current conductor path generates a magnetic field that the Hall IC converts into a proportional voltage. The current is sensed differentially in order to reject fields, improving common-mode accuracy magnetically noisy environments. Device accuracy is optimized through the close proximity of the magnetic field to the Hall transducer.

A precise, proportional voltage is provided by the low-offset, chopper-stabilized BCD Hall IC, which has been accurately calibrated when it is manufactured. The output of the device has a positive slope (>VOQ) when an increasing current flows through the primary current conduction path (from pins 1,2,3 and 4, to pins 5,6,7 and 8). The internal resistance of this conductive path is $0.85~\text{m}\Omega$ typical, providing low-power loss. The terminals of the conductive path are electrically isolated from the signal leads (pins 9 through 16). This allows the MT9222 current sensor IC to be used in high-side current sense applications without use other costly isolation techniques.

The MT9222 family provides SOP-16W package to customers, The package is RoHS compliant.

2 Features

- ±0.5% Typical Linearity Over Temperature
- Fast Output Response Time: 4 µs (typ.)
- 150kHz Signal Bandwidth
- 5.0V Single Supply Operation
- -40°C~125°C Operating Temperature
- **Primary Conductor Resistance**
 - ---0.85mΩ
- Sensing Current Range: (AC or DC)
 - ---±20A, ±30A, ±50A, ±65A
 - ---+40A, +50A
- Output Voltage Proportional to AC or DC Current
- Ratiometric Output from Supply Voltage
- Extremely Stable Quiescent Output Voltage
- Device Bandwidth is Set via FILTER Pin
- Near Zero Magnetic Hysteresis
- Isolation Voltage:
 - ---4.8kVrms Dielectric Surge Isolation
 - ---7.5mm Clearance Distance
 - ---8.2mm Creepage Distance
- Package Option:
 - ---SOP-16W
- RoHS Compliant: (EU)2015/863



Not to scale

3 Applications

- Low-power inverter current sensing
- Motor phase and rail current sensing
- **PV String Inverters**
- **Chargers and Converters**
- **Switching Power Supplies**
- Overcurrent Protection
- **UPS Power**

4 Product Overview of MT9222

Part No.	Description
MT9222WT	SOP-16 Tape & Reel (1000 pcs/bag)



Table of Contents

1	Product Description	1
2	Features	1
3	Applications	1
4	Product Overview of MT9222	1
5	Functional Block Diagram	3
6	Pin Configuration and Function	3
7	Naming Specification	4
8	Selection Guide	4
9	Output Voltage vs. Input Current	5
10	Typical Application Circuit	5
11	Electrical and Magnetic Characteristics	6
	11.1 Absolute Maximum Ratings	6
	11.2 Isolation and Package Characteristics	6
	11.3 Life Time Characteristics	6
	11.4 ESD Rating	7
	11.5 Electrical Characteristics	7
	11.6 Accuracy Specifications	8
12	Characteristic Definitions	12
13	Package Material Information	15
	13.1 SOP-16W Package Information	15
14	Copy Rights and Disclaimer	16

Reversion History

Originally Version 1



5 Functional Block Diagram

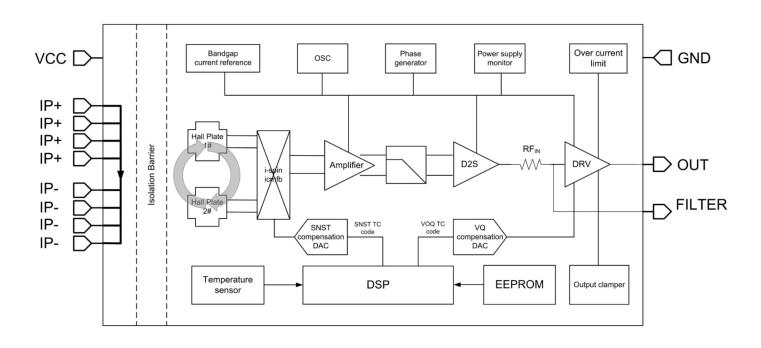
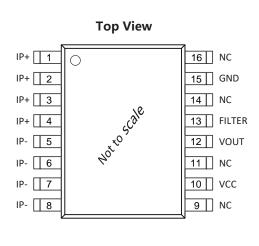


Figure.1 **Functional Block Diagram**

6 Pin Configuration and Functions



No.	Name	Description
1, 2, 3, 4	IP+	Input Current Positive Pin
5, 6, 7, 8	IP-	Input Current Negative Pin
9	NC	No Internal Connection
10	VCC	Power Supply
11	NC	No Internal Connection
12	VOUT	Analog Output Signal
13	FILTER	Filter Pin to Set Bandwidth
14	NC	No Internal Connection
15	GND	Signal Ground
16	NC	No Internal Connection

Figure.2 Pin Configuration & Functions (SOP-16W)



7 Naming Specification



Series Name

2 Package Type

Type	Package Type
WT	SOP-16W

Input Current Range

Type	Input Current Range
20	Full scale sensing range: 20A
30	Full scale sensing range: 30A
40	Full scale sensing range: 40A
50	Full scale sensing range: 50A
65	Full scale sensing range: 65A

4 Current Polarity

Туре	Current Polarity		
В	BidirectionalAC Current		
U	UnidirectionalDC Current		

6 Output Mode

Туре	Output Mode
R	Ratiometry output

6 Power Supply

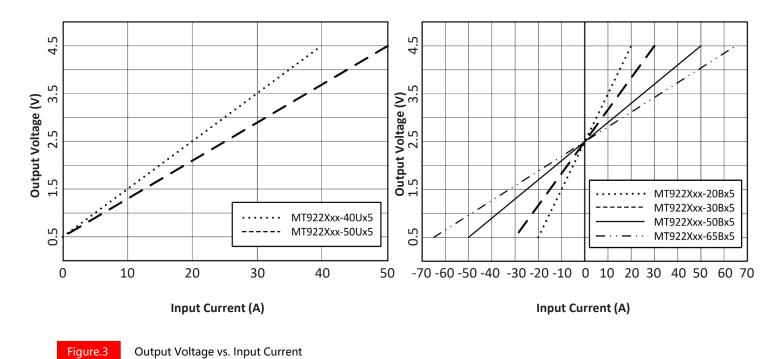
Туре	Power Supply
5	VCC=5V

8 Selection Guide

Ordering Part Number	Current Range (A)	VCC (V)	Sensitivity (mV/A)	Package	Qty per Reel (pcs)
MT9222WT-40UR5	+40	5	100	SOP-16W	1000
MT9222WT-50UR5	+50	5	80	SOP-16W	1000
MT9222WT-20BR5	±20	5	100	SOP-16W	1000
MT9222WT-30BR5	±30	5	66.7	SOP-16W	1000
MT9222WT-50BR5	±50	5	40	SOP-16W	1000
MT9222WT-65BR5	±65	5	30.8	SOP-16W	1000

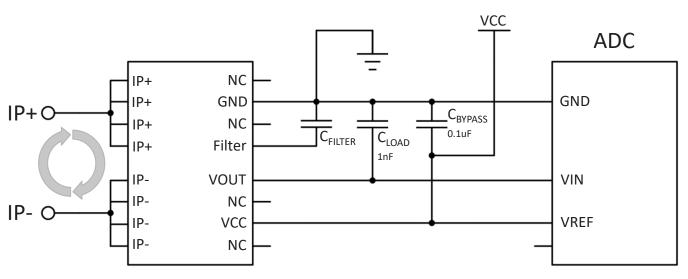


9 Output Voltage vs. Input Current



10 Typical Application Circuit

The typical application circuits of MT9222 series products include a bypass capacitor (CBYPASS capacitor between VCC and GND is necessary) and a filter capacitor as an additional external components. On the input side, pins 1,2,3 and 4 are shorted together, pins 5,6,7 and 8 are shorted together. The analog signal output is measured directly from the VOUT pin. The signal is proportional to the IP of the primary sensing current of AC or DC within the specified measurement range. The FILTER pin can be used to reduce bandwidth to optimize noise performance, but at the same time proportionally increase response time.



 $fc=1/(2*\pi*R_F*C_{FILTER})$ $R_F=1.8K\Omega$

Figure.4

Typical Application Circuit



11 Electrical Magnetic Characteristics

11.1 Absolute Maximum Ratings

Absolute maximum ratings are limited values to be applied individually, and beyond which the serviceability of the circuit may be impaired. Functional operability is not necessarily implied. Exposure to absolute maximum rating conditions for an extended period of time may affect device reliability.

Symbol	Parameters	Min	Max	Units
VCC	Supply Voltage	-	6	V
VRCC	Reverse Battery Voltage	-0.1	-	V
VOUT	Output Voltage	-	VCC+0.5	V
VROUT	Reverse Output Voltage	-0.1	-	V
IOUT(source)	Continuous Output Current	-	80	mA
IOUT(sink)	Continuous Output Current	-	40	mA
TA	Operating Ambient Temperature	-40	125	°C
TS	Storage Temperature	-50	150	°C
TJ	Junction Temperature	-	175	°C

11.2 Isolation and Package Characteristics

Symbol	Parameters	Test Conditions	Value	Units
V _{SURGE}	Dielectric Surge Strength Test Voltage	Tested ± 5 pulses at 30 seconds in compliance to IEC 61000-4-5 1.2 / 50 μ s (width).	10000	V
V_{ISO}	Dielectric Strength Test Voltage	Test method per UL standard 60950-1 (edition 2). VTEST = VISO, t = 60 s (qualification); VTEST = 1.2 × VISO, t = 1 s (100% production)	4800	Vrms
Dcl	External Clearance	Measured from input terminals to output terminals, shortest distance through air.	7.5	mm
Dcr	External Creepage	Measured from input terminals to output terminals, shortest distance path along body.	8.2	mm

11.3 Life Time Characteristics

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
ESNST_DRIFT	Sensitivity Error Lifetime Drift	TA=-40~125°C	-	±3	-	%
ETOT_DRIFT	Total Output Error Lifetime Drift	TA=-40~125°C	-	±3	-	%

^{*} Based on characterization data obtained during AEC-Q100 stress testing.



11.4 ESD Ratings

Symbol	Parameters	Reference	Values	Unit
VESD	Human-body model (HBM)	AEC-Q100-002	Class IIIA	Grade
	Charged-device model (CDM)	AEC-Q100-011	Class C3	Grade
	Latch up	AEC-Q100-004	Class IIA	Grade

11.5 Electrical Specifications

At TA =-40~125 °C, Vcc=5V (unless otherwise specified)

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
VCC	Supply Voltage	-	4.5	5	5.5	V
ICC	Supply Current	TA = 25°C	-	14	20	mA
RIP	Primary Conductor Resistance	TA = 25°C	-	0.85	-	mΩ
BW	Internal Bandwidth	Small signal –3 dB; CL = 1 nF	-	150	-	KHz
TPO	Power on time	TA = 25°C, CL = 1nF, IP=IPR(max) applied	-	190	-	us
TR	Rise time	IP = IPR(max), A = 25°C, CL = 1nF	-	3	-	us
TPD	Propagation Delay	IP = IPR(max), TA = 25°C, CL = 1nF	-	2	-	us
TRESP	Response Time	IP = IPR(max), TA = 25°C, CL = 1nF	-	4	5	us
VOL	Analog Output Low Saturation Level	RL>=4.7KΩ	-	-	0.3	V
VOH	Analog Output High Saturation Level	RL>=4.7KΩ	VCC-0.3	-	-	V
CL	Output CAP Load	OUT to GND	-	1	10	nF
ROUT	DC Output resistance		-	5	-	Ω
RL	Output RES Load	Pull-down to Ground	4.7	-	-	ΚΩ
KL	Output RES LOAU	Pull-up to VCC	4.7	-	-	ΚΩ
RF(int)	Internal filter resistance		-	1.8	-	ΚΩ
IND	Noise Density	Input-referenced noise density; TA = 25°C, CL = 1 nF	-	450	-	uA/√Hz
ELIN	Nonlinearity error	Through full range of IP	-1.5	-	1.5	%
ERAT_SNST	Ratiometry Sensitivity Error	VCC = 4.5 to 5.5 V, TA = 25°C	-	±1.5	-	%
ERAT_VOQ	Ratiometry Quiescent Voltage Output Error	VCC = 4.5 to 5.5 V, TA = 25°C	-	±1	-	%

^{*} Device may be operated at higher primary current levels, ambient temperatures, and internal leadframe temperatures, provided the Maximum Junction Temperature, is not exceeded.

Continued on the next page...



Electrical Specifications (*continued***)**

At T_A =-40~125 °C, VCC=5V (unless otherwise specified)

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
CMFRR	Common Mode Field Rejection Ratio	Uniform external magnetic field	-	40	-	dB
SNST(match)	Hall Plate Sensitivity Matching	TA = 25°C		±1	-	%
VPORH	Power-On Reset High Voltage	TA = 25°C, VCC rising	3.75	4	4.25	V
VPORL	Power-On Reset Low Voltage	TA = 25°C, VCC falling	-	3.5	-	V
VPORHYS	Power-On Reset Hysteresis	TA = 25°C	-	0.5	-	V
TPORR	Power-On Reset Release Time	TA = 25°C, VCC rising	-	30	-	us
TPORA	Power-On Reset Analog Delay	TA = 25°C, VCC rising	-	5	-	us
ISCLP	Source Current of Over-Current- Limit	-	-	80	+	mA
ISCLN	Sink Current of Over-Current- Limit	-	-	40	-	mA
TSCLD	Detect Time for Over-Current- Limit	TA = 25°C, IOUT>ISCLP or IOUT <iscln< td=""><td>-</td><td>7</td><td>-</td><td>us</td></iscln<>	-	7	-	us
TSCLR	Release Time for Over-Current- Limit	TA = 25°C	-	0.62	-	ms

^{*} Device may be operated at higher primary current levels, ambient temperatures, and internal leadframe temperatures, provided the Maximum Junction Temperature, is not exceeded.

11.6 Accuracy Specifications

MT9222CT-20BR5 At T_A = -40~125 °C, VCC=5V (unless otherwise specified)

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
IPR	Optimized Input Current Range		-20	-	20	Α
SNST	Sensitivity	-20A≤IP≤20A	-	100	-	mV/A
VOQ	Zero-Current Output Voltage		-	$V_{CC} \times 0.5$	-	V
		IP=0 A, TA=25°C	-10	-	10	mV
VOE	Zero-Current Output Error	IP=0 A, TA=-40°C to 25°C	-25	-	25	mV
		IP=0 A, TA=25°C to 125°C	-25	-	25	mV
		IP=20A, TA=25°C	-	±1.1	-	%
ESNST	Sensitivity Error	IP=20A, TA=-40°C to 25°C	-	±2.5	-	%
		IP=20A, TA=25°C to 125°C	-	±2.5	-	%
		IP=20A, TA=25°C	-	±1.5	-	%
ETOT	Total Output Error	IP=20A, TA = -40° C to 25°C	-	±3.5	-	%
	·	IP=20A, TA = 25°C to 125°C	-	±3.5	-	%

^{*} Device may be operated at higher primary current levels, ambient temperatures, and internal leadframe temperatures, provided the Maximum Junction Temperature, is not exceeded.



MT9222CT-30BR5 At T_A = -40~125 °C, Vcc=5V (unless otherwise specified)

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
IPR	Optimized Input Current Range		-30	-	30	Α
SNST	Sensitivity	-30A≤IP≤30A	-	66.6	-	mV/A
VOQ	Zero-Current Output Voltage		-	$V_{CC} \times 0.5$	-	V
		IP=0 A, TA=25°C	-10	-	10	mV
VOE	Zero-Current Output Error	IP=0 A, TA=-40°C to 25°C	-25	-	25	mV
		IP=0 A, TA=25°C to 125°C	-25	-	25	mV
		IP=30A, TA=25°C	-	±1.1	-	%
ESNST	Sensitivity Error	IP=30A, TA=-40°C to 25°C	-	±2.5	-	%
		IP=30A, TA=25°C to 125°C	-	±2.5	-	%
		IP=30A, TA=25°C	-	±1.5	-	%
ETOT	Total Output Error	IP=30A, TA = -40° C to 25°C	-	±3.5	-	%
		IP=30A, TA = 25°C to 125°C	-	±3.5	-	%

^{*} Device may be operated at higher primary current levels, ambient temperatures, and internal leadframe temperatures, provided the Maximum Junction Temperature, is not exceeded.

MT9222CT-40UR5 At $T_A = -40 \sim 125$ °C, Vcc=5V (unless otherwise specified)

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
IPR	Optimized Input Current Range		0	-	40	Α
SNST	Sensitivity	0A≤IP≤40A	-	100	-	mV/A
VOQ	Zero-Current Output Voltage		-	$V_{CC} \times 0.1$	-	V
		IP=0 A, TA=25°C	-10	-	10	mV
VOE	Zero-Current Output Error	IP=0 A, TA=-40°C to 25°C	-25	-	25	mV
		IP=0 A, TA=25°C to 125°C	-25	-	25	mV
		IP=40A, TA=25°C	-	±1.1	-	%
ESNST	Sensitivity Error	IP=40A, TA=-40°C to 25°C	-	±2.5	-	%
		IP=40A, TA=25°C to 125°C	-	±2.5	-	%
		IP=40A, TA=25°C	-	±1.5	-	%
ETOT	Total Output Error	IP=40A, TA = -40° C to 25°C	-	±3.5	-	%
		IP=40A, TA = 25°C to 125°C	-	±3.5	-	%

^{*} Device may be operated at higher primary current levels, ambient temperatures, and internal leadframe temperatures, provided the Maximum Junction Temperature, is not exceeded.



MT9222CT-50UR5 At T_A = -40~125 °C, Vcc=5V (unless otherwise specified)

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
IPR	Optimized Input Current Range		0	-	50	Α
SNST	Sensitivity	0A≤IP≤50A	-	80	-	mV/A
VOQ	Zero-Current Output Voltage		-	V _{CC} ×0.1	-	V
		IP=0 A, TA=25°C	-10	-	10	mV
VOE	Zero-Current Output Error	IP=0 A, TA=-40°C to 25°C	-25	-	25	mV
		IP=0 A, TA=25°C to 125°C	-25	-	25	mV
		IP=50A, TA=25°C	-	±1.5	-	%
ESNST	Sensitivity Error	IP=50A, TA=-40°C to 25°C	-	±3.0	-	%
		IP=50A, TA=25°C to 125°C	-	±3.0	-	%
		IP=50, TA=25°C	-	±1.9	-	%
ETOT	Total Output Error	IP=50A, TA = -40° C to 25°C	-	±4.0	-	%
		IP=50A, TA = 25°C to 125°C	-	±4.0	-	%

^{*} Device may be operated at higher primary current levels, ambient temperatures, and internal leadframe temperatures, provided the Maximum Junction Temperature, is not exceeded.

MT9222CT-50BR5 At T_A = -40~125 °C, Vcc=5V (unless otherwise specified)

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
IPR	Optimized Input Current Range		-50	-	50	Α
SNST	Sensitivity	-50A≤IP≤50A	-	40	-	mV/A
VOQ	Zero-Current Output Voltage		-	$V_{CC} \times 0.5$	-	V
		IP=0 A, TA=25°C	-10	-	10	mV
VOE	Zero-Current Output Error	IP=0 A, TA=-40°C to 25°C	-25	-	25	mV
		IP=0 A, TA=25°C to 125°C	-25	-	25	mV
		IP=50A, TA=25°C	-	±1.5	-	%
ESNST	Sensitivity Error	IP=50A, TA=-40°C to 25°C	-	±3.0	-	%
		IP=50A, TA=25°C to 125°C	-	±3.0	-	%
		IP=50A, TA=25°C	-	±1.9	-	%
ETOT	Total Output Error	IP=50A, TA = -40° C to 25°C	-	±4.0	-	%
		IP=50A, TA = 25°C to 125°C	-	±4.0	-	%

^{*} Device may be operated at higher primary current levels, ambient temperatures, and internal leadframe temperatures, provided the Maximum Junction Temperature, is not exceeded.



MT9222CT-65BR5 At $T_A = -40 \sim 125$ °C, VCC=5V (unless otherwise specified)

Symbol	Parameters	Test Condition	Min	Тур	Max	Unit
IPR	Optimized Input Current Range		-65	-	65	Α
SNST	Sensitivity	-65A≤IP≤65A	-	30.8	-	mV/A
VOQ	Zero-Current Output Voltage		-	$V_{CC} \times 0.5$	-	V
		IP=0 A, TA=25°C	-10	-	10	mV
VOE	Zero-Current Output Error	IP=0 A, TA=-40°C to 25°C	-25	-	25	mV
		IP=0 A, TA=25°C to 125°C	-25	-	25	mV
		IP=65A, TA=25°C	-	±1.8	-	%
ESNST	Sensitivity Error	IP=65A, TA=-40°C to 25°C	-	±3.5	-	%
		IP=65A, TA=25°C to 125°C	-	±3.5	-	%
		IP=65A, TA=25°C	-	±2.2	-	%
ETOT	Total Output Error	IP=65A, TA = -40° C to 25°C	-	±4.5	-	%
		IP=65A, TA = 25°C to 125°C	-	±4.5	-	%

^{*} Device may be operated at higher primary current levels, ambient temperatures, and internal leadframe temperatures, provided the Maximum Junction Temperature, is not exceeded.

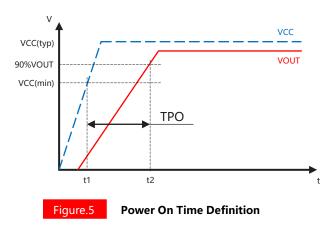


12 Characteristic Definitions

Power On Time---TPO

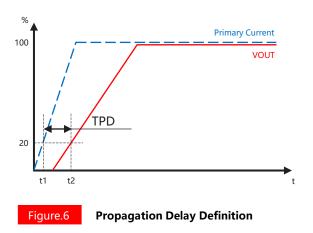
When the supply is ramped to its operating voltage, the device requires a finite time to power its internal components before responding to an input magnetic field.

The Power-On Time (TPO) is defined as the time taken between the supply reaching the minimum operating voltage VCCmin (t1), and the output voltage to settling to within ±10% of its steady state value under an applied magnetic field (t2) (See Figure 5).



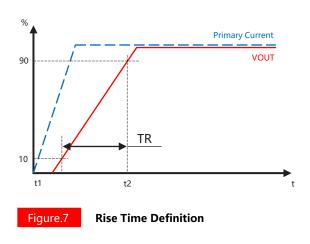
Propagation Delay---TPD

The time interval between a) when the primary current signal reaches 20% of its final value, and b) when the output reaches 20% of its final value (see Figure 6).



Rise Time---TR

Rise Time is the time interval between the sensor VOUT reaching 10% of its full scale value (t1), and it reaching 90% of its full scale value (t2). (see Figure 7). Both TR and TRESP can be negatively affected by any eddy current losses created if a conductive ground plane is used.



Response Time---TRESP

The time interval between a) when the primary current signal reaches 80% of its final value, and b) when the sensor reaches 80% of its output corresponding to the applied current. (see Figure 8). Both TR and TRESP can be negatively affected by any eddy current losses created if a conductive ground plane is used.

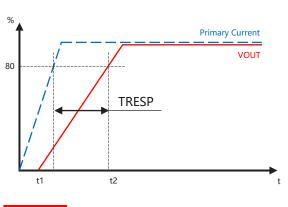


Figure.8 **Response Time Definition**



Zero-Current Output Voltage---VOQ

VOQ is the voltage output when the primary current is zero. For a unipolar supply voltage, it nominally remains at 0.5 × VCC for a bidirectional device and 0.1 × VCC for a unidirectional device. For example, in the case of a bidirectional output device, VCC=5V translates into VOQ=2.5V. Variation in VOQ from the nominal value is due to thermal drift and the resolution limits of voltage offset trimming in the factory.

Zero-Current Output Error---VOE

The deviation of the device output from its ideal quiescent value of 0.5 × VCC (bidirectional) or 0.1 × VCC (unidirectional) due to nonmagnetic causes. To convert this voltage to amp, divide by the device sensitivity (SNST).

Sensitivity---SNST

The Sensitivity in mV/A indicates how much the output changes when the primary current changes. It is the product of the magnetic circuit sensitivity (G/A) (1G = 0.1mT) and the linear IC amplifier gain (mV/G). The linear IC amplifier gain is programmed at the factory to optimize the sensitivity (mV/A) for the full-scale current of the device.

$$SNST = \left(\frac{VOUT_IPOSx-VOUT_INEGx}{IPOSx-INEGx}\right)$$

where IPOSx and INEGx are two current values with opposite polarities.

Total Output Error---ETOT

The accuracy represents the maximum deviation of the actual current output from its ideal current value. This is equivalent to the difference between the actual output voltage and the ideal output voltage, divided by the ideal sensitivity, relative to current flowing through the primary conduction path:

$$ETOT(IP) = \left(\frac{VOUT_(IP) - VOUT_ideal(IP)}{SNST_ideal*IP}\right) *100\%$$

Nonlinearity Sensitivity Error---ELIN

Ideally the primary current vs sensor output function is a straight line. The non-linearity is an indication of the worst deviation from this straight line. The ELIN in % is defined as:

$$ELIN = \left(\frac{SNST_I1}{SNST_I2} - 1\right) *100\%$$

Where:

$$SNST_I1 = \left(\frac{VOUT_IPOS1 - VOUT_INEG1}{IPOS1 - INEG1}\right)$$

$$SNST_I2 = \left(\frac{VOUT_IPOS2 - VOUT_INEG2}{IPOS2 - INEG2}\right)$$

and IPOSx and INEGx are positive and negative current values, with respect to the quiescent voltage output such that |IPOS2| = |INEG2| = IPRmax, and $IPOS2| = 2 \times |IPOS1|$ and INEG2| =2 × |INEG1|.

Ratiometry Error---ERAT

The MT9222 device features ratiometric output. This means that the Quiescent Voltage Output (VOQ) and sensitivity (SNST), are proportional to the Supply Voltage, VCC. In other words, when the supply voltage increases or decreases by a certain percentage, each characteristic also increases or decreases by the same percentage. Error is the difference between the measured change in the supply voltage relative to 5 V, and the measured change in each characteristic.

Ratiometry Quiescent Voltage Output Error---ERAT VOQ

ERAT_VOQ, for a given supply voltage, is defined

$$ERAT_VOQ = \left(\frac{VOQ(VCC)/VCC)}{VOQ(5V)/5V} - 1\right) * 100\%$$

Ratiometry Sensitivity Error---ERAT SNST ERAT_SNST, for a given supply voltage, is defined

$$ERAT_SNST = \left(\frac{SNST_I1(VCC)/VCC)}{SNST_I1(5V)/5V} - 1\right)*100\%$$



Power-On Reset---POR

The descriptions in this section assume temperature = 25°C, no output load (RL, CL), and no the applied current is

Power-Up. At power-up, as VCC ramps up, the output is in a high-impedance state. When VCC crosses VPORH, the output will go to VCC/2 after POR Release counter (TPORR) + POR Analog delay (TPORA).

VCC drops below VCC(min) = 4.5 V. If VCC drops below VPORL, the output will be in a high-impedance state. If VCC recovers and exceeds VPORH, the output will go back to normal operation after POR Release counter (TPORR) + POR Analog delay (TPORA) (See Figure. 10).

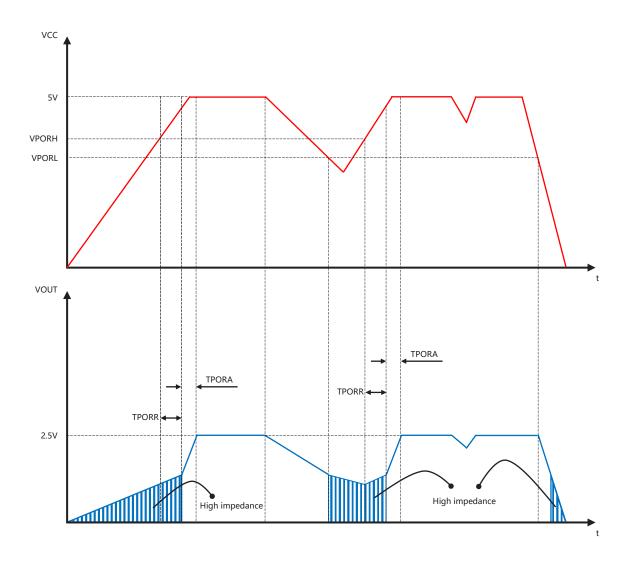


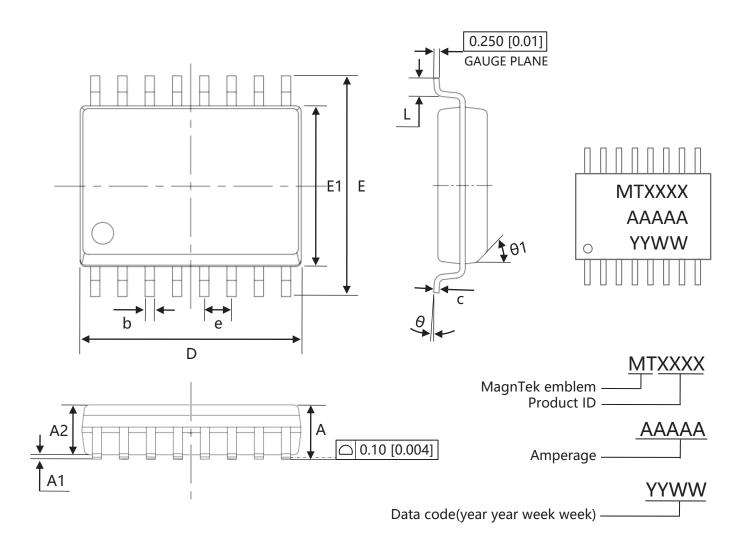
Figure.9

Power-On Reset Definition



13 Package Material Information (For Reference Only – Not for Tooling Use)

13.1 SOP-16W Package Information



Symbol	Dimensions in Millimeters		Dimension	s in Inches
	Min	Max	Min	Max
А	2.350	2.650	0.093	0.104
A1	0.100	0.300	0.004	0.012
A2	2.050	2.550	0.081	0.100
b	0.330	0.510	0.013	0.020
С	0.230	0.320	0.009	0.013
D	10.10	10.50	0.398	0.413
E	10.00	10.63	0.394	0.419
E1	7.400	7.600	0.291	0.299
е	1.270	(BSC)	0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°
θ1	4!	5°	4:	5°



14 Copy Rights and Disclaimer

- 1. This document may not be reproduced or duplicated, in any form, in whole or in part without prior written consent of MagnTek . Copyrights © 2019, MagnTek Incorporated.
- 2. MagnTek reserves the right to make changes to the information published in this document at anytime without notice.
- 3. MagnTek' s products are limited for use in normal commercial applications. MagnTek' s products are not to be used in any device or system, including but not limited to medical life support equipment and system.

For the latest version of this document, please visit our website: www.magntek.com.cn

单击下面可查看定价,库存,交付和生命周期等信息

>>Magntek(麦歌恩)