

Features

- Trench Power LV MOSFET Technology
- High Density Cell Design for Low R_{DS(ON)}
- · High Speed Switching
- · Halogen Free. "Green" Device (Note 1)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

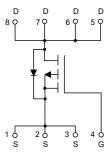
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 17°C/W Junction to Ambient (2)
- Thermal Resistance: 1.5°C/W Junction to Case (2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Volltage	V _{GS}	±25	V
Continuous Drain Current	I _D	-50	Α
Pulsed Drain Current (3)	I _{DM}	-210	Α
Total Power Dissipation	P _D	83	W
Single Pulsed Avalanche Energy ⁽⁴⁾	E _{AS}	360	mJ

Note:

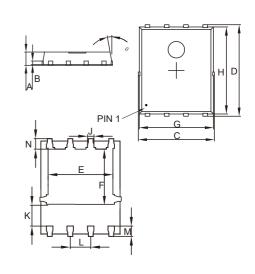
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design, while $R_{\theta JA}$ is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.
- 3. Pulse Test: Pulse Width≤300us,Duty cycle ≤2%.
- 4. $T_J=25$ °C, $V_{DD}=-25V$, $V_{G}=-10V$, L=2mH.

Internal Structure



P-CHANNEL MOSFET

DFN5060



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	NOIL
Α	0.031	0.047	0.80	1.20	
В	0.010		0.254		TYP.
С	0.193	0.222	4.90	5.64	
D	0.232	0.250	5.90	6.35	
E	0.148	0.167	3.75	4.25	
F	0.126	0.154	3.20	3.92	
G	0.189	0.213	4.80	5.40	
Н	0.222	0.239	5.65	6.06	
K	0.045	0.059	1.15	1.50	
J	0.012	0.020	0.30	0.50	
L	0.046	0.054	1.17	1.37	
M	0.012	0.028	0.30	0.71	
N	0.016	0.028	0.40	0.71	

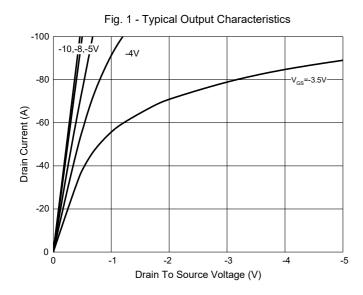


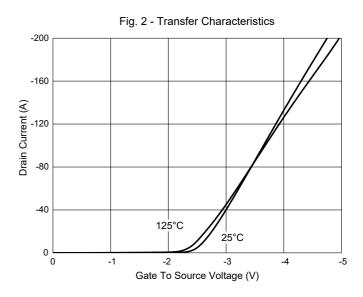
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

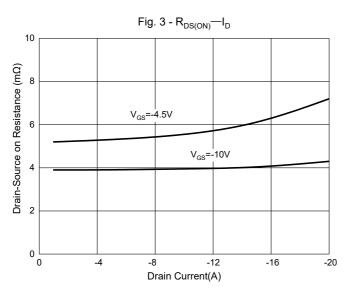
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics				1		I	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-30			V	
Gate-Source Leakage Current	I _{GSS}	V_{DS} =0V, V_{GS} =±25V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V			-1	μA	
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1.2	-1.8	-2.8	V	
Drain Cauras On Basistanas	R _{DS(on)}	V _{GS} =-10V, I _D =-20A		4	5.5	mΩ	
Drain-Source On-Resistance		V _{GS} =-4.5V, I _D =-20A		6	9.5	mΩ	
Gate Resistance	R _g	Drain open, f=1Mhz		6.5		Ω	
Diode Characteristics							
Continuous Body Diode Current	Is				-50	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-20A			-1.2	V	
Reverse Recovery Time	t _{rr}	I _s =-15A,di/dt=100A/μs		24		ns	
Reverse Recovery Charge	Q _{rr}	1813Α,α//αι-100Α/μ5		8.5		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			6464			
Output Capacitance	C _{oss}	V _{DS} =-15V,V _{GS} =0V,f=1MHz		779		pF	
Reverse Transfer Capacitance	C _{rss}			477		1	
Total Gate Charge	Qg			111.7			
Gate-Source Charge	Q _{gs}	V _{DS} =-15V,V _{GS} =-10V,I _D =-20A		21.1		nC	
Gate-Drain Charge	Q_{gd}			22.9			
Turn-On Delay Time	t _{d(on)}			15			
Turn-On Rise Time	t _r	V _{DS} =-15V, V _{GS} =-10V,		75		ns	
Turn-Off Delay Time	t _{d(off)}	$R_G=3\Omega$, $R_L=0.75\Omega$		130		113	
Turn-Off Fall Time	t _f			80			

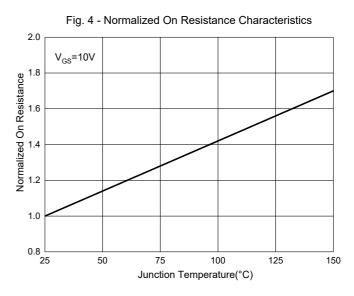


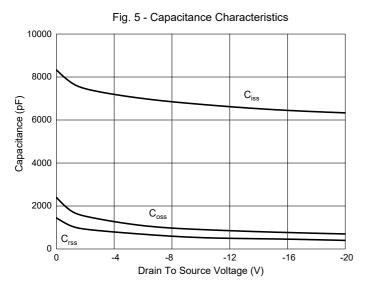
Curve Characteristics

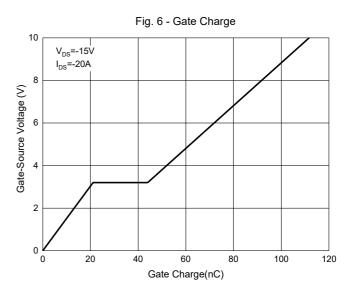














Curve Characteristics

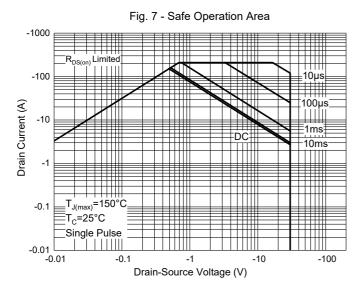
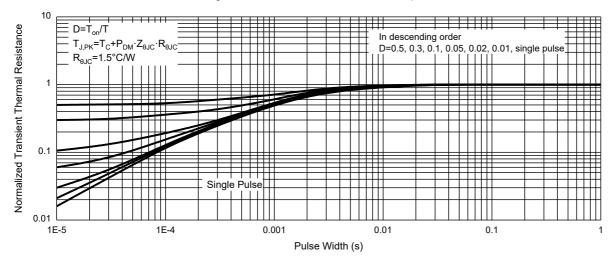


Fig. 8 - Normalized Transient Thermal Impedance





Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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