

Features

- Trench Power LV MOSFET Technology
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low R_{DS(ON)}
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- · Moisture Sensitivity Level 1

Maximum Ratings

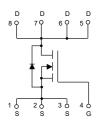
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 20°C/W Junction to Ambient⁽²⁾
- Thermal Resistance: 7.1°C/W Junction to Case

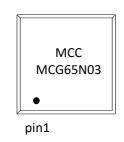
Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Volltage		V _{GS}	±20	V	
Continuous Drain Current	T _C =25°C		65		
	T _A =25°C	I _D	40	Α	
	T _A =70°C		32		
Pulsed Drain Current ⁽³⁾		I _{DM}	240	Α	
Total Power Dissipation ⁽⁴⁾		P _D	75	W	
Single Pulsed Avalanche Energy ⁽⁵⁾		E _{AS}	400	mJ	

Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{\theta JA}$ is measured with the device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.
- 3. Pulse Test: Pulse Width≤300us, Duty cycle ≤2%.
- 4. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- 5. T_J =25°C, V_{DD} =20V, V_G =10V, L=2.0mH, R_g =25 Ω .

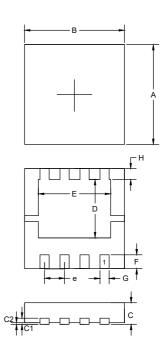
Internal Structure and Marking Code





N-CHANNEL MOSFET

DFN3333



DIMENSIONS					
DIM INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.126	0.130	3.20	3.30	
В	0.126	0.130	3.20	3.30	
С	0.030	0.033	0.75	0.85	
C1	0.007	0.009	0.18	0.22	
C2		0.002		0.05	
D	0.071	0.079	1.80	2.00	
Е	0.087	0.098	2.20	2.50	
F	0.016	0.020	0.40	0.50	
G	0.010	0.014	0.25	0.35	
Н	0.012	0.016	0.30	0.40	
е	0.024	0.028	0.60	0.70	

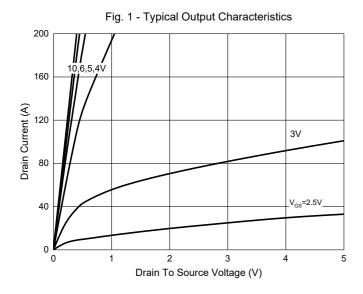


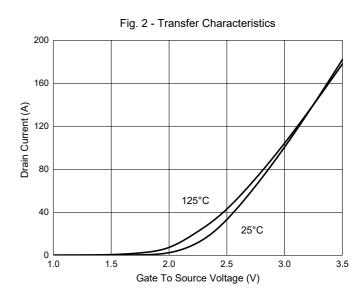
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

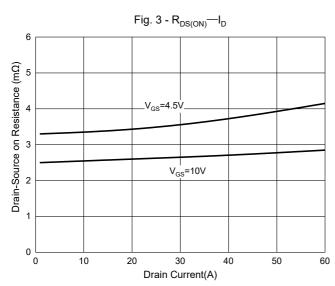
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics			-			I	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	30			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.5	2.5	V	
Drain-Source On-Resistance	Б	V _{GS} =10V, I _D =20A		2.6	3.2	mΩ	
	$R_{DS(on)}$	V _{GS} =4.5V, I _D =20A		3.4	4	mΩ	
Gate Resistance	R _g	F=1 MHz, Open drain		2.9		Ω	
Diode Characteristics							
Continuous Body Diode Current	Is				65	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.2	V	
Reverse Recovery Time	t _{rr}	L 00A II / II 500A/		15		ns	
Reverse Recovery Charge	Q _{rr}	I _F =20A, dI _F /dt=500A/μs		3		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			4498			
Output Capacitance	C _{oss}	V_{DS} =15V, V_{GS} =0V,f=1MHz		800		pF	
Reverse Transfer Capacitance	C _{rss}			643		1	
Total Gate Charge	Qg			92.7			
Gate-Source Charge	Q _{gs}	V _{DS} =15V,V _{GS} =10V,I _D =20A		13.5		nC	
Gate-Drain Charge	Q_{gd}			22.8			
Turn-On Delay Time	t _{d(on)}			11			
Turn-On Rise Time	t _r	V _{DS} =20V, V _{GS} =10V,		80		no	
Turn-Off Delay Time	t _{d(off)}	$R_G=3\Omega$, $R_L=0.75\Omega$, $I_{DS}=4A$		39		ns	
Turn-Off Fall Time	t _f			92			

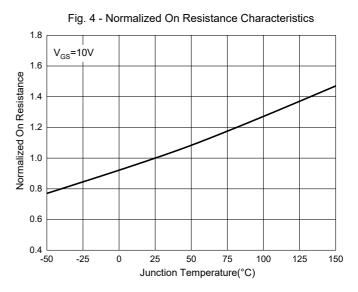


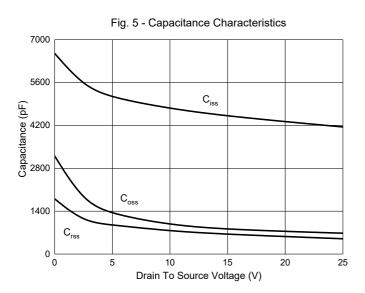
Curve Characteristics

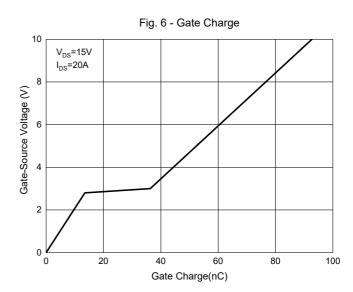














Curve Characteristics

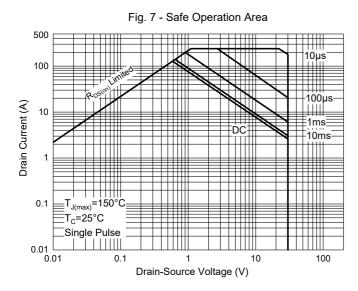
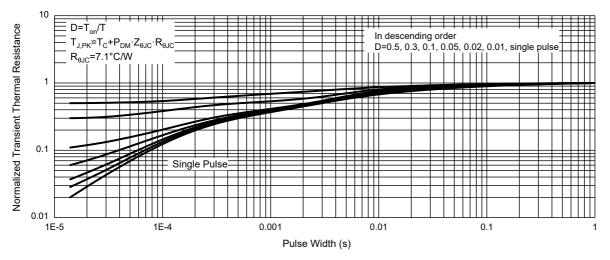


Fig. 8 - Normalized Transient Thermal Impedance





Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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