

Features

- Voltage Controlled Small Signal Switch
- Surface Mount Package
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

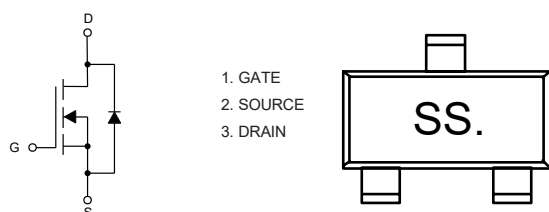
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 357°C/W Junction to Ambient (Note 2)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	50	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current	T _A =25°C	I _D	0.22	A
	T _A =100°C		0.14	
Pulsed Drain Current ^(Note3)		I _{DM}	0.88	A
Total Power Dissipation ^(Note4)		P _D	0.35	W

Note:

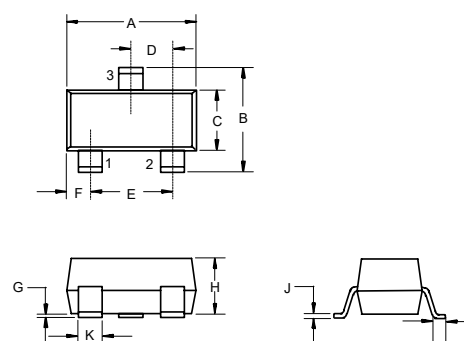
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of RθJA is measured with the device mounted on the minimum recommend pad size, in the still air environment with $T_A = 25^\circ\text{C}$.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-ambient thermal resistance.

Internal Structure and Marking Code



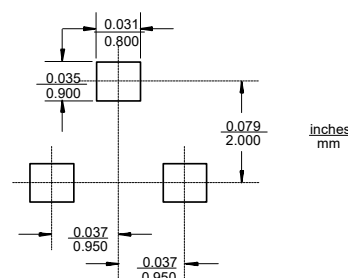
N-Channel MOSFET

SOT-23



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.110	0.120	2.80	3.04	
B	0.083	0.104	2.10	2.64	
C	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
E	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
H	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	

Suggested Solder Pad Layout



ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	50			V
Gate-Threshold Voltage ^(Note5)	V _{GS(th)}	V _{DS} =V _{GS} , I _D =1mA	0.8	1.1	1.5	V
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =50V, V _{GS} =0V			100	nA
Drain-Source On-Resistance ^(Note5)	R _{DS(on)}	V _{GS} =10V, I _D =0.3A		0.9	2.5	Ω
		V _{GS} =4.5V, I _D =0.2A		1.05	3	
Forward Transconductance ^(Note5)	g _{FS}	V _{DS} =10V, I _D =0.22A	120			mS
Gate Resistance	R _g	f=1 MHz, Open drain		4.2		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				0.22	A
Diode Forward Voltage ^(Note5)	V _{SD}	V _{GS} =0V, I _S =0.44A			1.4	V
Reverse Recovery Time	t _{rr}	I _F =300mA, dI _F /dt=100A/μs		12.2		ns
Reverse Recovery Charge	Q _{rr}			2.6		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1MHz		27	60	pF
Output Capacitance	C _{oss}			3	10	
Reverse Transfer Capacitance	C _{rss}			2	6	
Total Gate Charge	Q _g	V _{DS} =25V, V _{GS} =10V, I _D =0.3A		1.65		nC
Gate-Source Charge	Q _{gs}			0.24		
Gate-Drain Charge	Q _{gd}			0.4		
Turn-On Delay Time	t _{d(on)}	V _{DD} =30V, V _{GS} =10V, R _G =6Ω, I _D =0.29A			5	ns
Turn-On Rise Time	t _r				18	
Turn-Off Delay Time	t _{d(off)}				36	
Turn-Off Fall Time	t _f				73	

Note:

5.Pulse Test : Pulse Width=300 μ s, Duty Cycles $\leq$ 2%.

Curve Characteristics

Fig. 1 - Typical Output Characteristics

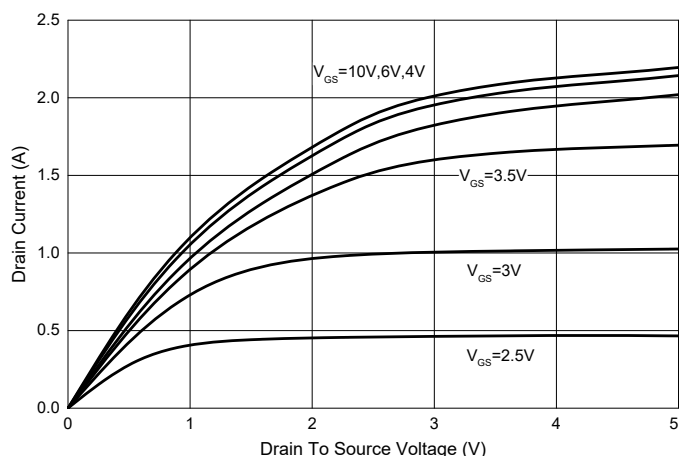


Fig. 2 - Transfer Characteristics

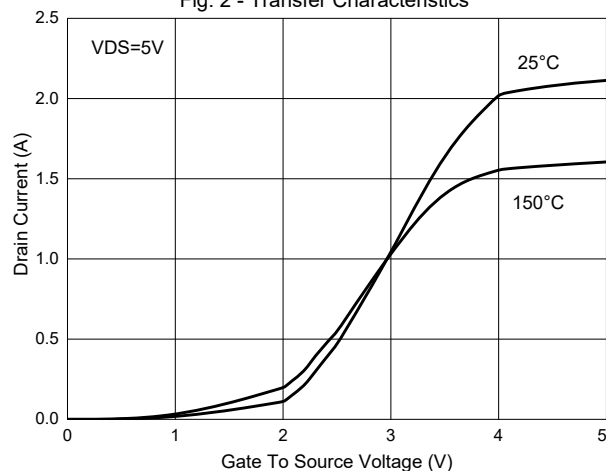


Fig. 3 - $R_{DS(ON)} - I_D$

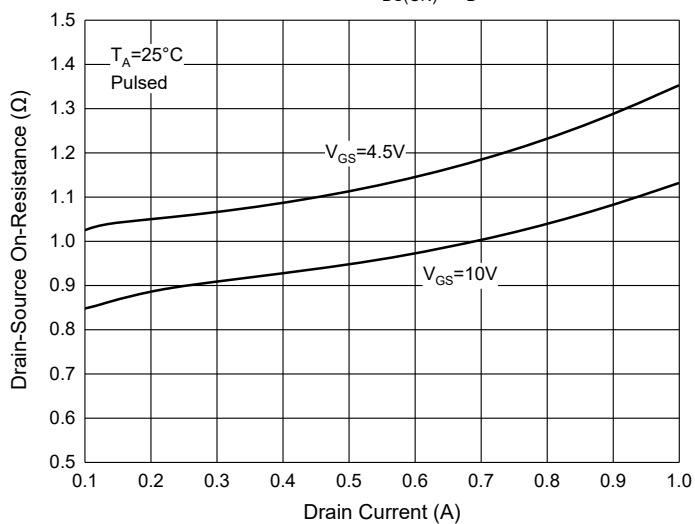


Fig. 4 - $R_{DS(ON)} - V_{GS}$

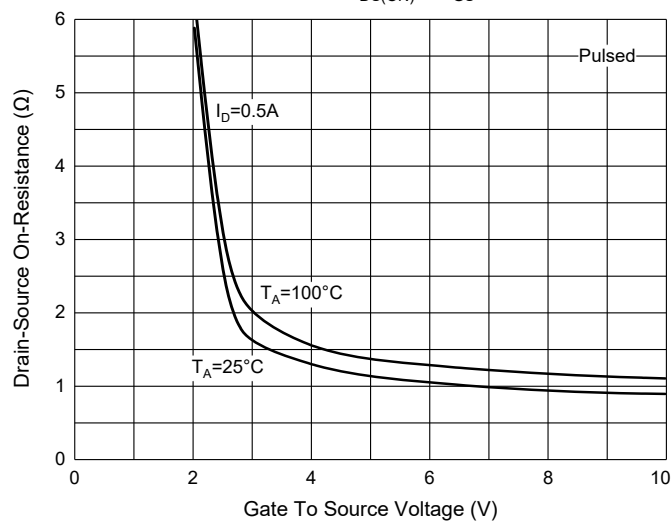


Fig. 5 - $I_S - V_{SD}$

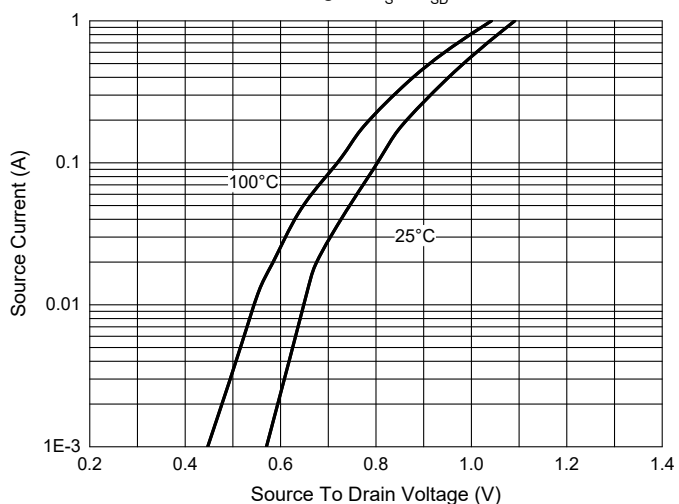


Fig. 6 - Threshold Voltage

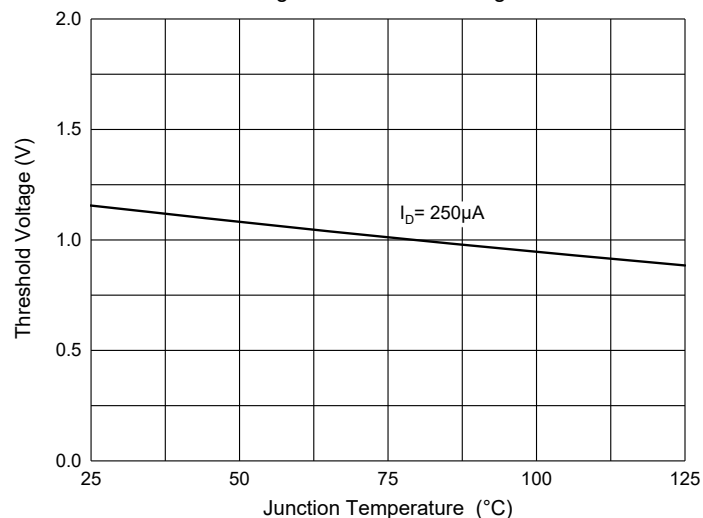


Fig. 7 - Normalized On Resistance Characteristics

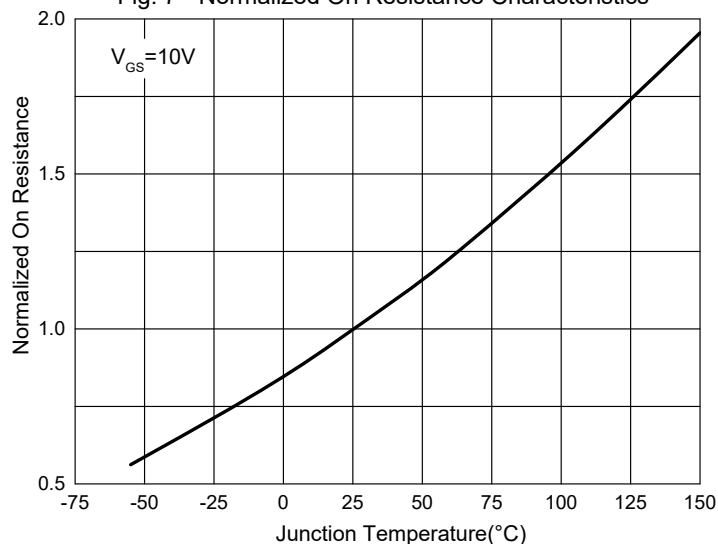


Fig. 8 - Gate Charge

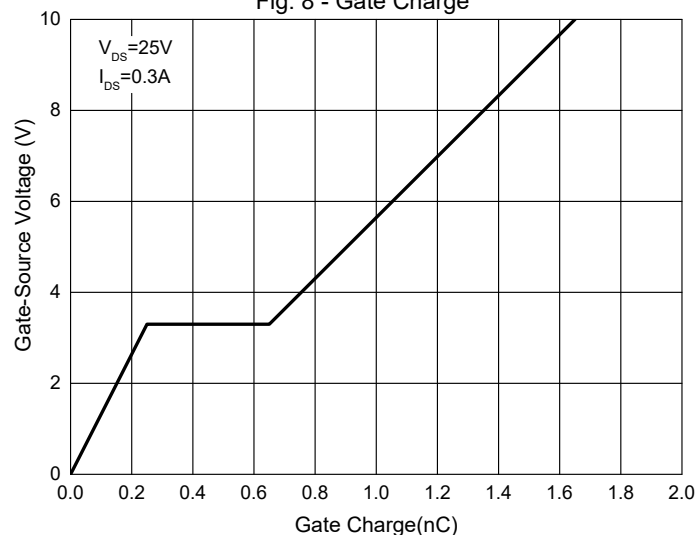


Fig. 9 - Capacitance Characteristics

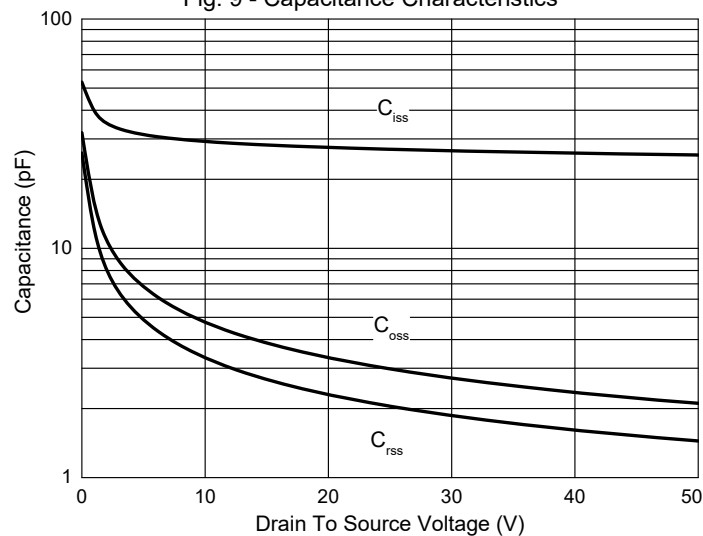


Fig. 10 - Current dissipation

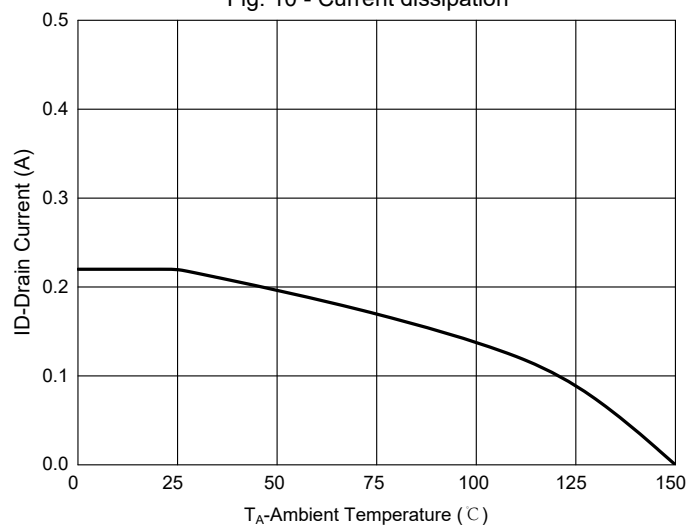


Fig. 11 - PD—TJ

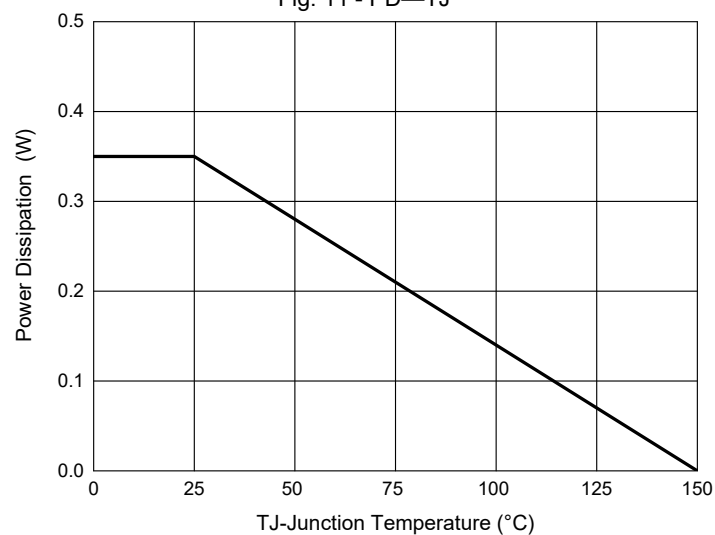


Fig. 12 - Safe Operation Area

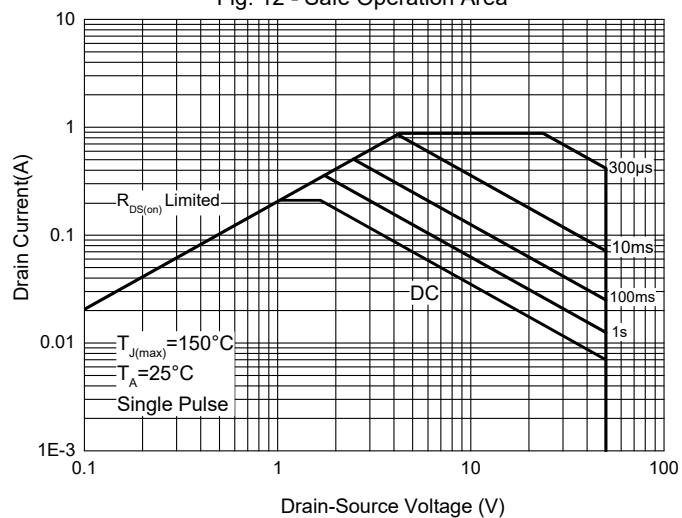
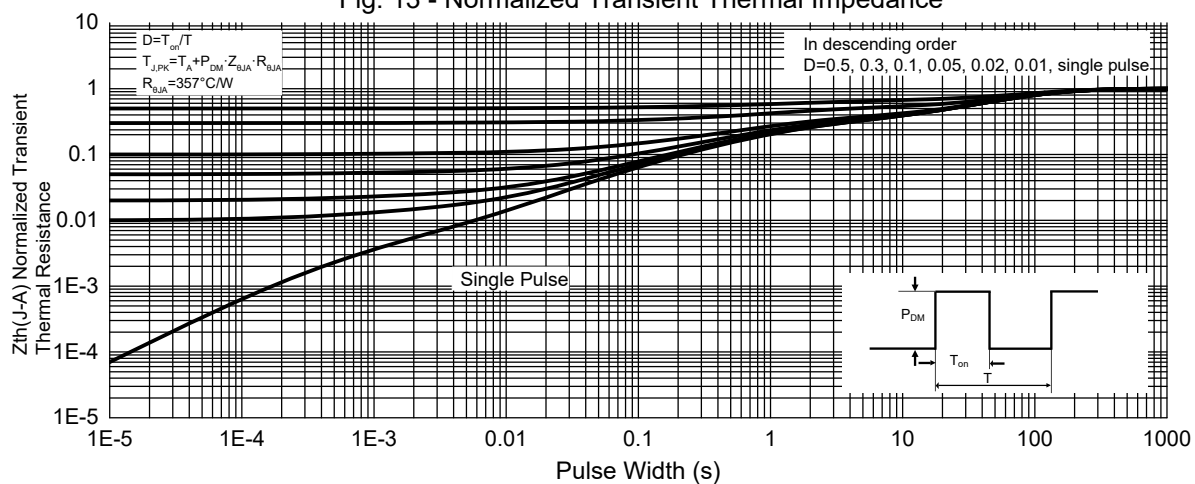


Fig. 13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel
Part Number-13P	Tape&Reel: 10Kpcs/Reel

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