

### Features

- Excellent Stability and Uniformity
- Split Gate Trench Mosfet Technology
- Lower  $R_{DS(ON)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

### Maximum Ratings

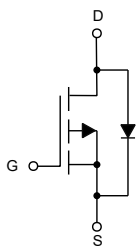
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 1°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain -Source Voltage	$V_{DS}$	-60	V
Gate -Source Voltage	$V_{GS}$	±18	V
Drain Current-Continuous	$I_D$	$T_C=25^\circ C$	-110
		$T_C=100^\circ C$	-69
Drain Current-Pulse <sup>(Note2)</sup>	$I_{DM}$	-320	A
Power Dissipation	$P_D$	125	W
Single Pulsed Avalanche Energy <sup>(Note3)</sup>	$E_{AS}$	729	mJ

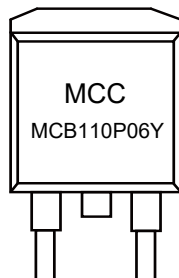
**Notes:**

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. Pulse Width Limited by Maximum Junction Temperature.
3. EAS Condition:  $T_J=25^\circ C$ ,  $V_{DD}=-40V, V_G=-10V, R_g=25\Omega, L=2mH, I_{AS}=-27A$ .

### Internal Structure and Marking Code

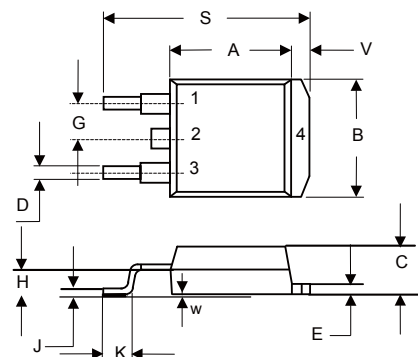


1.GATE  
2.DRAIN  
3.SOURCE



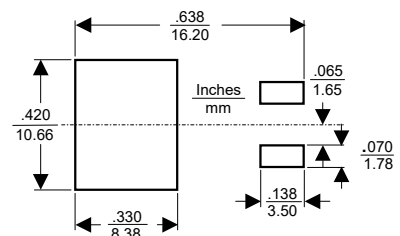
## P-CHANNEL MOSFET

### D2-PAK



DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	0.331	0.370	8.40	9.40	
B	0.378	0.417	9.60	10.60	
C	0.165	0.189	4.20	4.80	
D	0.027	0.037	0.68	0.94	
E	0.045	0.055	1.14	1.40	
G	0.010		2.54		TYP.
H	0.096	0.134	2.43	3.40	
J	0.011	0.025	0.28	0.64	
K	0.071	0.131	1.80	3.32	
S	0.575	0.625	14.60	15.87	
V	0.042	0.058	1.07	1.47	
W	0.000	0.010	0.00	0.25	

### Suggested Solder Pad Layout



**Electrical Characteristics @ 25°C (Unless Otherwise Noted)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-60			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 18V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-60V, V_{GS}=0V$			-1	$\mu A$
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-2.0	-2.6	-3.5	V
Drain-Source On-Resistance <sup>(Note3)</sup>	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-20A$		6.2	8.4	m $\Omega$
		$V_{GS}=-6V, I_D=-20A$		7.4	10	
Gate resistance	$R_G$	$V_{GS}=0V, f=1MHz$		8.3		$\Omega$
Body Diode Voltage	$V_{SD}$	$I_{SD}=-20A, V_{GS}=0V$			-1.2	V
<b>Dynamic Characteristics <sup>(Note 4)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS}=-30V, V_{GS}=0V, f=1MHz$		5810		pF
Output Capacitance	$C_{oss}$			935		
Reverse Transfer Capacitance	$C_{rss}$			79		
Total Gate Charge	$Q_g$	$V_{DS}=-30V, V_{GS}=-10V, I_D=-3A$		88		nC
Gate-Source Charge	$Q_{gs}$			25		
Gate-Drain Charge	$Q_{gd}$			15		
Reverse Recovery Charge	$Q_{rr}$	$I_F=-40A, di/dt=100A/\mu s$		84		
Reverse Recovery Time	$t_{rr}$			57		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=-30V, I_D=-3A, V_{GS}=-10V, R_G=6\Omega$		12		ns
Turn-On Rise Time	$t_r$			24		
Turn-Off Delay Time	$t_{d(off)}$			174		
Turn-Off Fall Time	$t_f$			81		

Note 3. Pulse Test : Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .

4. Guaranteed by Design, Not Subject to Production Testing.

Fig. 1 - Typical Output Characteristics

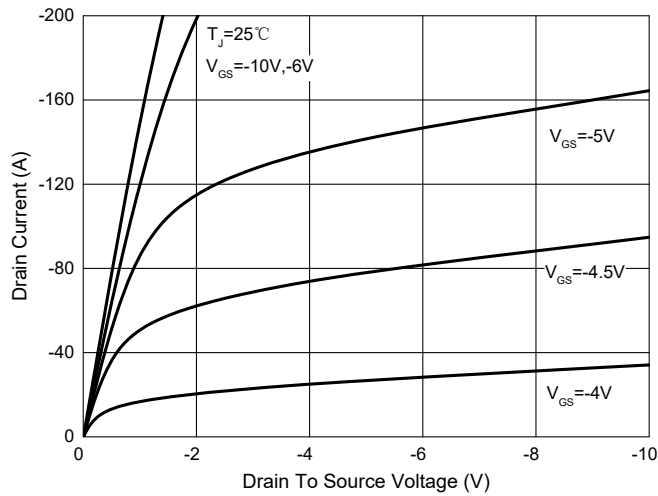


Fig. 2 - Transfer Characteristics

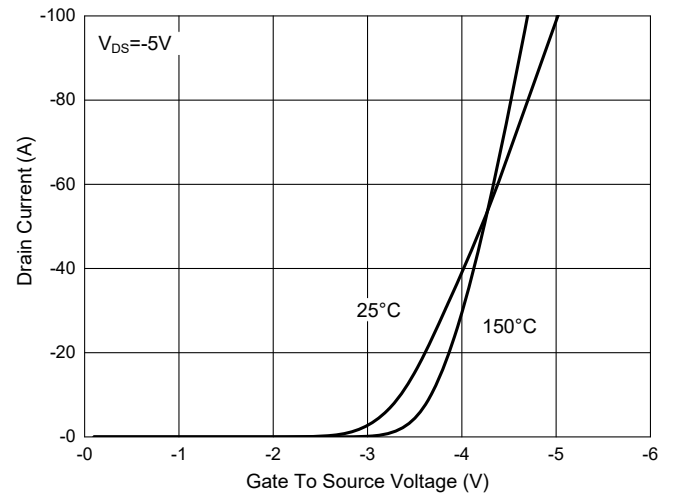


Fig. 3 -  $R_{DS(ON)} - V_{GS}$

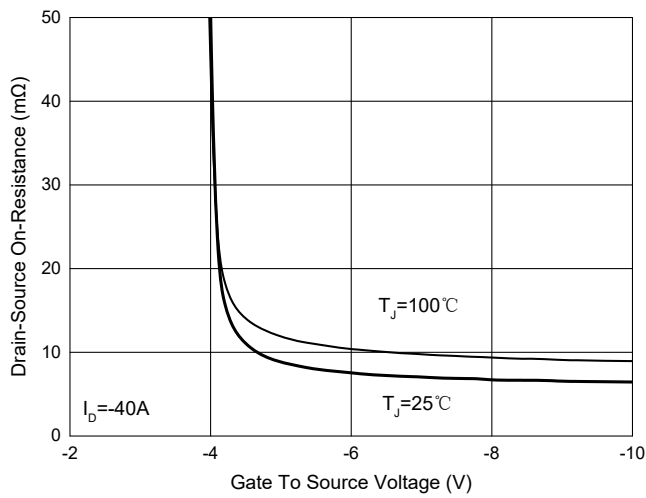


Fig. 4 - Normalized On Resistance Characteristics

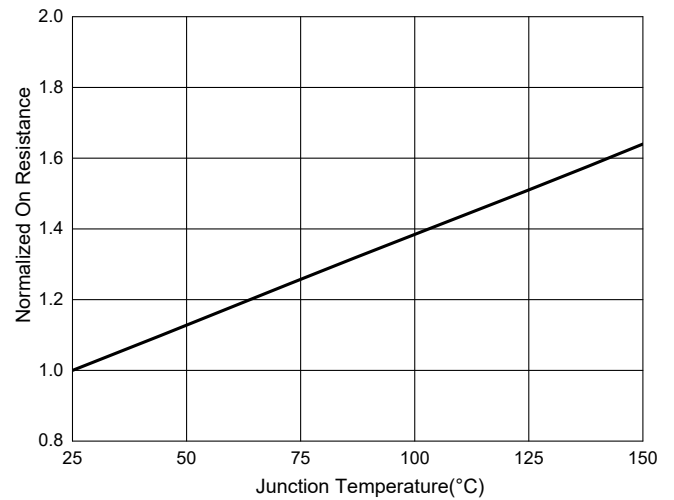


Fig. 5 - Capacitance Characteristics

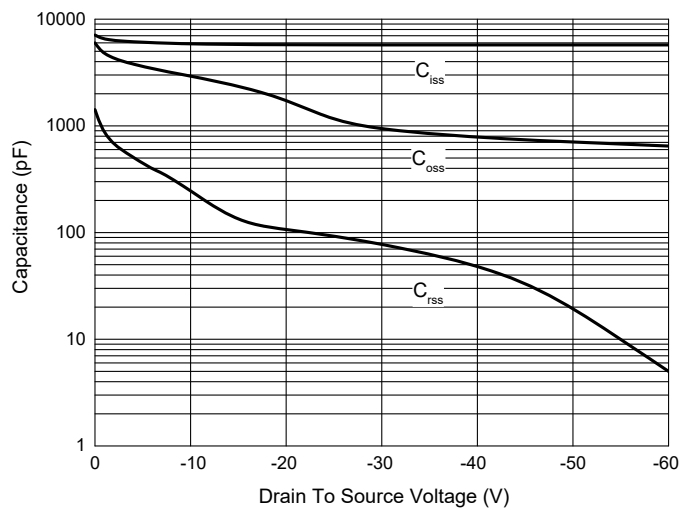


Fig. 6 - Gate Charge

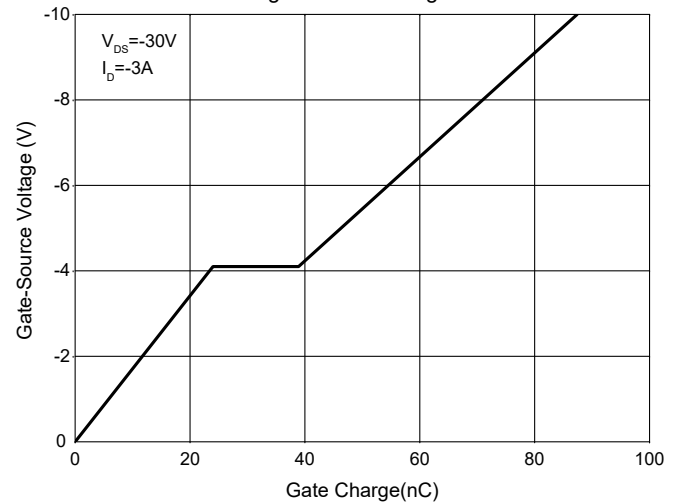


Fig. 7 -  $I_s - V_{SD}$

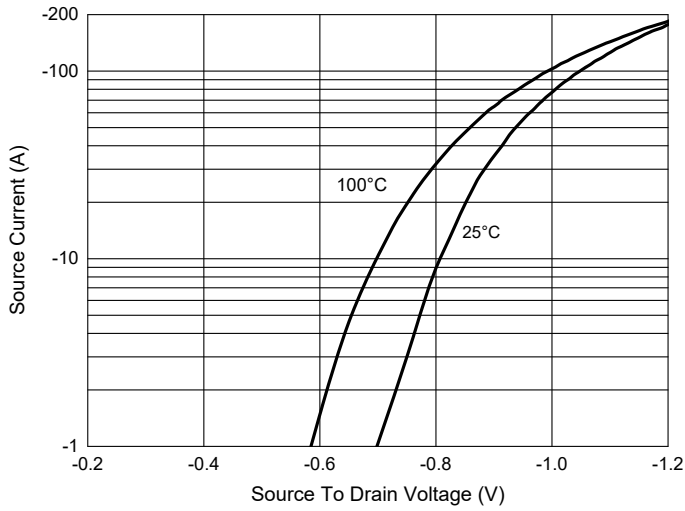


Fig. 8 - Safe Operation Area

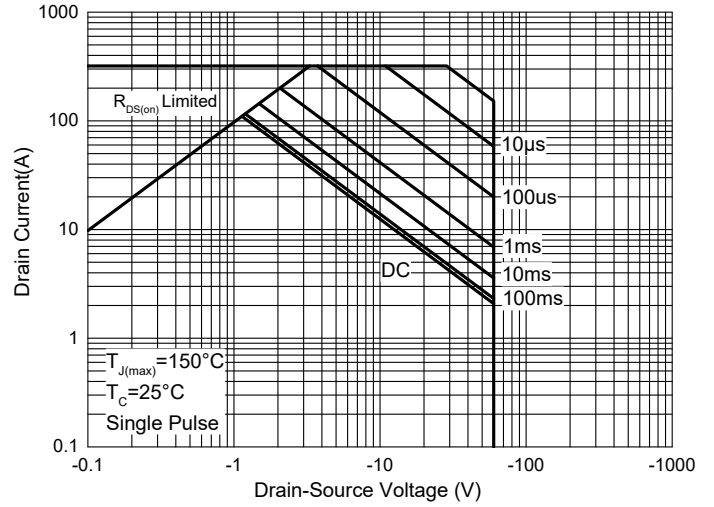
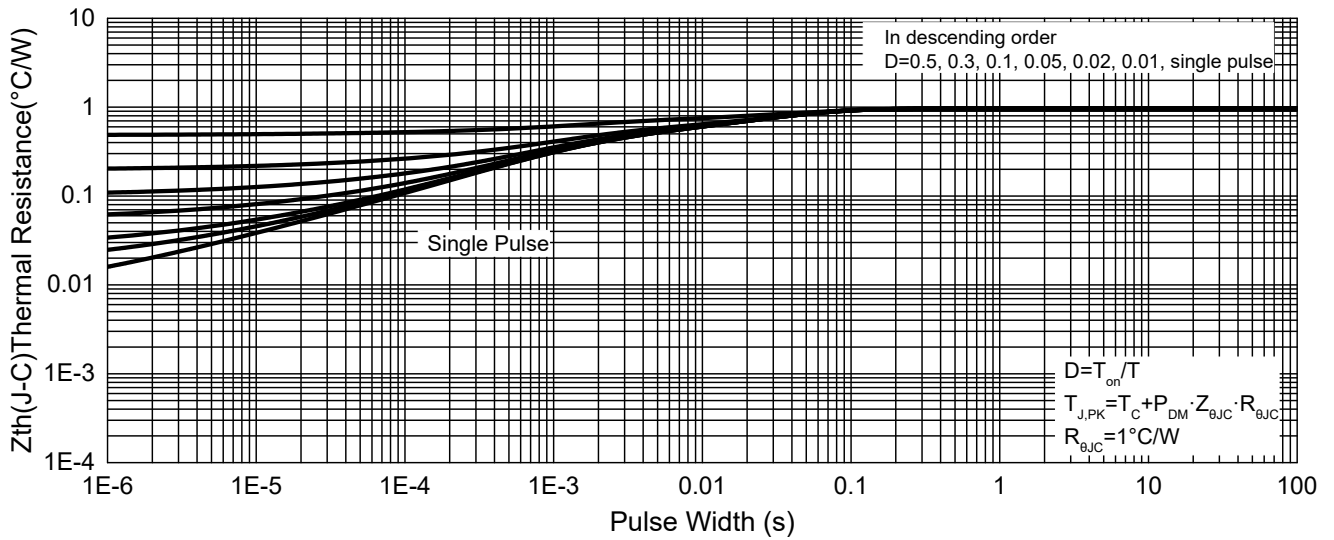


Fig. 9 - Maximum Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 800pcs/Reel

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