

Features

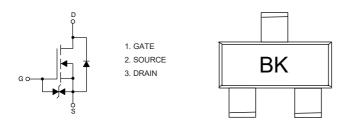
- ESD Protected up to 2KV (HBM)
- High Dense Cell Design For Extremely Low RDS(ON)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- · Halogen Free. "Green" Device
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

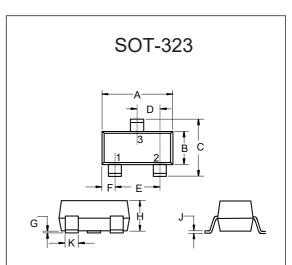
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 500°C/W Junction to Ambient

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	50	V
Gate-Source Volltage	V _{GS}	±20	V
Continuous Drain Current	I _D	0.37	Α
Pulsed Drain Current	I _{DM}	1.48	Α
Total Power Dissipation T _A =25°C	P _D	0.25	W

Internal Structure and Marking Code

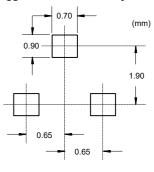


N-CHANNEL MOSFET



DIMENSIONS						
INCHES		MM		NOTE		
MIN	MAX	MIN	MAX	NOTE		
0.071	0.087	1.80	2.20			
0.045	0.053	1.15	1.35			
0.083	0.096	2.10	2.45			
0.026		0.65		TYP.		
0.047	0.055	1.20	1.40			
0.012	0.016	0.30	0.40			
0.000	0.004	0.00	0.10			
0.035	0.044	0.90	1.10			
0.002	0.010	0.05	0.25			
0.006	0.016	0.15	0.40			
	MIN 0.071 0.045 0.083 0.0 0.047 0.012 0.000 0.035 0.002	INCHES MIN MAX 0.071 0.087 0.045 0.053 0.083 0.096 0.026 0.047 0.055 0.012 0.016 0.000 0.004 0.035 0.044 0.002 0.010	INCHES M MIN 0.071 0.087 1.80 0.045 0.053 1.15 0.083 0.096 2.10 0.026 0.047 0.055 1.20 0.012 0.016 0.30 0.000 0.004 0.00 0.035 0.044 0.90 0.002 0.010 0.05	INCHES MM MIN MAX MIN MAX 0.071 0.087 1.80 2.20 0.045 0.053 1.15 1.35 0.083 0.096 2.10 2.45 0.026 0.65 0.047 0.055 1.20 1.40 0.012 0.016 0.30 0.40 0.000 0.004 0.00 0.10 0.035 0.044 0.90 1.10 0.002 0.010 0.05 0.25		

Suggested Solder Pad Layout



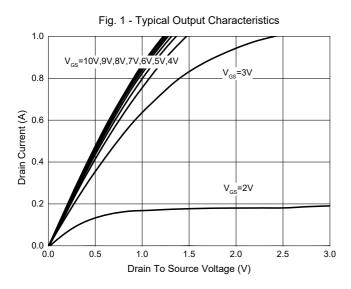


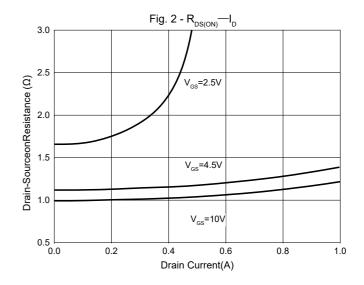
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

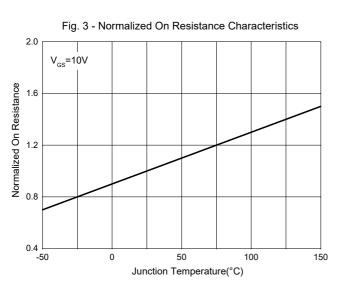
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics				1	1		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	50			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±10	μA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =50V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.5		1.5	V	
Drain-Source On-Resistance		V _{GS} =10V, I _D =0.3A		1	1.5	Ω	
	R _{DS(on)}	V _{GS} =4.5V, I _D =0.2A		1.1	2.3		
		V _{GS} =2.5V, I _D =0.1A		1.6	4.1		
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =0.3A			1.4	V	
Dynamic Characteristics							
Input Capacitance	C _{iss}			58		pF	
Output Capacitance	C _{oss}	V_{DS} =30V, V_{GS} =0V, f =1MHz		16			
Reverse Transfer Capacitance	C _{rss}			9.4			
Total Gate Charge	Q_g			1.5			
Gate-Source Charge	Q _{gs}	$V_{DS}=30V, V_{GS}=10V, I_{D}=0.3A$		0.3		nC	
Gate-Drain Charge	Q_{gd}			0.2			
Turn-On Delay Time	t _{d(on)}			2.5			
Turn-On Rise Time	t _r	V_{DS} =30V, V_{GEN} =10V, R_{G} =3.9 Ω , R_{L} =100 Ω ,		2			
Turn-Off Delay Time	t _{d(off)}	R_{G} =3.9Ω, R_{L} =100Ω, I_{DS} =0.3A		9.2		ns	
Turn-Off Fall Time	t _f			7.7			

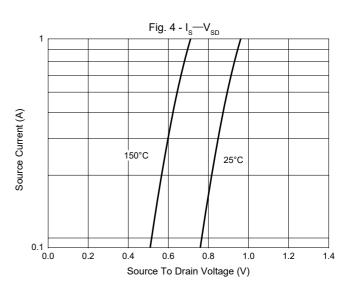


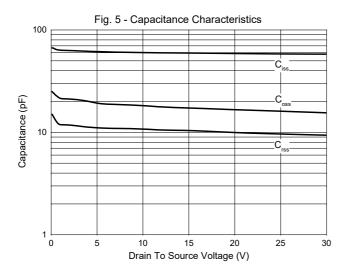
Curve Characteristics

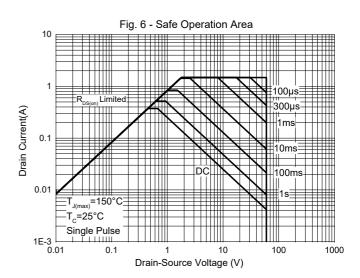














Curve Characteristics

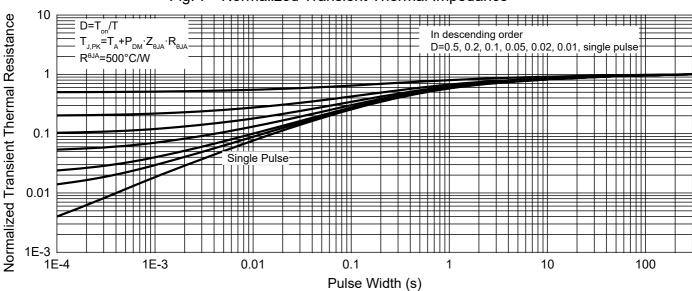


Fig. 7 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 3Kpcs/Reel	

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