

#### **Features**

- ESD Protected up to 2KV (HBM)
- · Operated at Low Logic Level Gate Drive
- P-Channel Switch with Low R<sub>DS(on)</sub>
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

# **Maximum Ratings**

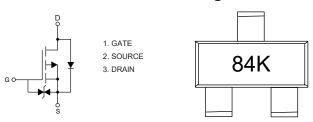
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Maximum Thermal Resistance: 461°C/W Junction to Ambient

Parameter		Symbol	Rating	Unit	
Drain -source Voltage	V <sub>DS</sub>	-60	V		
Gate -Source Voltage		V <sub>GS</sub>	±20	V	
Continuous Drain Current	T <sub>A</sub> =25°C	I <sub>D</sub>	-0.26	А	
	T <sub>A</sub> =100°C		-0.16	Α	
Plused Drain Current (Note2)		I <sub>DM</sub>	-1.04	А	
Power Dissipation <sup>(Note3)</sup>	P <sub>D</sub>	0.27	W		

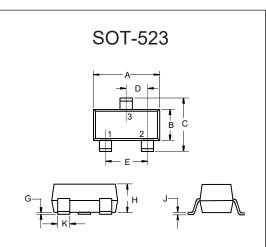
Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

- 2. Repetitive rating: Pulse width limited by junction temperature.
- 3. Surface mounted on FR4 board, t≤10s.

# **Internal Structure and Marking Code**

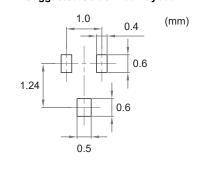


# **P-Channel MOSFET**



DIMENSIONS					
DIM INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.059	0.067	1.50	1.70	
В	0.030	0.033	0.75	0.85	
С	0.057	0.069	1.45	1.75	
D	0.020		0.50		TYP.
Е	0.035	0.043	0.90	1.10	
G	0.000	0.004	0.00	0.10	
Н	0.024	0.031	0.60	0.80	
J	0.004	0.008	0.10	0.20	
K	0.006	0.014	0.15	0.35	

### Suggested Solder Pad Layout





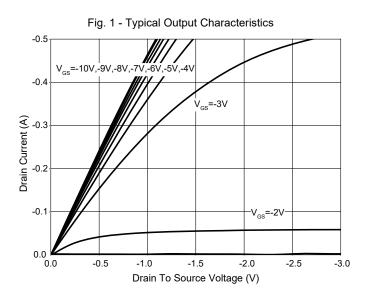
# **ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)**

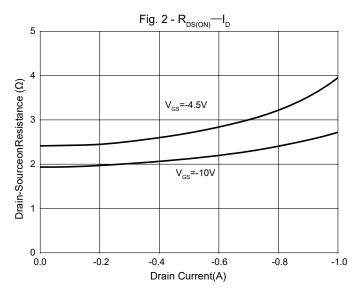
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit	
Static Characteristics	1		l	1	I		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-60			V	
Gate-Threshold Voltage <sup>(Note 4)</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1		-2	V	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±10	μΑ	
Drain Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V			-1	μA	
Drain-Source On-Resistance(Note 4)	Real	V <sub>GS</sub> =-10V, I <sub>D</sub> =-0.2A		2.2	6	Ω	
	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.1A	2.5 7		7		
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-0.2A			-1.3	V	
Dynamic Characteristics <sup>(Note 4)</sup>	)		•				
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, f=1MHz		32		pF	
Output Capacitance	C <sub>oss</sub>			2.3			
Reverse Transfer Capacitance	C <sub>rss</sub>			1.6			
Total Gate Charge	$Q_g$			1.6			
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ =-30V, $V_{GS}$ =-10V, $I_{D}$ =-0.2A		0.4		nC	
Gate-Drain Charge	$Q_{gd}$			0.2			
Switching Characteristics <sup>(Note</sup>	5)						
Turn-On Delay Time	t <sub>d(on)</sub>			5.5			
Turn-On Rise Time	t <sub>r</sub>	V <sub>DS</sub> =-30V, V <sub>GEN</sub> =-10 V,		4.8			
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G$ =3.9 $\Omega$ , $R_L$ =150 $\Omega$ , $I_{DS}$ =-0.2 $A$		27		- ns	
Turn-Off Fall Time	t <sub>f</sub>			19			

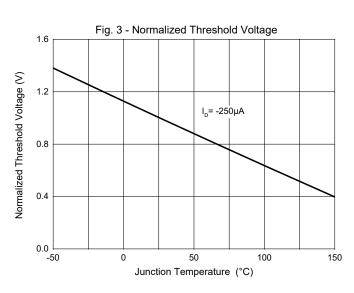
Note: 4. Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%. 5. Guaranteed by design, not subject to production.

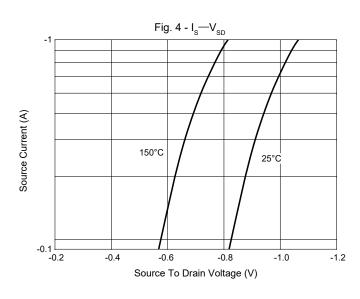


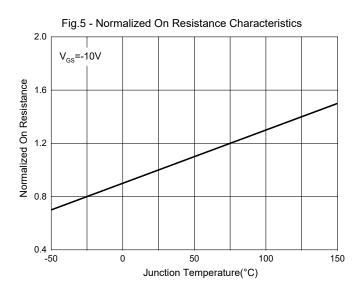
## **Curve Characteristics**

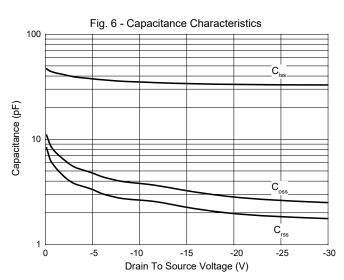






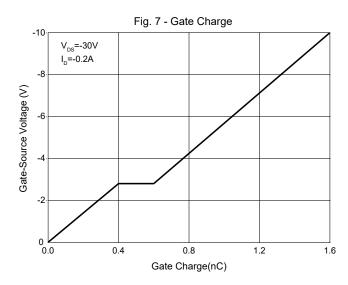








## **Curve Characteristics**



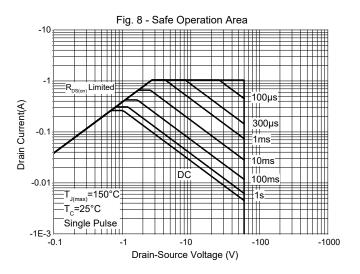
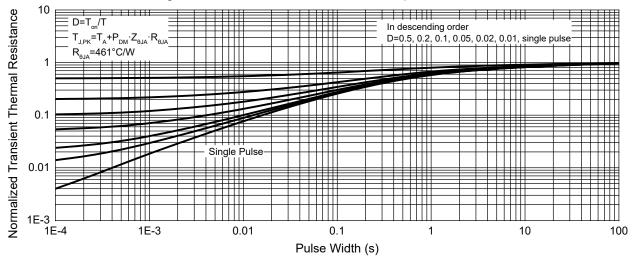


Fig. 9 - Normalized Transient Thermal Impedance





## **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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