

Features

- Epitaxial Planar Die Construction
- · Built-In Biasing Resistors
- Halogen Free. "Green" Device (Note 1)
- · Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant.See Ordering Information)

PNP Digital Transistor

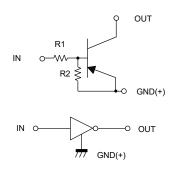
Maximum Ratings @ 25°C Unless Otherwise Specified

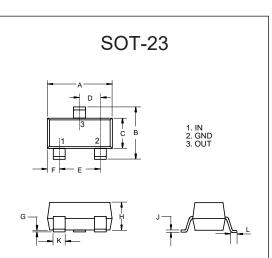
Parameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	V _{CC}		-50		V
Input Voltage	V _{IN}	-12		+5	V
Output Current	Io		-500		mA
Power Dissipation	P _D		200		mW
Junction Temperature	TJ			150	°C
Storage Temperature	T _{stg}	-55		150	°C

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Device Marking: 234

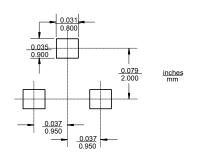
Internal Structure





DIMENSIONS						
DIM	INCI	HES	M	М	NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.110	0.120	2.80	3.04		
В	0.083	0.104	2.10	2.64		
С	0.047	0.055	1.20	1.40		
D	0.034	0.041	0.85	1.05		
Е	0.067	0.083	1.70	2.10		
F	0.018	0.024	0.45	0.60		
G	0.0004	0.006	0.01	0.15		
Н	0.035	0.043	0.90	1.10		
J	0.003	0.007	0.08	0.18		
K	0.012	0.020	0.30	0.51		
L	0.007	0.020	0.20	0.50		

Suggested Solder Pad Layout



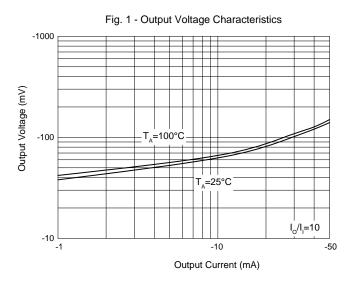


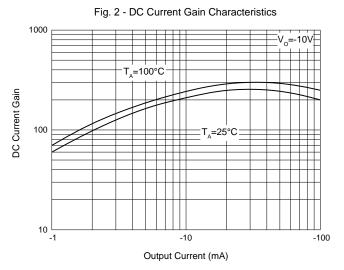
Electrical Characteristics @ 25°C Unless Otherwise Specified

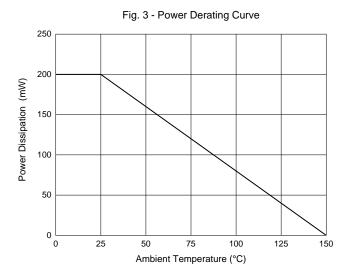
Parameter	Symbol	Min	Тур	Max	Unit	Conditions
Innut Voltage	$V_{I(off)}$	-0.5			V	V _{CC} =-5V, I _O =-100μA
Input Voltage	$V_{I(on)}$			-2	V	V _O =-0.3V, I _O =-20mA
Output Voltage	V _{O(on)}			-0.3	V	I _O =-10mA,I _I =-0.5mA
Input Current	I _I			-3.8	mA	V _I =-5V
Output Current	I _{O(off)}			-0.5	μA	V _{CC} =-50V, V _I =0
DC Current Gain	Gı	56				V _O =-5V, I _O =-50mA
Input Resistance	R ₁	1.54	2.2	2.86	ΚΩ	
Resistance Ratio	R ₂ /R ₁	3.6	4.5	5.5		
Transition Frequency	f _T		200		MHz	V _{CE} =-10V, I _E =5mA, f=100MHz



Curve Characteristics









Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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