

Features

- Split Gate Trench MOSFET Technology
- **Excellent Package for Heat Dissipation**
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

Maximum Ratings

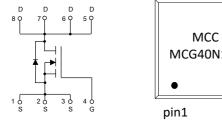
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 25°C/W Junction to Ambient(t≤10s)⁽²⁾
- Thermal Resistance: 55°C/W Junction to Ambient(Steady-State)⁽²⁾
- Thermal Resistance: 2.9°C/W Junction to Case(Steady-State)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Volltage	V _{GS}	±20	V
Continuous Drain Current	I _D	40	Α
Pulsed Drain Current ⁽³⁾	I _{DM}	160	А
Total Power Dissipation ⁽⁴⁾	P _D	43	W
Single Pulsed Avalanche Energy ⁽⁵⁾	E _{AS}	81	mJ

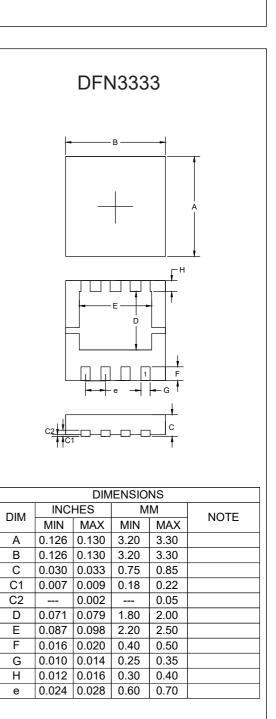
Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{R,IA}$ is measured with the device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^{\circ}C$. The Power dissipation P_{DSM} is based on $R_{\theta JA}$ t \leq 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_D is based on max. junction temperature, using junction-case thermal resistance.
- 5. T_J=25°C, V_{DD}=50V, R_G=25Ω, L=0.5mH.

Internal Structure and Marking Code







N-CHANNEL

MOSFET

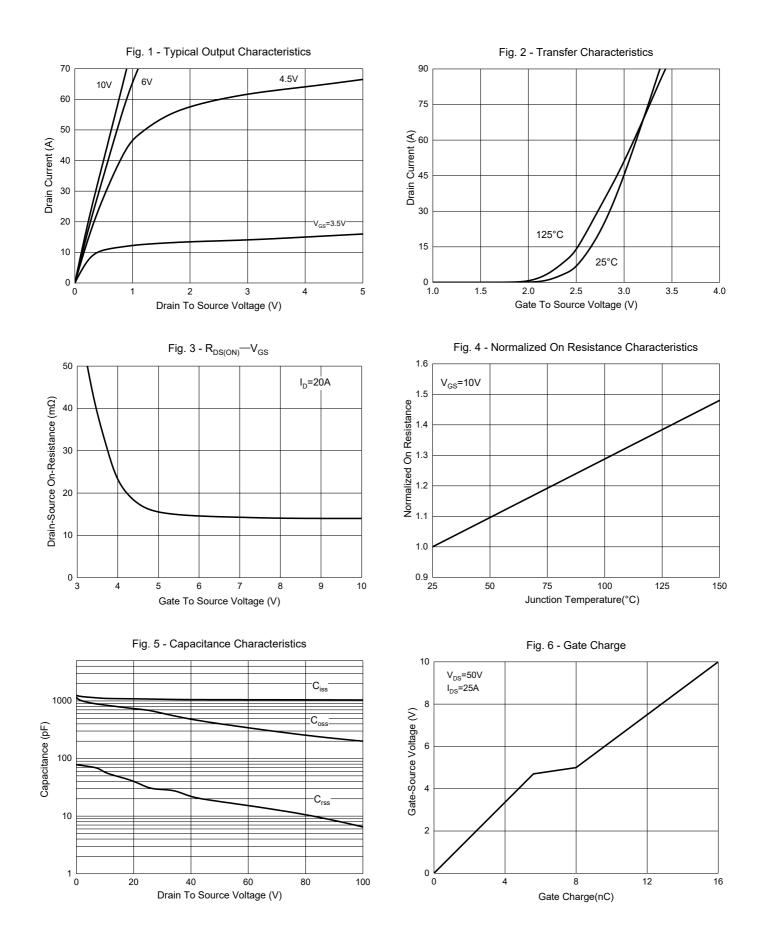


Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Мах	Unit	
Static Characteristics			1				
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250µA	100			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250µA	1	1.8	2.5	V	
Drain-Source On-Resistance	D	V _{GS} =10V, I _D =20A		15	18.5	mΩ	
	R _{DS(on)}	V _{GS} =4.5V, I _D =20A		18	22.5	mΩ	
Gate Resistance	R _g	F=1 MHz, Open drain		1		Ω	
Diode Characteristics							
Continuous Body Diode Current	I _S				40	А	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.3	V	
Reverse Recovery Time	t _{rr}			39.8		ns	
Reverse Recovery Charge	Q _{rr}	l _F =20A, dl _F /dt=100A/µs		42		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}	V _{DS} =50V,V _{GS} =0V,f=1MHz		1051			
Output Capacitance	C _{oss}			399		pF	
Reverse Transfer Capacitance	C _{rss}			18		1	
Total Gate Charge	Qg			16			
Gate-Source Charge	Q _{gs}	V _{DS} =50V,V _{GS} =10V,I _D =25A		5.6		nC	
Gate-Drain Charge	Q _{gd}			2.4			
Turn-On Delay Time	t _{d(on)}			39.2			
Turn-On Rise Time	t _r	V _{DS} =50V, V _{GS} =10V,		11		20	
Turn-Off Delay Time	t _{d(off)}	R _G =2.2Ω, I _{DS} =25A		53.2		ns	
Turn-Off Fall Time	t _f			15.8			



Curve Characteristics





Curve Characteristics

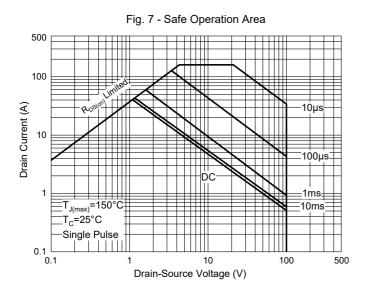
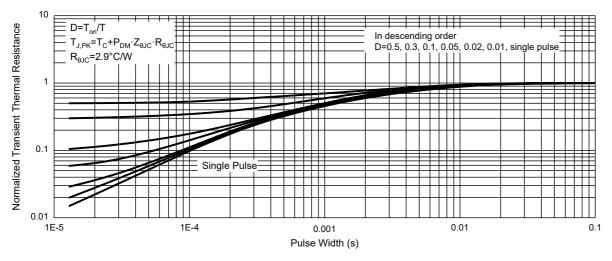


Fig. 8 - Normalized Transient Thermal Impedance





Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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