

#### **Features**

- Split Gate Trench MOSFET Technology
- · AEC-Q101 Qualified
- · Moisture Sensitivity Level 1
- · Extremely Low Switching Loss
- Fast Switching and Soft Recovery
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## **Maximum Ratings**

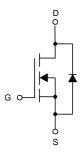
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 1.7°C/W Junction to Case (Steady-State)
- Thermal Resistance: 20°C/W Junction to Ambient (t≤10s)<sup>(1)</sup>
- Thermal Resistance: 50°C/W Junction to Ambient (Steady-State)<sup>(1)</sup>

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Volltage	$V_{GS}$	±20	V
Continuous Drain Current	I <sub>D</sub>	45	Α
Pulsed Drain Current <sup>(2)</sup>	I <sub>DM</sub>	180	Α
Total Power Dissipation <sup>(3)</sup>	P <sub>D</sub>	72	W
Single Pulsed Avalanche Energy <sup>(4)</sup>	E <sub>AS</sub>	81	mJ

#### Note:

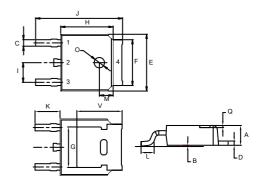
- 1. The value of  $R_{\Theta JA}$  is measured with the device mounted on  $1\text{in}^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A$ =25°C. The Power dissipation  $P_{DSM}$  is based on  $R_{\Theta JA}$  t≤10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 2. Repetitive rating; pulse width limited by max. junction temperature.
- 3.  $\mbox{P}_{\mbox{\scriptsize D}}$  is based on max. junction temperature, using junction-case thermal resistance.
- 4.  $V_{DD}$ =50V,  $R_{G}$ =25 $\Omega$ , L=0.5mH,  $I_{AS}$ =25A.

#### **Internal Structure**



# N-CHANNEL MOSFET

# DPAK(TO-252)



- 1. Gate
- 2,4. Drain
  - 3. Source

DIMENIOLONIO					
	DIMENSIONS				
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	NOTE
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
Е	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
Н	0.236	0.244	6.00	6.20	
ı	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
М	0.063		1.60		TYP.
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.3	35	TYP.

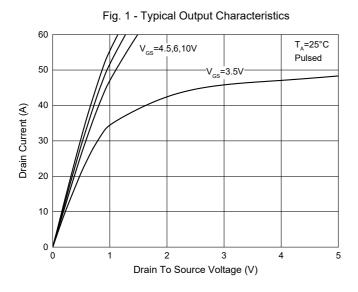


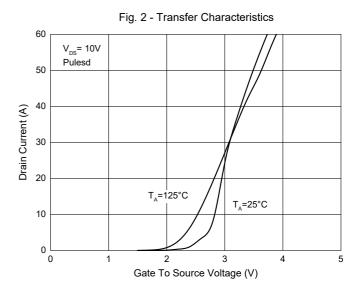
# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

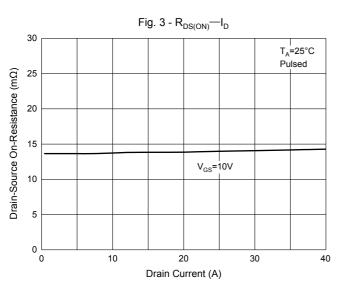
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics					I		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	μA	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.8	2.5	V	
Drain Course On Registeres	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		14	17	mΩ	
Drain-Source On-Resistance		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A		17	21.5	mΩ	
Gate Resistance	R <sub>g</sub>	f=1MHz, Open drain		1		Ω	
Diode Characteristics							
Continuous Body Diode Current	Is				40	А	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A			1.3	V	
Reverse Recovery Time	t <sub>rr</sub>	L 000 dl /dt-4000/		39.8		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>S</sub> =20A, dI <sub>F</sub> /dt=100A/μs		42		nC	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>			1135			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =50V, $V_{GS}$ =0V,f=1MHz		399		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			18			
Total Gate Charge	$Q_g$			16			
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =10V,I <sub>D</sub> =25A		5.6		nC	
Gate-Drain Charge	$Q_{gd}$			2.4			
Turn-On Delay Time	t <sub>d(on)</sub>			39.2			
Turn-On Rise Time	t <sub>r</sub>	$V_{GS}$ =10V, $V_{DD}$ =50V, $I_{D}$ =25A		11		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_{GEN}$ =2.2 $\Omega$		53.2		113	
Turn-Off Fall Time	t <sub>f</sub>			15.8			

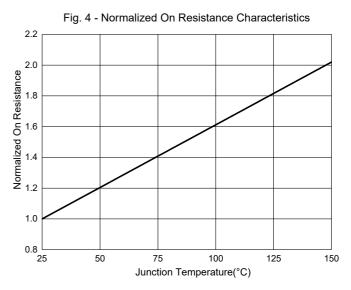


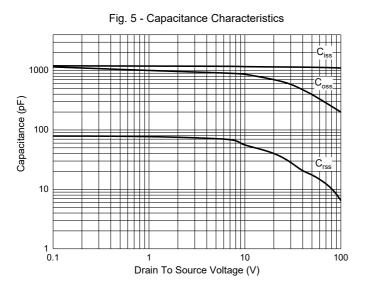
### **Curve Characteristics**

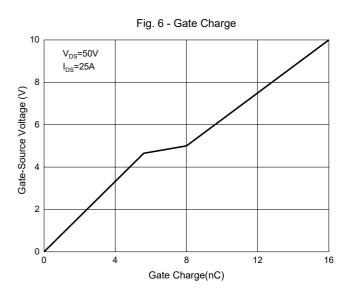






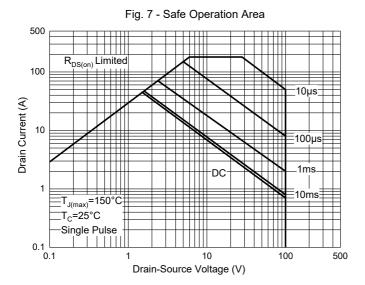








## **Curve Characteristics**





## **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

Note: Adding "-HF" Suffix for Halogen Free, eg. Part Number-TP-HF

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