

#### **Features**

- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)

# N-Channel Enhancement Mode Field Effect Transistor

# **Maximum Ratings**

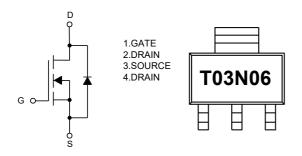
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 104°C/W Junction to Ambient(Note 2)

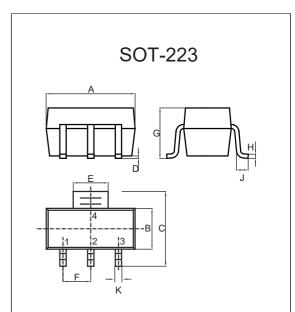
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Volltage	V <sub>GS</sub>	±20	V
Drain Current	I <sub>D</sub>	3.0	Α
Pulsed Drain Current <sup>(Note3)</sup>	I <sub>DM</sub>	10	Α
Total Power Dissipation	$P_D$	1.2	W

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

- 2. Surface mounted on FR4 board , t≤10s.
- 3. Repetitive rating: Pulse width limited by junction temperature.

# **Internal Structure and Marking Code**





DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	NOTE
Α	0.248	0.264	6.30	6.70	
В	0.130	0.146	3.30	3.70	
С	0.264	0.287	6.70	7.30	
D	0.001	0.004	0.02	0.10	
Е	0.114	0.122	2.90	3.10	
F	0.091		2.30		TYP.
G		0.071		1.80	
Н	0.009	0.014	0.23	0.35	
J	0.030		0.75		
K	0.026	0.033	0.66	0.84	



# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60			V
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	μΑ
Gate-Threshold Voltage(Note 4)	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	0.5		2.0	V
Drain-Source On-Resistance(Note 4)	В	V <sub>GS</sub> =10V, I <sub>D</sub> =3A			105	
	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A			125	mΩ
Diode Forward Voltage <sup>(Note 4)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =3A			1.2	V
Forward Tranconductance(Note 4)	<b>9</b> FS	V <sub>DS</sub> =15V, I <sub>D</sub> =2A	1.4			S
Dynamic Characteristics(Note 5)				1	1	
Input Capacitance	C <sub>iss</sub>			247		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =30V, $V_{GS}$ =0V,f =1MHz		34		
Reverse Transfer Capacitance	C <sub>rss</sub>			19.5		
Switching Characteristics(Note	5)		·			
Total Gate Charge	$Q_g$			6		
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ =30V, $V_{GS}$ =4.5V, $I_{D}$ =3A		1		nC
Gate-Drain Charge	$Q_{gd}$			1.3		
Turn-On Delay Time	t <sub>d(on)</sub>			6		
Turn-On Rise Time	t <sub>r</sub>	$V_{GS}$ =10V, $V_{DD}$ =30V, $I_{D}$ =1.5A,		15		no
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_{GEN}$ =1 $\Omega$		15		ns
Turn-Off Fall Time	t <sub>f</sub>			10		

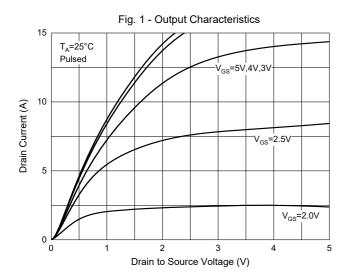
### Note:

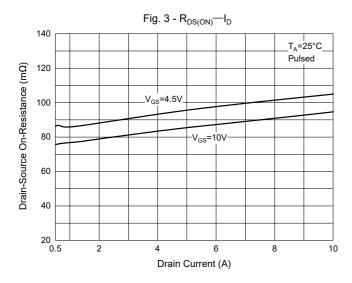
4. Pulse Test : Pulse Width≤300µs, Duty Cycle≤0.5%.

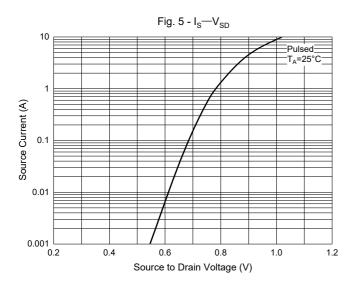
5. Guaranteed by design, not subject to producting.

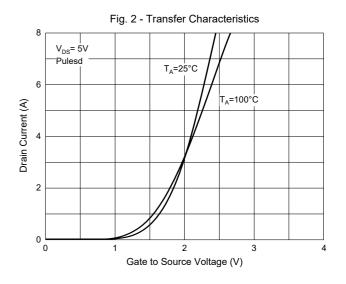


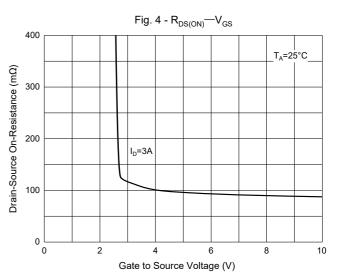
#### **Curve Characteristics**

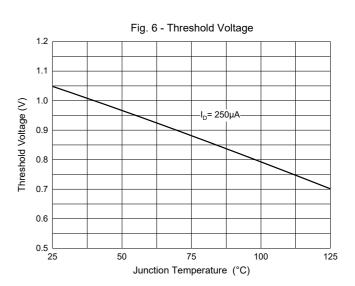














# **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

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