

Features

- Split Gate Trench MOSFET Technology
- High Density Cell Design for Low R_{DS(ON)}
- · Excellent Package for Heat Dissipation
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

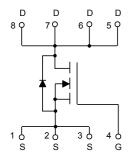
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 32°C/W Junction to Ambient(t≤10s)⁽²⁾
- Thermal Resistance: 60°C/W Junction to Ambient(Steady-State)⁽²⁾
- Thermal Resistance: 20°C/W Junction to Lead(Steady-State)

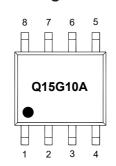
Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Volltage		V _{GS}	±20	V
Continuous Drain Current	T _A =25°C	1	15	Α
	T _A =100°C	– I _D	9.5	^
Pulsed Drain Current ⁽³⁾		I _{DM}	75	Α
Total Power Dissipation ⁽⁴⁾		P _D	2	W
Single Pulsed Avalanche Energy		E _{AS}	200	mJ

Note:

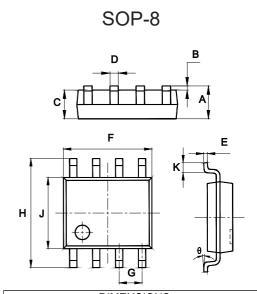
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2.The value of $R_{\theta JA}$ is measured with the device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with TA =25°C. The value in any given application depends on the user's specific board design.
- 3.Repetitive rating; pulse width limited by max. junction temperature.
- 4.PD is based on max. junction temperature, using Steady-State junction-ambient thermal resistance.

Internal Structure and Marking Code

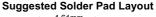


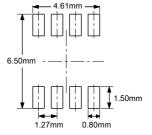


N-CHANNEL MOSFET



DIMENSIONS					
DIM	INC	INCHES		M	NOTE
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.053	0.069	1.35	1.75	
В	0.004	0.010	0.10	0.25	
С	0.053	0.061	1.35	1.55	
D	0.013	0.020	0.33	0.51	
E	0.007	0.010	0.17	0.25	
F	0.185	0.200	4.70	5.10	
G	0.050		1.270		TYP.
Н	0.228	0.244	5.80	6.20	
J	0.150	0.157	3.80	4.00	
K	0.016	0.050	0.40	1.27	
θ	0°	8°	0°	8°	





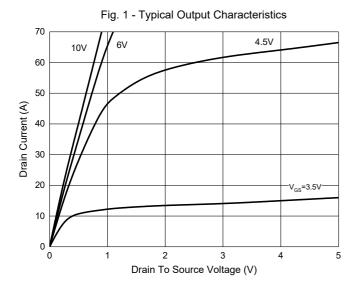


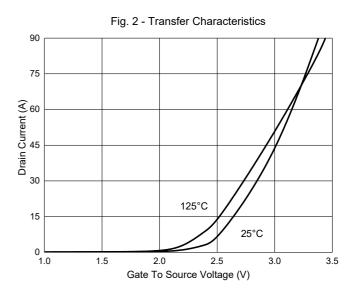
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

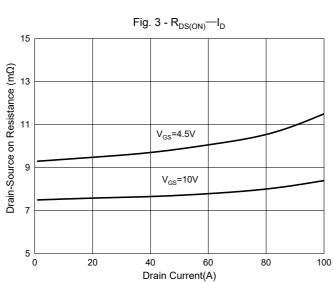
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics			'	1	1		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	100			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.8	3	V	
Drain Cauras On Besistanes	_	V _{GS} =10V, I _D =10A		8 9.5		mΩ	
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =5A	9.7 12.5		12.5		
Gate Resistance	R _g	f=1MHz, Open drain		0.68		Ω	
Diode Characteristics			·				
Continuous Body Diode Current	Is				15	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =15A			1.3	V	
Reverse Recovery Time	t _{rr}	1 -404 -41/44-4004/		51.5		ns	
Reverse Recovery Charge	Q _{rr}	I _S =10A, dI/dt=100A/μs		84		nC	
Dynamic Characteristics			•				
Input Capacitance	C _{iss}			2270			
Output Capacitance	C _{oss}	V _{DS} =50V,V _{GS} =0V,f=1MHz		797		pF	
Reverse Transfer Capacitance	C _{rss}			36			
Total Gate Charge	Qg			32			
Gate-Source Charge	Q _{gs}	V _{DS} =50V,V _{GS} =10V,I _D =10A		11.1		nC	
Gate-Drain Charge	Q_{gd}			4.78			
Turn-On Delay Time	t _{d(on)}			51			
Turn-On Rise Time	t _r	V _{DD} =50V, V _{GS} =10V,		14.4			
Turn-Off Delay Time	t _{d(off)}	R_{GEN} =2.2 Ω , I_D =10A		69.2		ns	
Turn-Off Fall Time	t _f			20.6			

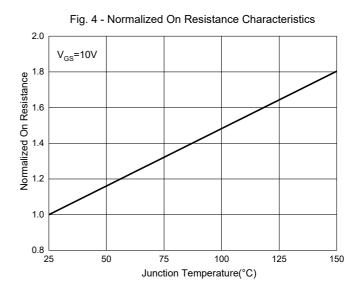


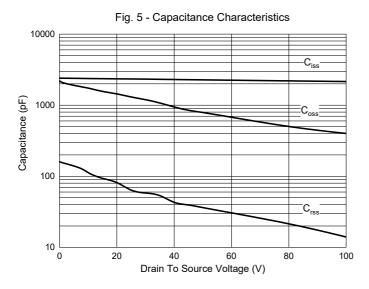
Curve Characteristics

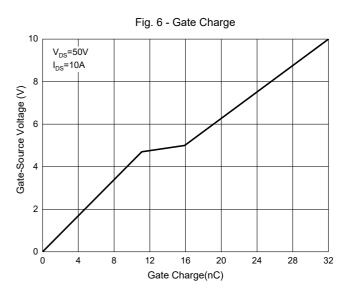














Curve Characteristics

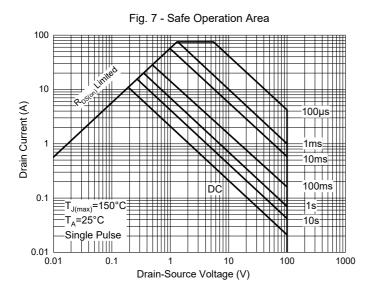
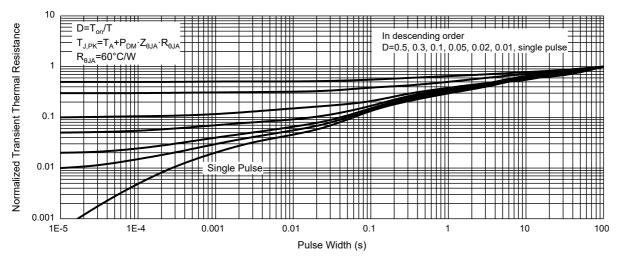


Fig. 8 - Normalized Maximum Transient Thermal Impedance





Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 4Kpcs/Reel

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