MSKSEMI 美森科













ESD

S

GD

PLED

IRFR220N(MS)

Product specification





DESCRIPTION

The IRFR220N(MS) uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- V_{DS} =200V, I_{D} =8A $R_{DS(ON)}$ <300m Ω @ V_{GS} =10V (Typ: 260m Ω)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Low gate to drain charge to reduce switching losses

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

Reference News

| PACKAGE OUTLINE | Schematic Diagram | Marking |
|-----------------|----------------------|---|
| G S | (2) D (1) G (3) S | MSKSEMI IRFR220N MS XXX Notes :XXX represents the order code |

Absolute Maximum Ratings (T_c=25 ℃unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|--|----------------------------------|------------|------|
| Drain-Source Voltage | V _{DS} | 200 | V |
| Gate-Source Voltage | V _G s | ±20 | V |
| Drain Current-Continuous | l _D | 8 | А |
| Drain Current-Continuous(T _C =100°C) | l₀ (100°C) | 5.6 | Α |
| Pulsed Drain Current | Ірм | 20 | А |
| Maximum Power Dissipation | P _D | 55 | W |
| Operating Junction and Storage Temperature Range | T _J ,T _{STG} | -55 To 150 | °C |

Thermal Characteristic

| | Thermal Resistance,Junction-to-Case ^(Note 2) | Rejc | 2.3 | °C/W |
|--|---|------|-----|------|
|--|---|------|-----|------|



Electrical Characteristics (TC=25°Cunless otherwise noted)

| Parameter | Symbol | Condition | Min | Тур | Max | Unit |
|---|---------------------|--|-----|-----|------|------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage V _{GS} =0V I _D =250µA | | | 200 | 215 | - | V |
| Zero Gate Voltage Drain Current V _{DS} =200V,V _{GS} =0V | | | - | - | 1 | μA |
| Gate-Body Leakage Current VGS=±20V,VDS=0V | | | - | - | ±100 | nA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage VDS=VGS, ID=250µA | | | 1 | 1.7 | 2.5 | V |
| Drain-Source On-State Resistance V _{GS} =10V, I _D =4.5A | | | - | 260 | 300 | mΩ |
| Forward Transconductance V _{DS} =25V,I _D =4.5A | | | 3 | - | - | S |
| Dynamic Characteristics (Note4) | | | | | | |
| Input Capacitance | C _{lss} | V _{DS} =25V,V _{GS} =0 | | 540 | | PF |
| Output Capacitance | Coss | | | 90 | | PF |
| Reverse Transfer Capacitance | Crss | V, F=1.0MHz | | 35 | | PF |
| Switching Characteristics (Note 4) | | | | | | |
| Turn-on Delay Time | t _{d(on)} | | _ | 6.4 | - | nS |
| Turn-on Rise Time | t _r | V _{DD} =100V,I _D =4.5 A | _ | 11 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | | _ | 20 | _ | nS |
| Turn-Off Fall Time | t _f | V_{GS} =10V, R_{GEN} =5 | - | 12 | - | nS |
| Total Gate Charge | Qg | | _ | 16 | _ | nC |
| Gate-Source Charge | Qgs | V _{DS} =160V,I _D =4.5 | _ | 3.4 | _ | nC |
| Gate-Drain Charge | Q _{gd} | A, V _{GS} = 10V | _ | 5.1 | _ | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage (Note 3) | VsD | V _{GS} =0V,I _S =8A | _ | - | 1.2 | V |
| Diode Forward Current (Note 2) | ls | | _ | - | 8 | А |

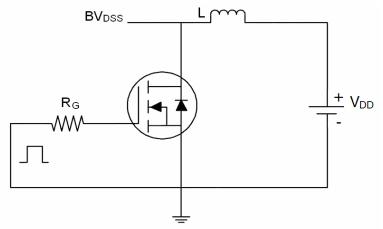
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2\%$.
- 4. Guaranteed by design, not subject to production

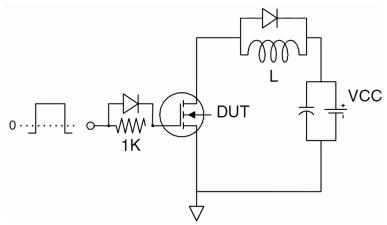


Test Circuit

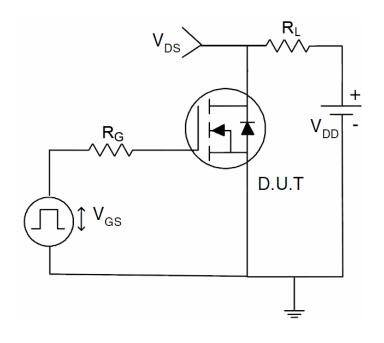
1) E_{AS} test Circuit



2) Gate charge test Circuit

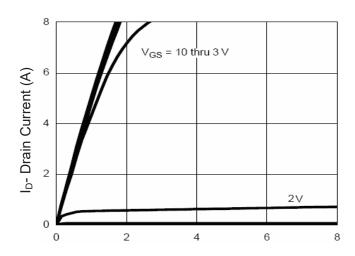


3) Switch Time Test Circuit



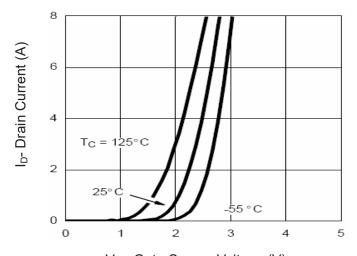


TypicalElectricalandThermal Characteristics (Curves)

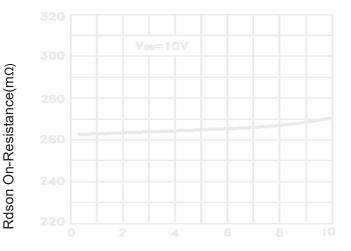


Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics



I_D- Drain Current (A) **Figure 3 Rdson- Drain Current**

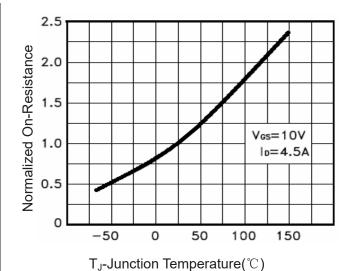


Figure 4 Rdson-JunctionTemperature

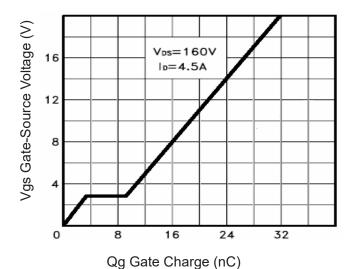
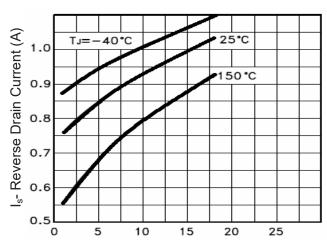


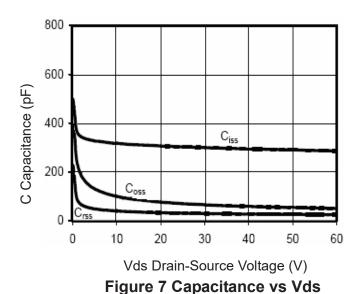
Figure 5 Gate Charge



Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward

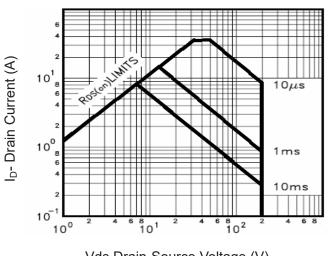


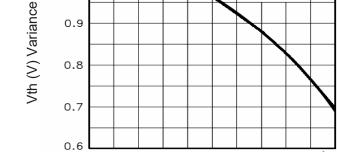


 $\label{eq:TJ-Junction} T_{J}-Junction Temperature(^{\circ}\mathbb{C}\,)$$ Figure 9 BVDSS vs Junction Temperature

V_{DS}=V_{GS}

 $I_D = 250 \mu A$



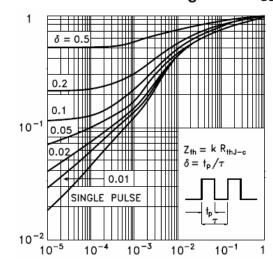


Vds Drain-Source Voltage (V)



Figure 10 V_{GS(th)} vs Junction Temperature





Square Wave Pluse Duration(sec)

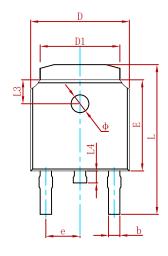
Vgs(th) (norm)

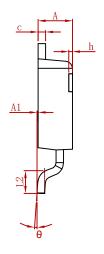
1.0

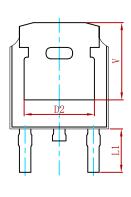
Figure 11 Normalized Maximum Transient Thermal Impedance



PACKAGE MECHANICAL DATA

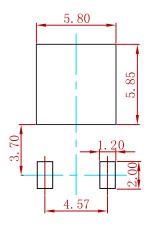






| Symbol | Dimensions | In Millimeters | Dimension | s In Inches |
|--------|------------|----------------|------------|-------------|
| Symbol | Min. | Min. Max. | | Max. |
| Α | 2.200 | 2.400 | 0.087 | 0.094 |
| A1 | 0.000 | 0.127 | 0.000 | 0.005 |
| b | 0.635 | 0.770 | 0.025 | 0.030 |
| С | 0.460 | 0.580 | 0.018 | 0.023 |
| D | 6.500 | 6.700 | 0.256 | 0.264 |
| D1 | 5.100 | 5.460 | 0.201 | 0.215 |
| D2 | 4.830 | 4.830 REF. | | REF. |
| E | 6.000 | 6.200 | 0.236 | 0.244 |
| е | 2.186 | 2.386 | 0.086 | 0.094 |
| L | 9.712 | 10.312 | 0.382 | 0.406 |
| L1 | 2.900 REF. | | 0.114 REF. | |
| L2 | 1.400 | 1.700 | 0.055 | 0.067 |
| L3 | 1.600 REF. | | 0.063 | REF. |
| L4 | 0.600 | 1.000 | 0.024 | 0.039 |
| Ф | 1.100 | 1.300 | 0.043 | 0.051 |
| θ | 0° | 8° | 0° | 8° |
| h | 0.000 | 0.300 | 0.000 | 0.012 |
| V | 5.250 REF. | | 0.207 | REF. |

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

| P/N | PKG | QTY |
|--------------|--------|------|
| IRFR220N(MS) | TO-252 | 2500 |



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