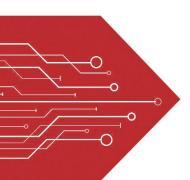
MSKSEMI















ESD

TVS

TSS

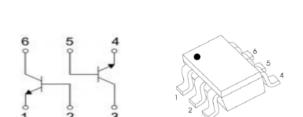
MOV

GDT

PLED

Brodnet data speet

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SOT-363

MMDT3052DW (NPN+NPN) Silicon Epitaxial Planar Transistor

Features

• Each transistor elements are independent

Applications

• For low frequency amplify application

MARKING: 5G

Absolute Maximum Ratings ($T_a = 25^{\circ}C$)

Parameter	Symbol	Value	Unit
Collector Base Voltage	V _{CBO}	50	V
Collector Emitter Voltage	V _{CEO}	50	V
Emitter Base Voltage	V_{EBO}	6	V
Collector Current	Ic	200	mA
Power Dissipation	P _{tot}	150	mW
Junction Temperature	T _j	125	${\mathbb C}$
Storage Temperature Range	T _{stg}	- 55 to + 125	${\mathbb C}$

Characteristics at T_a = 25 °C

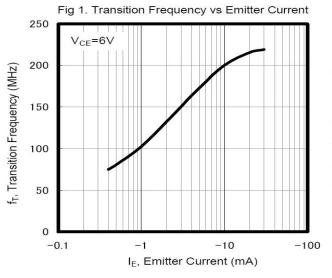
Parameter	Symbol	Min.	Тур.	Max.	Unit
DC Current Gain at V_{CE} = 6 V, I_C = 0.1 mA at V_{CE} = 6 V, I_C = 1 mA E	h _{FE} h _{FE} h _{FE}	90 120 200 350	- - -	- 240 400 700	- - - -
Collector Base Cutoff Current at V _{CB} = 50 V	I _{CBO}	-	-	100	nA
Emitter Base Cutoff Current at V _{EB} = 6 V	I _{EBO}	-	-	100	nA
Collector Emitter Breakdown Voltage at I _C = 100 μA	V _{(BR)CEO}	50	-	-	V
Collector Emitter Saturation Voltage at $I_C = 100 \text{ mA}$, $I_B = 10 \text{ mA}$	V _{CE(sat)}	-	-	0.3	V
Transition Frequency at $V_{CE} = 6 \text{ V}$, $-I_E = 10 \text{ mA}$	f _T	-	200	-	MHz
Collector Output Capacitance at $V_{CB} = 6 \text{ V}$, $f = 1 \text{ MHz}$	C _{ob}	-	2.5	_	pF

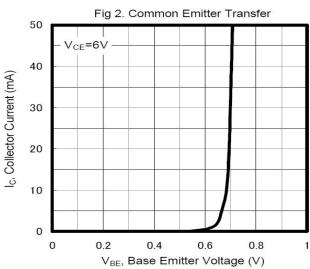


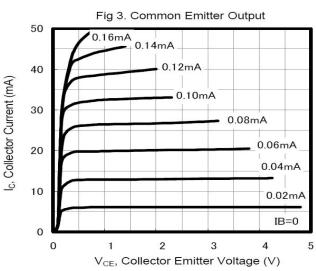
MMDT3052DW

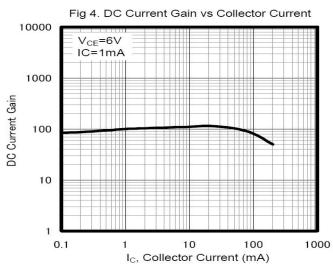


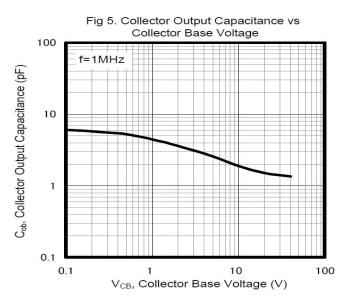
Electrical Characteristics Curves





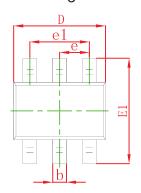


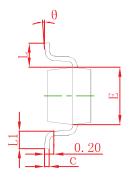


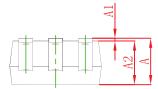




SOT-363 Package Outline Dimensions

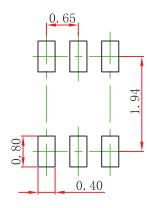






Symbol	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
С	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.400	0.085	0.094
е	0.650) TYP	0.026	S TYP
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

SOT-363 Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MMDT3052DW	SOT-363	3000



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