MSKSEMI 美森科













ESD

T\/9

TSS

MOV

GDI

PLED

AON6407-MS

Product specification





Description

The AON6407-MS uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

Features

VDS = -30V, ID = -100A

RDS(ON) < $4 \text{ m}\Omega$ VGS=-10V

Application

- Battery protection
- Load switch
- Uninterruptible power supply

Reference News

PACKAGE OUTLINE	P-Channel MOSFET	Marking
S S S S S S S S S S S S S S S S S S S	G G	MSKSEMI AON6407 P30
DFN5X6-8L		

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-30	V
Vgs	Gate-Source Voltage	±20	V
In @Tc=25°C	Continuous Drain Current, V cs @ 10V ¹	- 100	А
In @Tc=100°C	Continuous Drain Current, V cs @ 10V ¹	-70	А
Ірм	Pulsed Drain Current ²	-250	А
EAS	Single Pulse Avalanche Energy ³	80	mJ
IAS	Avalanche Current	-70	А
Pb@Tc=25°C	Total Power Dissipation ⁴	120	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
RөJA	Thermal Resistance Junction-Ambient ¹	50	°C/ W
Rejc	Thermal Resistance Junction- Case ¹	1.6	°C/ W



Electrical Characteristics (TA=25℃unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain- Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30			V	
<u> </u>	Static Drain-Source On-Resistance ²	V _{GS} =- 10V , I _D =-20A		3	4.0	mΩ	
$R_{DS(ON)}$	Static Dialit- Source Off- Resistance-	V_{GS} =-4.5 V , I_D =- 15 A		4.2	6.0	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250uA$	- 1.2		-2.5	V	
	Drain- Source Leakage Current	V_{DS} =-24V , V_{GS} =0V , T_J =25°C			- 1	А	
I _{DSS}	Diani- Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =55°C			-5		
I _{GSS}	Gate- Source Leakage Current	V _{GS} = ±20V , V _{DS} =0V			± 100	Α	
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.2		Ω	
Qg	Total Gate Charge (- 10V)			60			
Q_{gs}	Gate- Source Charge	V _{DS} =- 15V , V _{GS} =- 10V , I _D =- 18A		9		nC	
Q_gd	Gate- Drain Charge			15			
$T_{d(on)}$	Turn-On Delay Time			17			
Tr	Rise Time	V_{DD} =- 15V , V_{GS} =- 10V , R_{G} =3.3 Ω ,		40		ns	
$T_{d(off)}$	Turn-Off Delay Time	I _D =-20A		55		115	
T _f	Fall Time			13			
C _{iss}	Input Capacitance			3450			
Coss	Output Capacitance	V _{DS} =-25V , V _{GS} =0V , f=1MHz		255		pF	
C_{rss}	Reverse Transfer Capacitance			140			

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current			- 100	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =- 1A , T _J =25°C			- 1.2	V
t _{rr}	Reverse Recovery Time	IF=-20A , di/dt=100A/μs ,		22		S
Q _{rr}	Reverse Recovery Charge	T _J =25℃		72		°C

Note:

- 1. The data tested by surface mounted on a 1 inch $_2\,\text{FR-4}$ board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leqq 300 \text{us}$, duty cycle $\, \leqq \, 2\%$
- $3. The \ EAS \ data \ shows \ Max. \ rating \ . \ The \ test \ condition \ is \ V_{DD} = -50V, V_{GS} = -10V, L = 0.1 mH, I_{AS} = -40A$
- 4.The power dissipation is limited by 150°C junction temperature
- 5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation
- 6. The maximum current rating is package limited.



Typical Characteristics

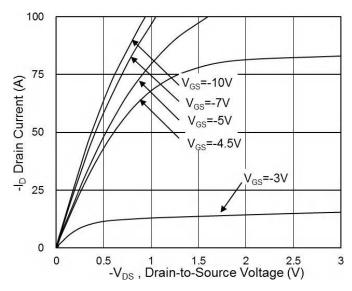


Figure 1:Switching Test Circuit

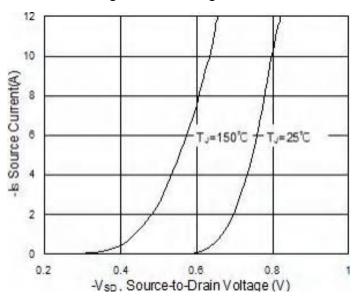


Fig. 3 Source Drain Forward Characteristics

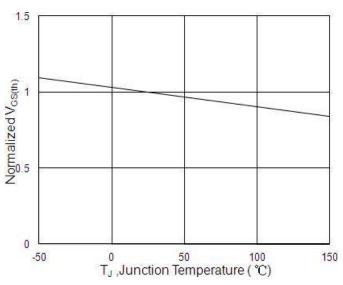


Fig.5 Normalized -V_{GS(th)} vs T_J

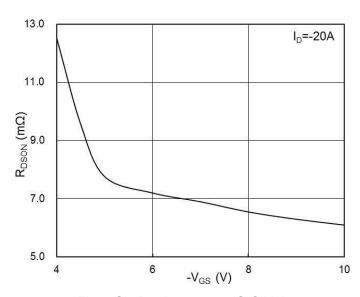


Fig.2 On-Resistance vs G-S Voltage

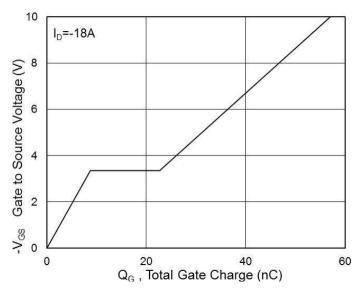


Fig.4 Gate-Charge Characteristics

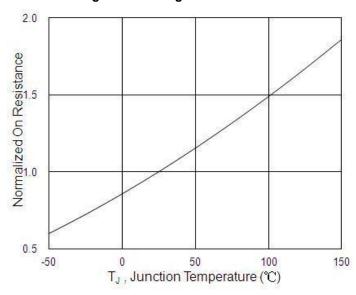
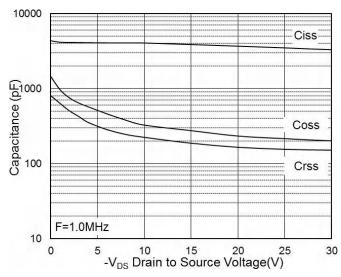


Fig.6 Normalized RDSON vs TJ



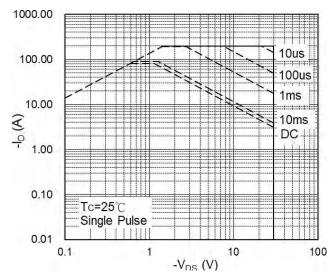


Fig.7 Capacitance

Fig.8 Safe Operating Area

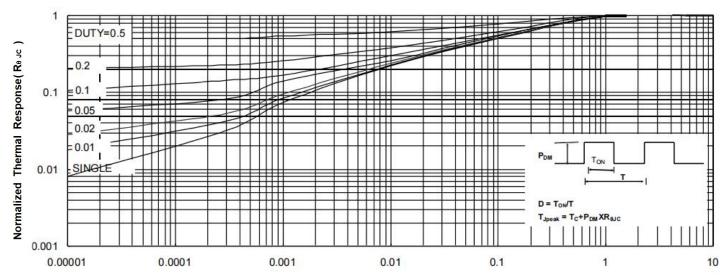


Fig. 9 Normalized Maximum Transient Thermal Impedance

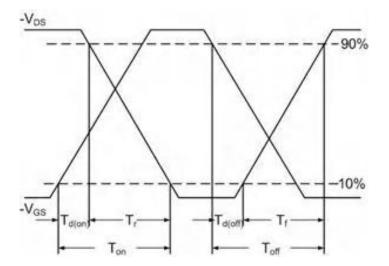


Fig.10SwitchingTimeWaveform

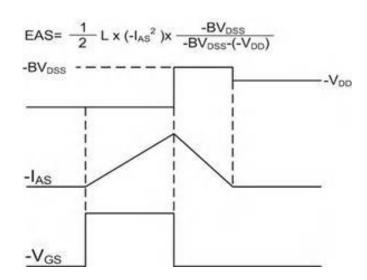
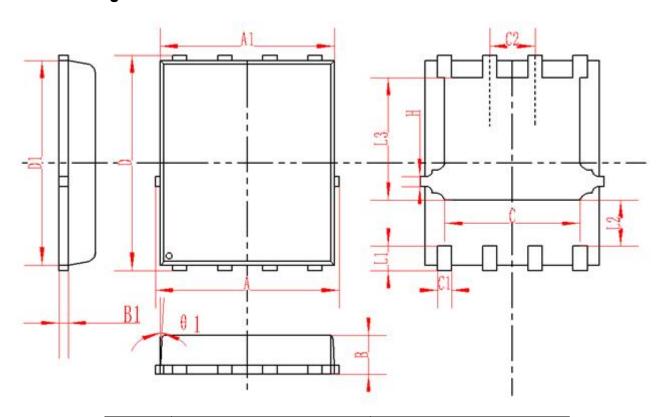


Fig.11UnclampedInductiveSwitchingWaveform



DFN5X6-8L Package Information



SYMBOL	MM			INCH		
STIVIDOL	MIN	NOM	MAX	MIN NOM M		MAX
А	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF			0.010REF		
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP			0.5TYP		
θ1	8。	10 _°	12 _°	8。	10 _°	12。
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010

REEL SPECIFICATION

P/N	PKG	QTY
AON6407-MS	DFN5X6-8L	5000



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specificationsof any andall MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

单击下面可查看定价,库存,交付和生命周期等信息

>>MSKSEMI (美森科)