

MSKSEMI 美森科

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



PLED

5N10T-MS

Product specification

General Description

- Low RDS(on) & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and soft recovery

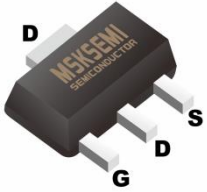
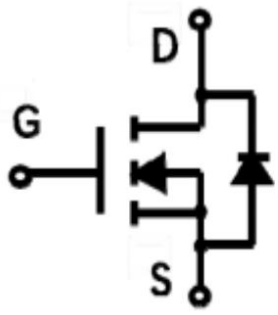

Product Summary

V_{DS}	100V
I_D	5.0A
R_{DS(ON)}(at V_{GS}=10V)	< 140 mohm

Applications

- Consumer electronic power supply
- Motor control
- Synchronous-rectification
- Isolated DC/DC convertor

Reference News

PACKAGE OUTLINE	PIN CONFIGURATION	Marking
 <p>SOT-89</p>		

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V _{DS}	100	V
Gate-source Voltage		V _{GS}	±20	V
Drain Current	T _A =25°C	I _D	5.0	A
	T _A =70°C		2.4	
Pulsed Drain Current ^A		I _{DM}	21	A
Total Power Dissipation @ T _A =25°C		P _D	1.2	W
Thermal Resistance Junction-to-Ambient ^B		R _{θJA}	104	°C/W
Junction and Storage Temperature Range		T _J , T _{STG}	-55~+150	°C

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}= \pm 20V, V_{DS}=0V$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.8	3.0	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3.0A$		110	140	m Ω
		$V_{GS}=4.5V, I_D=2.0A$		160	300	
Diode Forward Voltage	V_{SD}	$I_S=3.0A, V_{GS}=0V$		0.8	1.2	V
Maximum Body-Diode Continuous Current	I_S				3.0	A
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V, f=1\text{MHz}$		206		pF
Output Capacitance	C_{oss}			29		
Reverse Transfer Capacitance	C_{rss}			1.4		
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS}=10V, V_{DS}=50V, I_D=3.0A$		4.3		nC
Gate-Source Charge	Q_{gs}			1.5		
Gate-Drain Charge	Q_{gd}			1.1		
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=10V, V_{DD}=50V, I_D=3.0A, R_{GEN}=2\Omega$		14.7		ns
Turn-on Rise Time	t_r			3.5		
Turn-off Delay Time	$t_{D(off)}$			20.9		
Turn-off fall Time	t_f			2.7		
Reverse recovery time	t_{rr}	$I_S=3A, di/dt=100 A/\mu s$		32		ns
Reverse recovery charge	Q_{rr}			39		nC
Peak reverse recovery current	I_{rrm}			2.1		A

 A. Pulse Test: Pulse Width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Typical Performance Characteristics

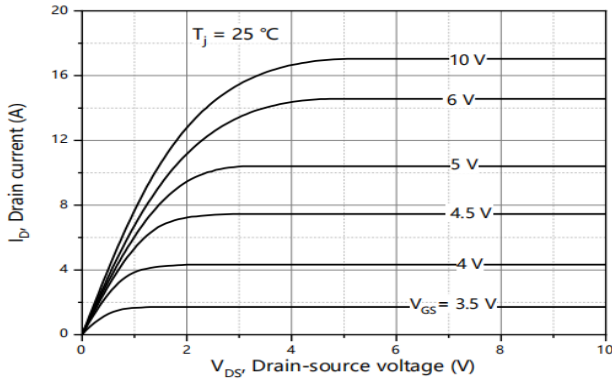


Figure1. Output Characteristics

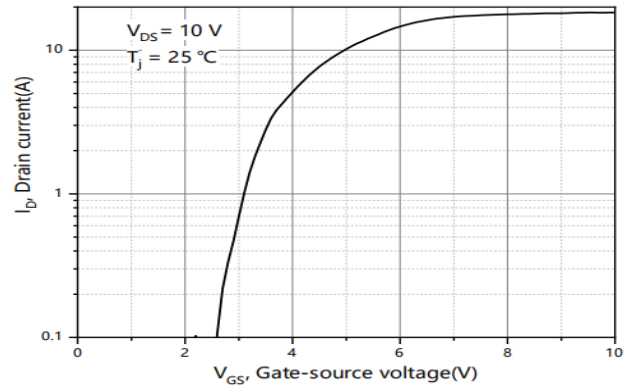


Figure2. Transfer Characteristics

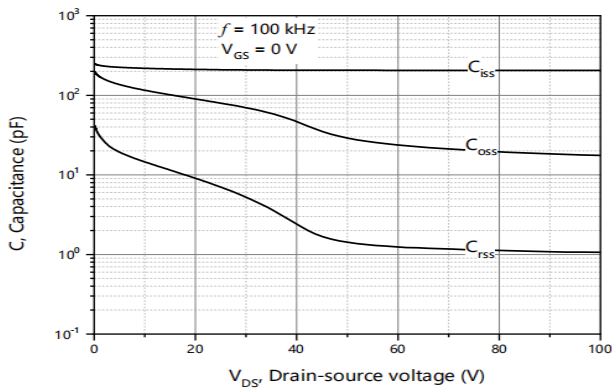


Figure3. Capacitance Characteristics

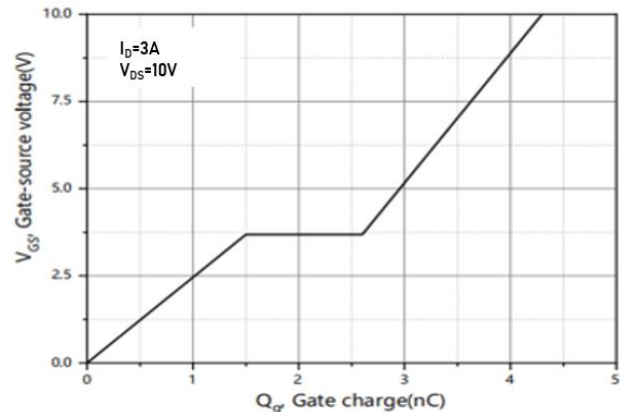


Figure4. Gate Charge

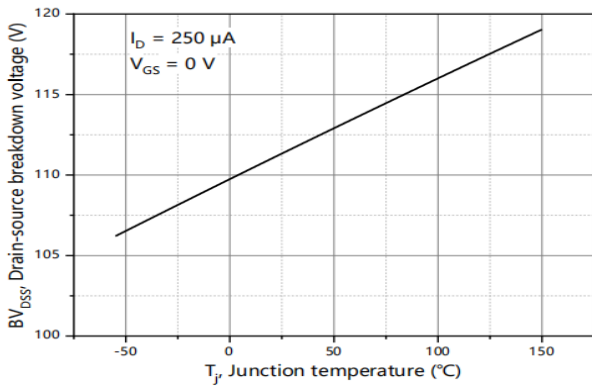


Figure5. Drain-Source breakdown voltage

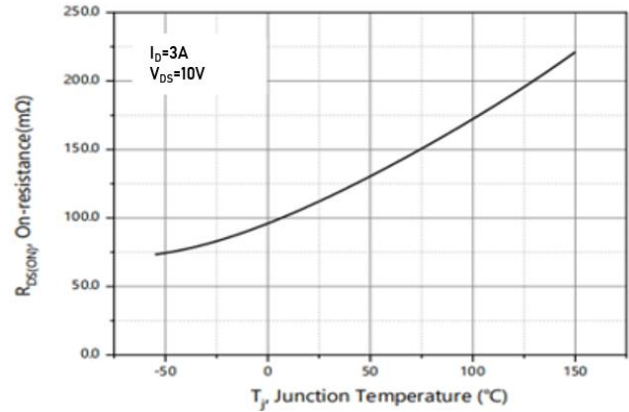


Figure6. Drain-Source on Resistance

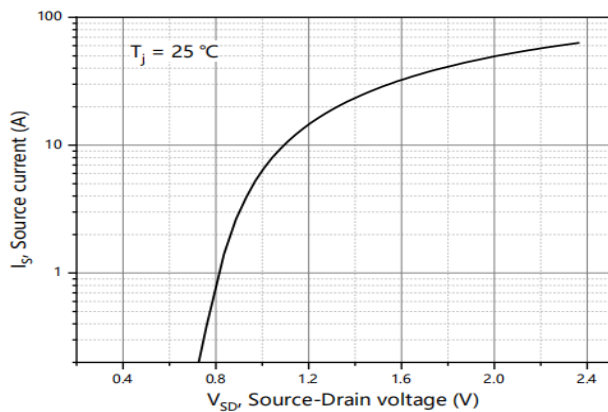


Figure7. Forward characteristic of body diode

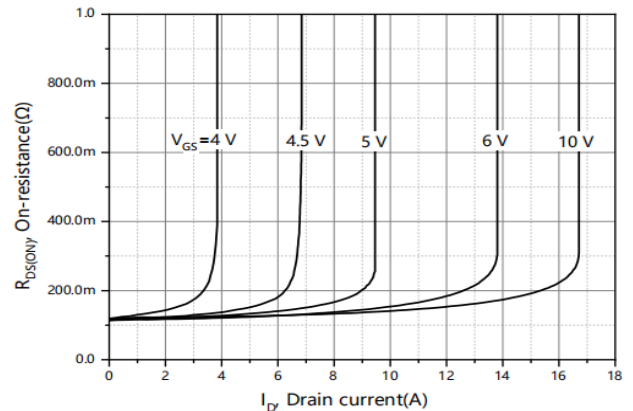


Figure8. Drain-source on-state resistance

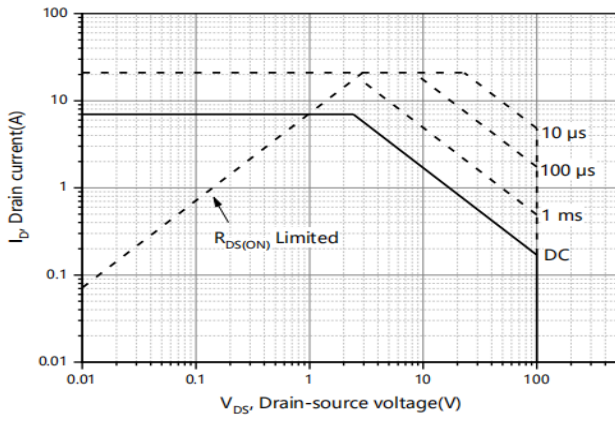


Figure9. Safe Operation Area $T_A=25\text{ }^\circ\text{C}$

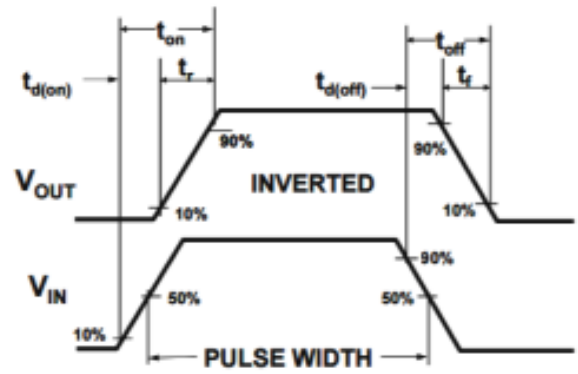
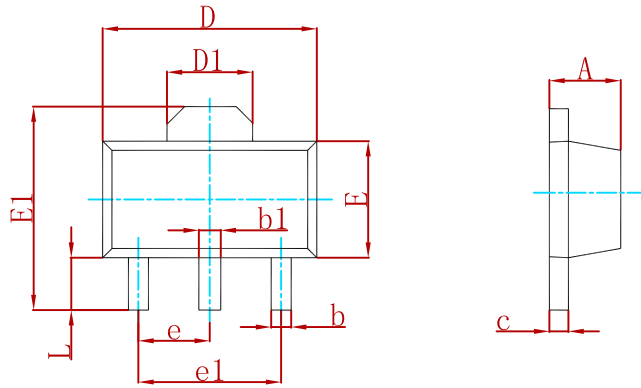


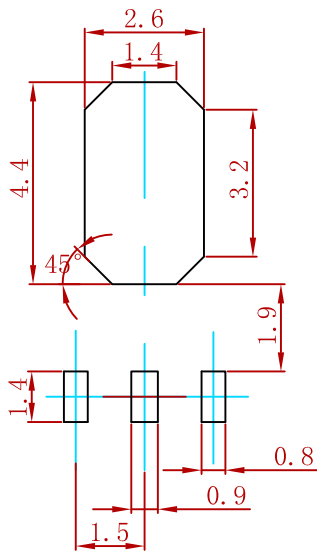
Figure10. Switching wave

PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047

Suggested Pad Layout



Note:
 1. Controlling dimension: in millimeters.
 2. General tolerance: ± 0.05 mm.
 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
5N10T-MS	SOT-89	1000

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