MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

F1(MS)THRU F7(MS)

Product specification





FEATURES

- Ideal for surface mount applications
- Easy pick and place
- Built-in strain relief
- Fast switching speed

VOLTAGE RANGE 50 to 1000 Volts CURRENT 1.0 Ampere

PACKAGE OUTLINE	PIN CONFIGURATION		
	1 — 2 1.Cathode 2.Anode		
SOD-123FL	2.7.11000		

MECHANICAL DATA

Case: Molded plastic

Epoxy: UL 94V-0 rate flame retardant

Metallurgically bonded construction

Polarity: Color band denotes cathode end

Mounting position: Any

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25 C ambient temperature unless otherwies specified. Single phase half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

TYPE NUMBER	F1	F2	F3	F4	F5	F6	F7	UNITS
Maximum Recurrent Peak Reverse Voltage	50	100	200	400	600	800	1000	V
Maximum RMS Voltage		70	140	280	420	560	700	V
Maximum DC Blocking Voltage		100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current								
at Ta=25 C		1.0					Α	
Peak Forward Surge Current, 8.3 ms single half sine-wave								
superimposed on rated load (JEDEC method)	30			Α				
Maximum Instantaneous Forward Voltage at 1.0A		1.3			V			
Maximum DC Reverse Current Ta=25℃		5.0					μΑ	
at Rated DC Blocking Voltage Ta=100 ℃		100 μΑ					μA	
Maximum Reverse Recovery Time (Note 1)		150 250 500		0	nS			
Typical Junction Capacitance (Note 2)		15				pF		
Typical Thermal Resistance R JA (Note 3)		80			°C/W			
Operating and Storage Temperature Range ТJ, Тsтв		-65 +150			$^{\circ}$ C			
Marking Code								

NOTES:

- 1. Reverse Recovery Time test condition: IF=0.5A, IR=1.0A, IRR=0.25A
- 2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
- 3. Thermal Resistance from Junction to Ambient.



RATINGAND CHARACTERISTIC CURVES (F1(MS)THRU F7(MS))

FIG.1-TYPICAL FORWARD

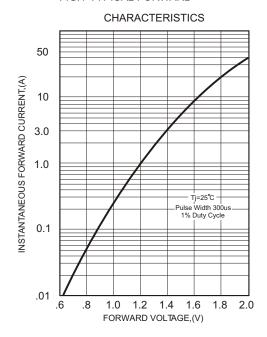
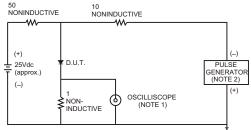


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE

RECOVERY TIME CHARACTERISTICS



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm.22pF

2. Rise Time= 10ns max., Source Impedance= 50 ohms.

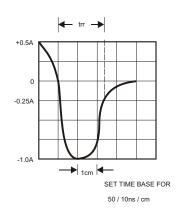


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

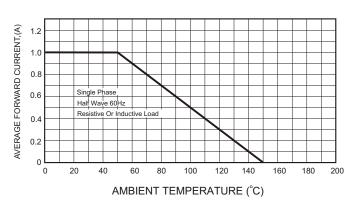


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

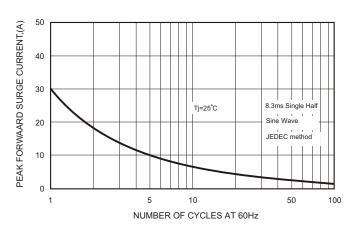
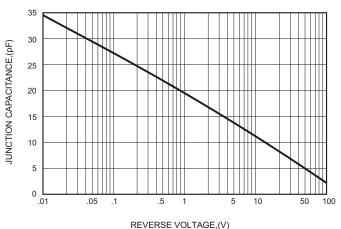
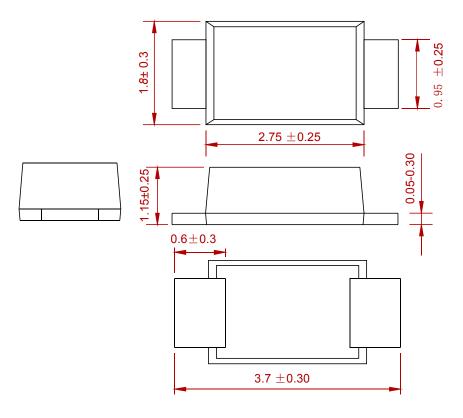


FIG.5-TYPICAL JUNCTION CAPACITANCE



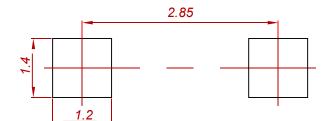


PACKAGE MECHANICAL DATA



Dimensions in millimeters

Suggested Pad Layout



Note:

- 1. Controlling dimension: in millimeters.
- 2.General tolerance:±0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
F1(MS)THRU F7(MS)	SOD-123FL	3000



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

单击下面可查看定价,库存,交付和生命周期等信息

>>MSKSEMI (美森科)