



Product data sheet

www.msksemi.com

Downloaded From Oneyac.com



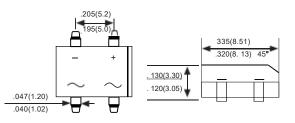


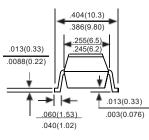
Features

Ideal for printed circuit board Reliable low cost construction utilizing molded plastic technique High temperature soldering guaranteed: 260°/10 seconds at 5 lbs., (2.3kg) tension Small size, simple installation High surge current capability

Mechanical Data

Case : JEDEC DBS Molded plastic body Terminals : Solder plated, solderable per MIL-STD-750, Method 2026 Polarity : Polarity symbol marking on body Mounting Position : Any Weight : 0.02 ounce, 0.4 grams





Dimensions in inches and (millimeters)

REEL SPECIFICATION

P/N	PKG	QTY
DB301S-DB307S	DBS	1500

Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unlss otherwise specified.

Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Parameter	SYMBOLS	DB301S	DB302S	DB303S	DB304S	DB305S	DB306S	DB307S	UNITS
Maximum repetitive peak reverse voltage	Vrrm	50	100	200	400	600	800	1000	V
Maximum RMS voltage	Vrms	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	Vdc	50	100	200	400	600	800	1000	V
Maximum average forward rectified current at T_A =40°C	lf(AV)				3.0				А
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	Ifsm	85						A	
Maximum instantaneous forward voltage drop per leg at 3.0A	Vf				1.1				V
Maximum DC reverse currentTa=25°Cat rated DC blocking voltageTa=125°C	R				10 500				рА pA
I ² t Rating for Fusing (t<8.3ms)	l2 t				10.4				A2 s
Operating temperature range (Note1)	CJ	25						pF	
Typical Thermal Resistance (Note2)	Reja	110						°C/W	
Operating temperature range	TJ	-55 to +150						°C	
storage temperature range	Тѕтс			- {	55 to +15	0			°C

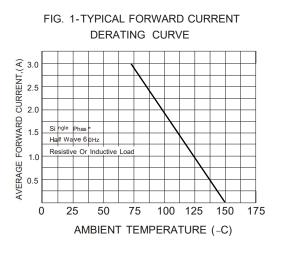
NOTES: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.

2. Thermal resistance from junction to ambient mounted on P.C.B. with 0.5*0.5" (13*13 mm) copper pads.



DB301S-DB307S Semiconductor

Ratings And Characteristic Curves



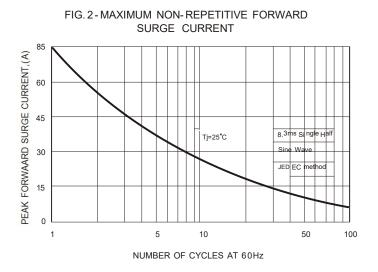


FIG. 3-TYPICAL FORWARD

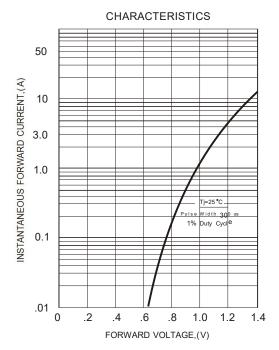
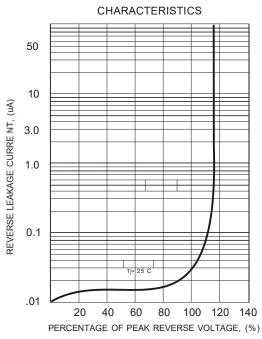


FIG.4-TYPICAL REVERSE





<u>Attention</u>

■ Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.

■ MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications f any and all MSKSEMI Semiconductor products described orcontained herein.

■ Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuits for safedesign, redundant design, and structural design.

■ In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

单击下面可查看定价,库存,交付和生命周期等信息

>>MSKSEMI (美森科)