MSKSEMI 美森科













ESD

TSS

MOV

GDT

PLED

MMBT5401-MS

Product specification





TRANSISTOR (PNP)

FEATURES

- Complementary to MMBT5551-MS
- Ideal for Medium Power Amplification and Switching

Reference News

PACKAGE OUTLINE		MARKING	
1 2	1. BASE 2. EMITTER 3.COLLECTOR	2 L	
SOT-23			

MAXIMUM RATINGS (Ta=25℃ unless otherwise noted)

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-160	V
V _{CEO}	Collector-Emitter Voltage	-150	V
V _{EBO}	Emitter-Base Voltage	-5	V
lc	Collector Current	-0.6	А
Pc	Collector Power Dissipation	0.3	W
R _{. JA}	Thermal Resistance from Junction to Ambient	416	°C/W
Tj	Junction Temperature	150	℃
T _{stg}	Storage Temperature	-55∼+150	℃

ELECTRICAL CHARACTERISTICS (Ta=25℃ unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	lc=-100μA, le=0	-160			V
Collector-emitter breakdown voltage	V _(BR) CEO	I _C =-1mA, I _B =0	-150			V
Emitter-base breakdown voltage	V _{(BR)EBO}	l∈=-10μA, lc=0	-5			V
Collector cut-off current	Ісво	V _{CB} =-120V, I _E =0			-0.1	μA
Emitter cut-off current	IEBO	V _{EB} =-4V, I _C =0			-0.1	μA
	hFE(1)	Vce=-5V, Ic=-1mA	80			
DC current gain	hFE(2)	V _{CE} =-5V, I _C =-10mA	100		300	
_	h _{FE(3)} *	Vce=-5V, Ic=-50mA	50			
Collector-emitter saturation voltage	VCE(sat)1	I _C =-10mA, I _B =-1mA			-0.2	V
	V _{CE(sat)2}	Ic=-50mA, I _B =-5mA			-0.5	V
Page emitter caturation voltage	V _{BE(sat)1}	Ic=-10mA, I _B =-1mA			-1	V
Base-emitter saturation voltage	V _{BE(sat)2}	I_C =-50mA, I_B =-5mA			-1	V
Transition frequency	f_T	V _{CE} =-5V,I _C =-10mA, f=30MHz	100			MHz

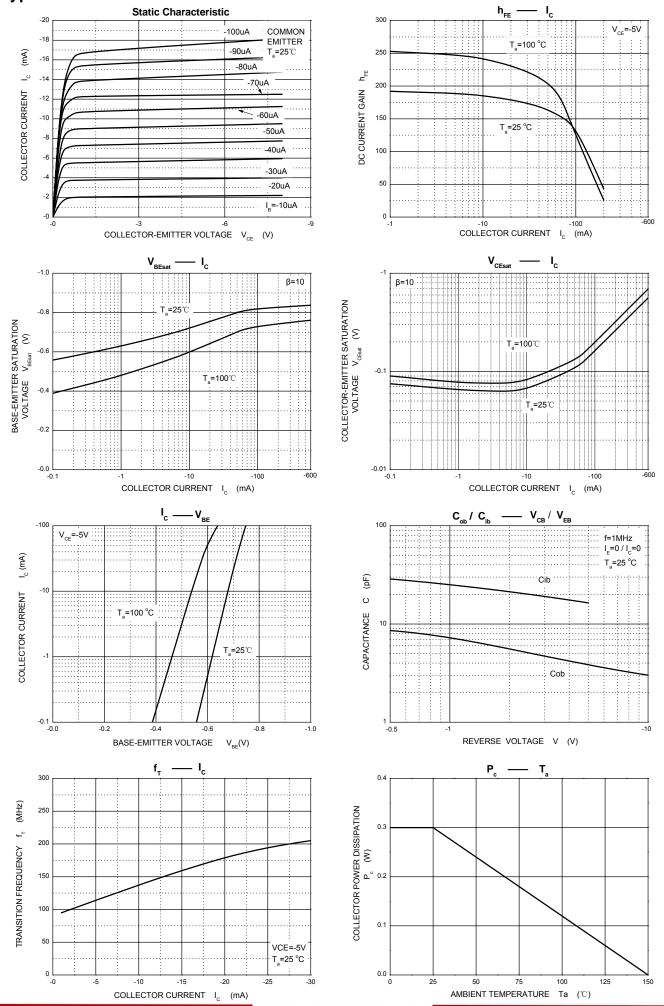
^{*}Pulse test: pulse width ≤300µs, duty cycle≤ 2.0%.

CLASSIFICATION OF hFE (2)

RANK	L	Н
RANGE	100-200	200-300

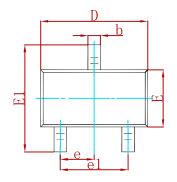


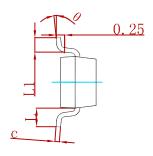
Typical Characteristics

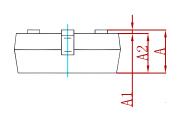




PACKAGE MECHANICAL DATA

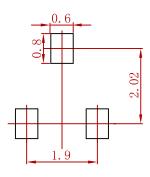






Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
Е	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950	0.950 TYP		7 TYP	
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022	REF	
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MMBT5401-MS	SOT-23	3000



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

单击下面可查看定价,库存,交付和生命周期等信息

>>MSKSEMI (美森科)