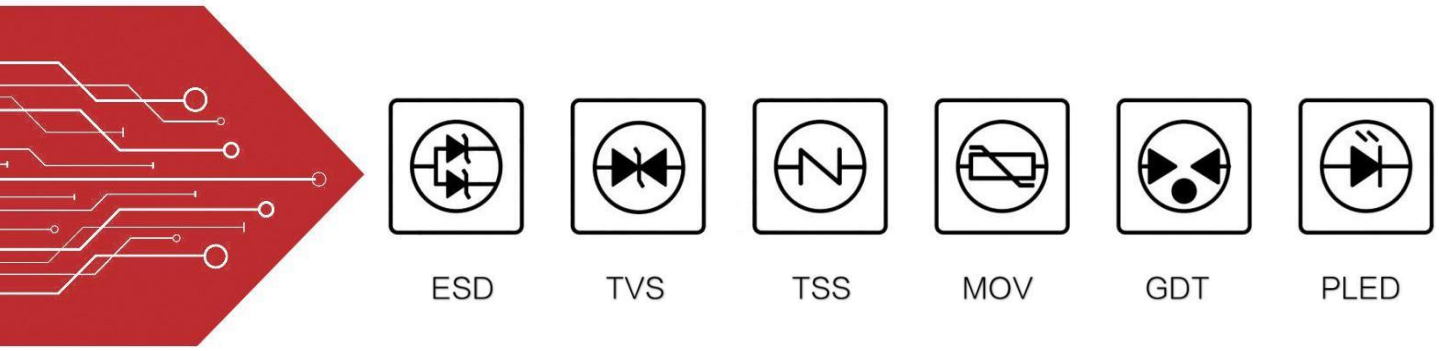
MSKSEMI
SEMICONDUCTOR



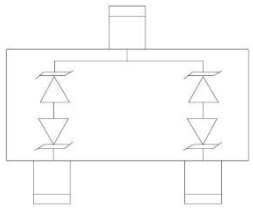
Product data sheet

www.msksemi.com

Downloaded From Oneyac.com

Features

- 150 Watts peak pulse power (tp = 8/20μs)
- Transient protection for high speed data lines to
  IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
  IEC 61000-4-4 (EFT) 40A (5/50ns)
- Working voltages : 5V
- Protects two bidirectional line
- Low operating and clamping voltages
- Solid-state silicon avalanche technology



SOT-23

Applications

- Notebooks, Desktops, Servers and Video Graphics Cards
- USB Power & Data Line Protection
- Monitors and Flat Panel Displays
- I²C Bus Protection
- Portable Instrumentation
- Set Top Box

Electrical Characteristics@ Ta=25℃ unless otherwise

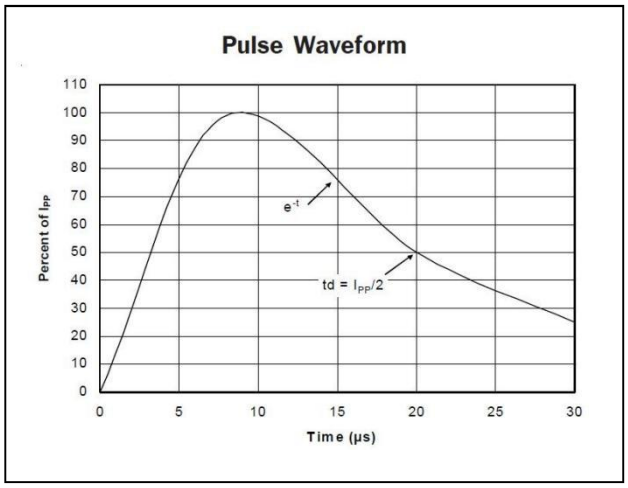
P/N	V_{DRM}	I_{DRM}	V_{BR}	I_R	Vc	Ipp	Vc	Ipp	CO
	V	μA	V	mA	V	A	V	A	pF
		MAX	MIN		MAX		MAX		TYP
ESD5V0S2BT-MS	5	1	5.5	1	9.8	1	15	12	50

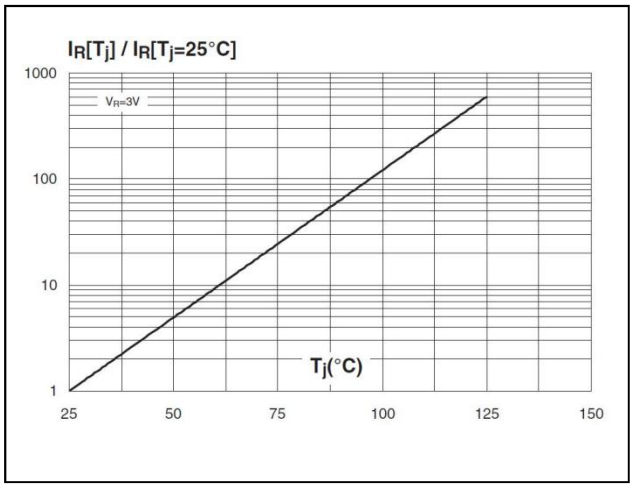
Maximum Rating @ Ta=25℃ unless otherwise specified

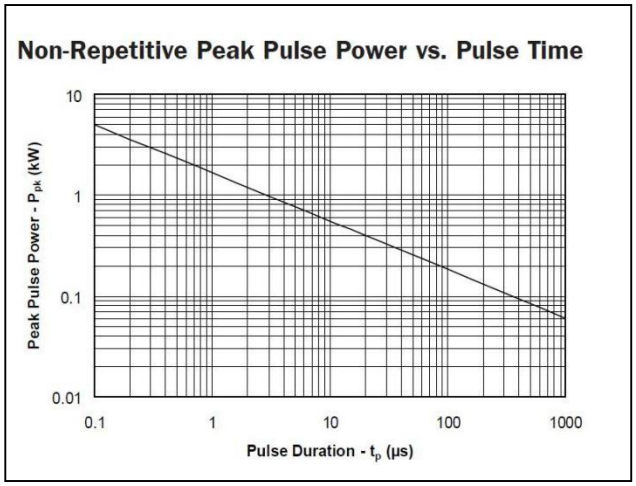
Symbol	Parameter	Ratings	Units
P_{PK}	Peak Pulse Power (tp = 8/20μs)	150	Watts
T_L	Lead Soldering Temperature	260(10sec.)	℃
T_J	Operating Temperature	-55 to +125	℃
T_{STG}	Storage Temperature	-55 to +150	℃

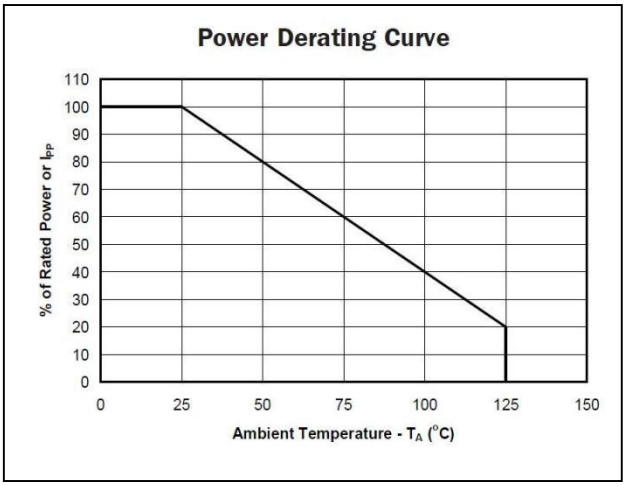
Downloaded From Oneyac.com

Typical Characteristics@ Ta=25℃ unless otherwise specified



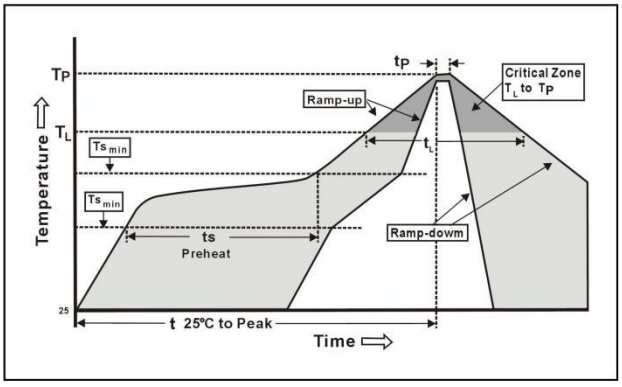






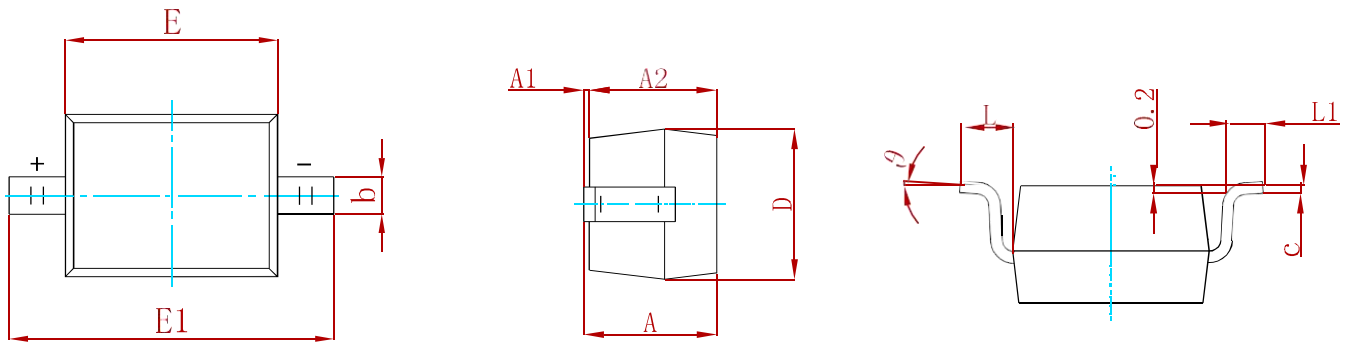
Soldering Parameters

Reflow Condition		Fb – Free assembly
Pre Heat	-Temperature Min ($T_{s(Min)}$)	150°C
	-Temperature Max ($T_{s(Max)}$)	200°C
	-Time (Min to max) (t_s)	60 – 180 secs
Average ramp up rate (Liquidus) Temp (T_L) to peak		3°C/second Max
$T_{S(Max)}$ to T_L - Ramp-up Rate		3°C/second Max
Reflow	-Temperature (T_L) (Liquidus)	217°C
	-Temperature (t_L)	60 – 150 seconds
Peak Temperature (T_P)		$250^{+0/-5}$ °C
Time within 5°C of actual peak Temperature (t_p)		20 – 40 seconds
Ramp-dowm Rate		6°C/second Max
Time 25°C to peak Temperature (T_P)		8 minutes Max.
Do not exceed		260°C



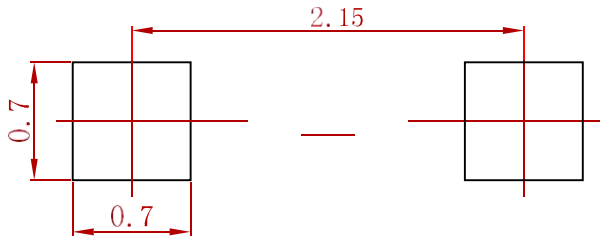
Downloaded From Oneyac.com

PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A		1.000		0.039
A1	0.000	0.100	0.000	0.004
A2	0.800	0.900	0.031	0.035
b	0.250	0.350	0.010	0.014
c	0.080	0.150	0.003	0.006
D	1.200	1.400	0.047	0.055
E	1.600	1.800	0.063	0.071
E1	2.550	2.750	0.100	0.108
L	0.475 REF.		0.019 REF.	
L1	0.250	0.400	0.010	0.016
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
ESD5V0S2BT-MS	SOT-23	3000

Downloaded From Oneyac.com

Attention

■ Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.

■ MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specificationsof any andall MSKSEMI Semiconductor products described orcontained herein.

■ Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts orequipment.

■ MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.

■ In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringementsof intellectual property rights or other rightsof third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

Downloaded From Oneyac.com

单击下面可查看定价，库存，交付和生命周期等信息

>>MSKSEMI(美森科)

Downloaded From Oneyac.com