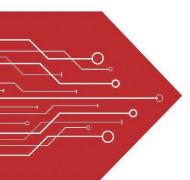
MSKSEMI















ESD

TVS

TSS

MOV

GDT

PLED

Product data sheet

www.msksemi.com







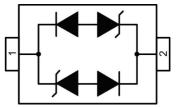


- ♦ 350W peak pulse power (8/20µs)
- ◆ Ultra low capacitance : 1.0pF typical
- ♦ Ultra low leakage: nA level
- ◆ Low Operating: 5V
- Low clamping voltage
- Protects one power line or data line
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test Air discharge: ±30kV
 - Contact discharge: ±30kV - IEC61000-4-4 (EFT) 40A (5/50ns)
- RoHS Compliant



Mechanical Characteristics

- ◆ Package: SOD-323 ◆ Lead Finish: Matte Tin
- ◆ Case Material: "Green" Molding Compound.
- ◆ UL Flammability Classification Rating 94V-0
- ◆ Moisture Sensitivity: Level 3 per J-STD-020
- ◆ Terminal Connections: See Diagram Below
- ◆ Marking Information: See Below



Circuit and Pin Schematic

SOD-323

Applications

- USB Ports
- **Smart Phones**
- ♦ Wireless Systems
- Ethernet 10/100/1000 Base T



Electrical Characteristics (T_{Δ} =25°C unless otherwise specified)

MSK32C05L01							
Parameter	Symbol	Min	Тур	Max	Unit	Test Condition	
Reverse Working Voltage	VRWM			5	V		
Breakdown Voltage	VBR	6			V	IT = 1mA	
Reverse Leakage Current	I _R		1	100	nA	VRWM = 5V	
Clamping Voltage	Vc			10	V	IPP = 1A (8 x 20μs pulse)	
Clamping Voltage	Vc			18	V	IPP = 18A (8 x 20µs pulse)	
Peak Pulse Current	IPP			18	Α	tp=8/20µs	
Junction Capacitance	Cı		1		pF	VR = 0V, f = 1MHz	

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

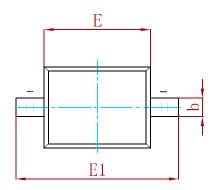
Parameter	Symbol	Value	Unit	
ESD per IEC 61000-4-2 (Air)	VESD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±30		
Operating Temperature Range	TJ	-40 to +85	°C	
Storage Temperature Range	Tstg	−55 to +150	°C	

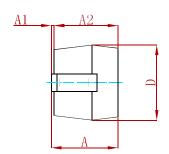


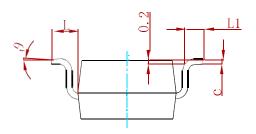




PACKAGE MECHANICAL DATA

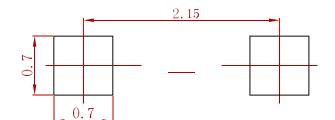






0	Dimensions	In Millimeters	Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
Α		1.000		0.039
A 1	0.000	0.100	0.000	0.004
A2	0.800	0.900	0.031	0.035
b	0.250	0.350	0.010	0.014
С	0.080	0.150	0.003	0.006
D	1.200	1.400	0.047	0.055
E	1.600	1.800	0.063	0.071
E1	2.550	2.750	0.100	0.108
L	0.475	REF.	0.019	REF.
L1	0.250	0.400	0.010	0.016
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MSK32C05L01	SOD-323	3000



Compiance

Semiconductor

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