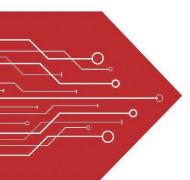
# MSKSEMI















**ESD** 

**TVS** 

**TSS** 

MOV

**GDT** 

**PLED** 

Product data sheet

www.msksemi.com

Semiconductor



# **Applications**

Cellular phones audio MP3 players Digital cameras Portable applicationss mobile telephone

#### **Features**

Small Body Outline Dimensions:

0.039" x 0.024"(1.0 mm x 0.60 mm)

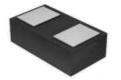
Low Body Height: 0.020" (0.50 mm) Max

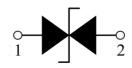
Stand-off Voltage: 12 V

Low Leakage

Response Time is Typically < 1 ns IEC61000-4-2 Level 4 ESD Protection We declare that the material of product compliance with RoHS requirements.

# Pin Description Schematic Diagram





DFN1006-2L

#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise noted, VF=0.9V Max. @ IF=10Ma for all types)

	$V_{RWM}$	I <sub>R</sub>	$V_{BR}$	Ι <sub>Τ</sub>	I <sub>PP</sub>	Vc	$P_{PK}$	С
	(V)	( µ A)	(V)	(mA)	(A)	(V)	(W)	(pF)
P/N		@	@ I <sub>T</sub>			@ Max I <sub>PP</sub>	(8*20 µs)	
		$V_{RWM}$	(Note 2)		(Note 3)	(Note 3)		
	Max	Max	Min		Max	Max	Тур	Тур
ESD9N12BA-MS	12	1.0	1.33	1.0	5.8	23.4	140	30

Other voltage available upon request.

- 3. Surge current waveform per Figure 3.

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Air Contact Contact discharge		±15 ±8	kV kV
ESD Voltage Per Human Body Model		16	kV
Total Power Dissipation on FR-5 Board (Note 1)	PD	150	Mw
<b>@</b> T <sub>A</sub> =25℃			
Junction and Storage Temperature Range	TJ,TSTTh	-55 to 150	$^{\circ}\mathbb{C}$
Lead Solder Temperature – Maximum (10	TL	260	$^{\circ}$
Second Duration)			

Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1 FR-5 = 1 0\*0 75\*0 62 in







#### **Electrical Parameter**

Symbol	Parameter
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
V <sub>RWM</sub>	Working Peak Reverse Voltage
I <sub>R</sub>	Maximum Reverse Leakage Current @ V <sub>RWM</sub>
I <sub>T</sub>	Test Current
$V_{BR}$	Breakdown Voltage @ I <sub>T</sub>

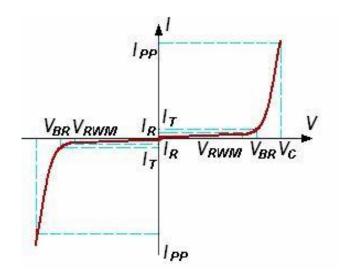


FIG1: Pulse Waveform

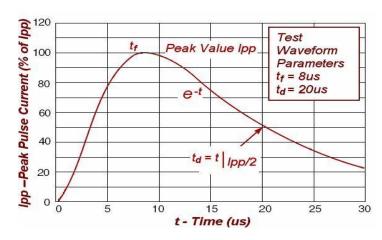
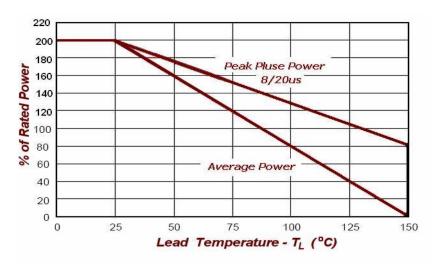


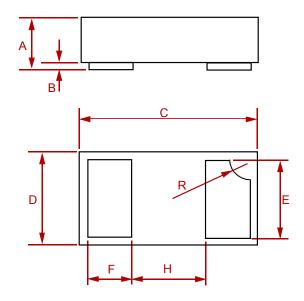
FIG2:Power Derating



#### Semiconductor

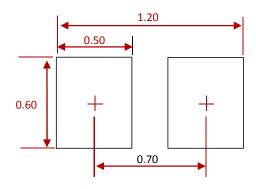


#### **PACKAGE MECHANICAL DATA**



Dim	Inc	hes	Millimeters		
	MIN	MAX	MIN	MAX	
Α	0.0125	0.02	0.32	0.52	
В	0.000	0.002	0.00	0.05	
С	0.037	0.043	0.95	1.080	
D	0.022	0.027	0.55	0.680	
E	0.016	0.024	0.40	0.60	
F	0.008	0.012	0.20	0.30	
Н	0.015Typ.		0.40Typ.		
R	0.001	0.005	0.05	0.15	

# **Suggested Pad Layout**



#### NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

# **REEL SPECIFICATION**

P/N	PKG	QTY
ESD9N12BA-MS	DFN1006-2L	10000



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