## MSKSEMI















**ESD** 

TVS

TSS

MOV

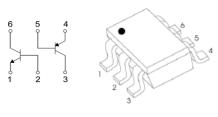
**GDT** 

**PLED** 

# Brodnet data speet

www.msksemi.com





**SOT-363** 

## **Plastic-Encapsulate Transistors**

**DUAL TRANSISTOR (NPN+PNP)** 

#### **FEATURES**

- Epitaxial Die Construction
- Two isolated NPN/PNP(BC846W+BC856W) Transistors in one package

MAKING: BB

#### MAXIMUM RATINGS TR1 (T<sub>a</sub>=25<sup>°</sup>C unless otherwise noted)

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	80	V
V <sub>CEO</sub>	Collector-Emitter Voltage	65	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
Ic	Collector Current –Continuous	0.1	Α
Pc	Collector Power Dissipation	200	mW
TJ	Junction Temperature	150	$^{\circ}$
T <sub>stg</sub>	Storage Temperature	-55-150	$^{\circ}$

#### CHARACTERISTICS of TR1 (NPN Transistor) (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	I <sub>C</sub> =10μA,I <sub>E</sub> =0	80			V
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> =10mA,I <sub>B</sub> =0	65			V
Emitter-base breakdown voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> =1μA,I <sub>C</sub> =0	6			V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> =30V,I <sub>E</sub> =0			15	nA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> =5V,I <sub>C</sub> =0			15	nA
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> =5V,I <sub>C</sub> =2mA	200		450	
Collector emitter acturation valtage	V <sub>CE(sat)</sub>	I <sub>C</sub> =10mA,I <sub>B</sub> =0.5mA			0.25	V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =100mA,I <sub>B</sub> =5mA			0.6	V
Base-emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =10mA,I <sub>B</sub> =0.5mA		0.7		V
base-entitler saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =100mA,I <sub>B</sub> =5mA		0.9		V
Base-emitter voltage	V <sub>BE(on)</sub>	V <sub>CE</sub> =5V,I <sub>C</sub> =2mA	0.58		0.7	V
base-enitter voitage	V <sub>BE(on)</sub>	V <sub>CE</sub> =5V,I <sub>C</sub> =10mA			0.72	V
Collector output capacitance	Cob	V <sub>CB</sub> =10V,I <sub>E</sub> =0,f=1MHz			6.0	pF
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> =5V,I <sub>C</sub> =10mA,f=100MHz	100			MHz
Noise figure	NF	$V_{CE}$ =5V, $I_c$ =0.2mA, f=1kHz,Rg=2K $\Omega$ , $\Delta$ f=200Hz			10	dB

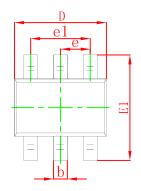
#### MAXIMUM RATINGS TR2 (T<sub>a</sub>=25℃ unless otherwise noted)

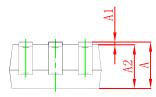
, ,				
Symbol	Parameter	Value	Units	
V <sub>CBO</sub>	Collector-Base Voltage	-80	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	-65	V	
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V	
Ic	Collector Current –Continuous	-0.1	Α	
P <sub>C*</sub>	Collector Power Dissipation	200	mW	
TJ	Junction Temperature	150	°C	
T <sub>stg</sub>	Storage Temperature	-55-150	°C	

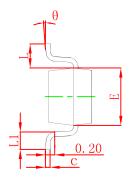
### CHARACTERISTICS of TR2 (PNP Transistor) (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	I <sub>C</sub> =-10μΑ,I <sub>E</sub> =0				V
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> =-10mA,I <sub>B</sub> =0				V
Emitter-base breakdown voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> =-1μΑ,I <sub>C</sub> =0	-5			V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> =-30V,I <sub>E</sub> =0			-15	nA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> =-5V,I <sub>C</sub> =0			-15	nA
DC current gain	h <sub>FE1</sub>	V <sub>CE</sub> =-5V,I <sub>C</sub> =-2mA	220		475	
Callegae amitter esturation valtage	V <sub>CE(sat)</sub>	I <sub>C</sub> =-10mA,I <sub>B</sub> =-0.5mA			-0.3	V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =-100mA,I <sub>B</sub> =-5mA			-0.65	V
Dage emitter esturation valters	V <sub>BE(sat)</sub>	I <sub>C</sub> =-10mA,I <sub>B</sub> =-0.5mA		-0.7		V
Base-emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =-100mA,I <sub>B</sub> =-5mA			-0.95	V
Page emitter veltere	V <sub>BE(on)</sub>	V <sub>CE</sub> =-5V,I <sub>C</sub> =-2mA	-0.6		-0.75	V
Base-emitter voltage	V <sub>BE(on)</sub>	V <sub>CE</sub> =-5V,I <sub>C</sub> =-10mA			-0.82	V
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> =-10V,I <sub>E</sub> =0,f=1MHz			4.5	pF
Transition frequency	f⊤	V <sub>CE</sub> =-5V,I <sub>C</sub> =-10mA,f=100MHz				MHz
Noise figure	NF	$V_{CE}$ =-5 $V$ , $I_{c}$ =-0.2 $m$ A, f=1 $k$ Hz, $Rg$ =2 $k$ Ω, $\Delta f$ =200Hz			10	dB

## **SOT-363 Package Outline Dimensions**

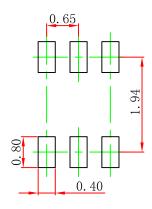






Symbol	Dimensions In Millimeters		Dimension	s In Inches
Syllibol	Min	Max	Min	Max
Α	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
С	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.400	0.085	0.094
е	0.650 TYP		0.026	S TYP
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021	REF
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

## **SOT-363 Suggested Pad Layout**



#### Note:

- 1. Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

#### **REEL SPECIFICATION**

P/N	PKG	QTY
BC846PN	SOT-363	3000



#### Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specificationsof any andall MSKSEMI Semiconductor products described orcontained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringementsof intellectual property rights or other rightsof third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.

## 单击下面可查看定价,库存,交付和生命周期等信息

>>MSKSEMI (美森科)