# MSKSEMI 美森科













**ESD** 

 $\Gamma VS$ 

SS

MOV

GDT

PIFD

# FDMC4435BZ-MS

**Product specification** 





## **Description**

The FDMC4435BZ-MS uses advanced trench technology excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as aload switch or in PWM applications.

#### **Features**

 $V_{DS} = -30V, I_{D} = -50A$ 

 $RDS(ON) < 25m\Omega$  @ VGS=-4.5V

 $RDS(ON) < 15m\Omega$  @ VGS=-10V

High Power and current handing capability

Lead free product is acquired

Surface mount package

## **Application**

- PWM applications
- Load switch
- Power management

#### **Reference News**

PACKAGE OUTLINE	P-Channel MOSFET	Marking
S S S S S S S S S S S S S S S S S S S	G S	MSKSEMI 4435BZ P30 ●
DFN5X6-8L		

## Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Limit	Unit	
VDS	Drain-Source Voltage	-30	V	
VGS	Gate-Source Voltage	±20	V	
	Drain Current-Continuous (Tc=25°C)	-50		
<b>I</b> D	Drain Current-Continuous (Tc=100°C)	-24	Α	
IDM	Drain Current-Pulsed (Note 1)	-80	Α	
	Maximum Power Dissipation (Tc=25 °C)	3		
PD	Maximum Power Dissipation (Tc=100°C)	1.3	W	
EAS	Single pulse avalanche energy (Note 5)	231	mJ	
TJ, TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C	
RθJA	Thermal Resistance, Junction-to-Ambient (Note 2)	41.67	°C/W	



## Electrical Characteristics (TA=25℃unless otherwise noted)

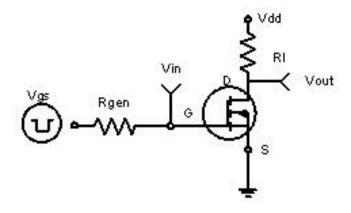
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	BVDSS	Vgs=0V ID=-250 μA	-30	-33	-	V
Zero Gate Voltage Drain Current	IDSS	VDS=-30V,VGS=0V	-	-	-1	μA
Gate-Body Leakage Current	IGSS	Vgs=±20V,Vps=0V	-	-	±100	nA
Gate Threshold Voltage	VGS(th)	Vps=Vgs,Ip=-250µA	-1	-1.5	-3	V
		Vgs=-10V, Ip=-10A	-	9	15	mΩ
Drain-Source On-State Resistance	RDS(ON)	Vgs=-4.5V, ID=-7A	-	18	25	mΩ
Forward Transconductance	gFS	VDS=-10V,ID=-10A	-	20	-	S
Input Capacitance	Clss		-	1750	-	PF
Output Capacitance	Coss	VDS=-15V,VGS=0V,	-	215	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	180	-	PF
Turn-on Delay Time	td(on)		-	9	-	nS
Turn-on Rise Time	tr	VDD=-15V, ID=-10A,	-	8	-	nS
Turn-Off Delay Time	td(off)	Vgs=-10V,Rgen=1 $\Omega$	-	28	-	nS
Turn-Off Fall Time	tf		-	10	-	nS
Total Gate Charge	Qg		-	24	-	nC
Gate- Source Charge	Qgs	VDS=-15V,ID=-10A,VGS=-	-	3.5	-	nC
Gate-Drain Charge	Qgd	- 10V	-	6	-	nC
Diode Forward Current (Note 2)	Is		-	-	-12	Α
Diode Forward Voltage (Note 3)	VSD	V <sub>G</sub> s=0V,Is=-12A	-	-	-1.2	V

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



## **Typical Electrical and Thermal Characteristics**



**Figure 1:Switching Test Circuit** 

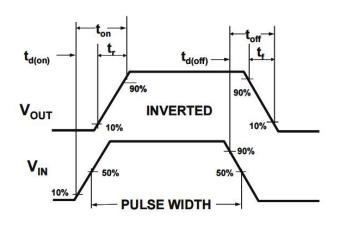
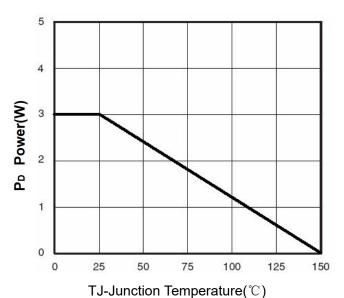


Figure 2:Switching Waveforms



**Figure 3 Power Dissipation** 

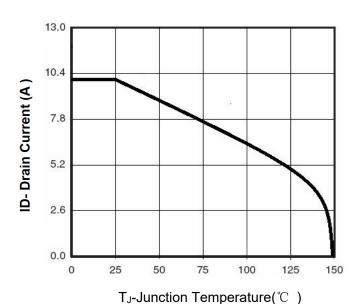


Figure 4 Drain Current

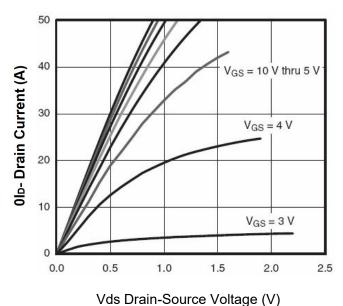
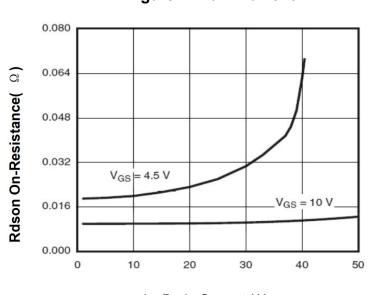
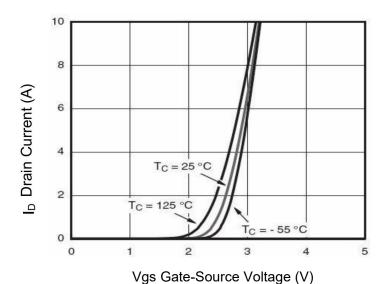


Figure 5 Output Characteristics

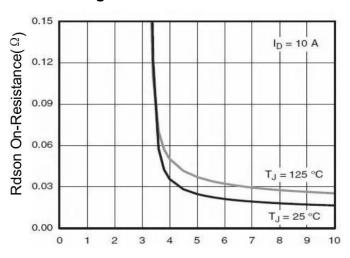


I<sub>D</sub>- Drain Current (A)
Figure 6 Drain-Source On-Resistance

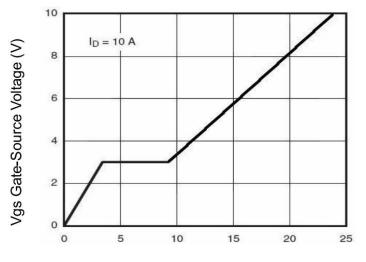




**Figure 7 Transfer Characteristics** 



Vgs Gate-Source Voltage (V) Figure 9 Rdson vs Vgs



Qg Gate Charge (nC)
Figure 11 Gate Charge

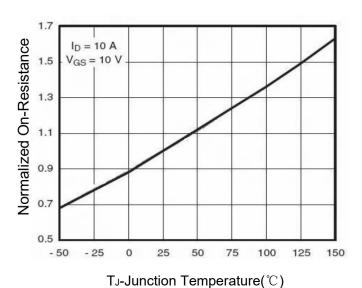
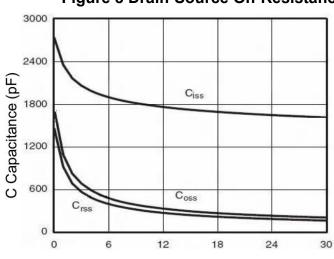
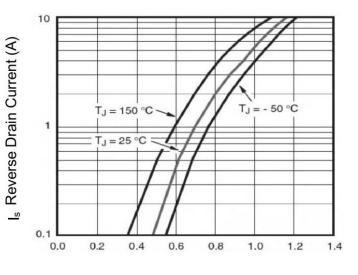


Figure 8 Drain-Source On-Resistance

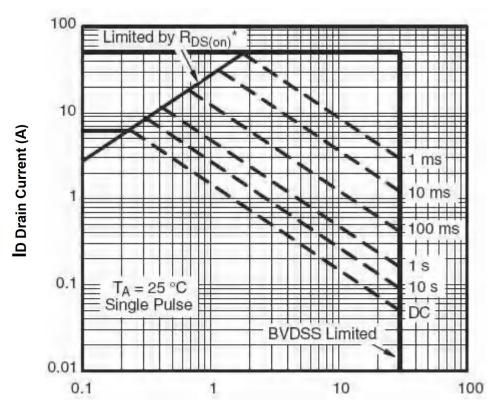


Vds Drain-Source Voltage (V)
Figure 10 Capacitance vs Vds



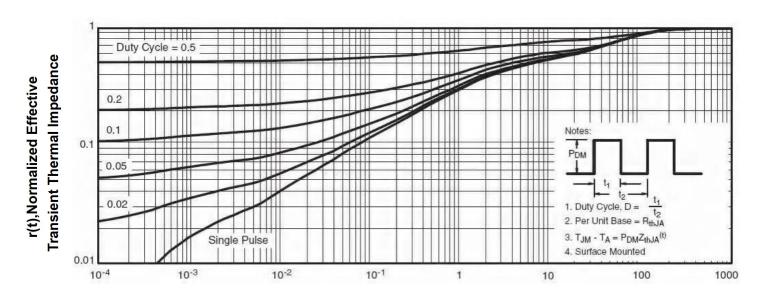
Vsd Source-Drain Voltage (V)
Figure 12 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area

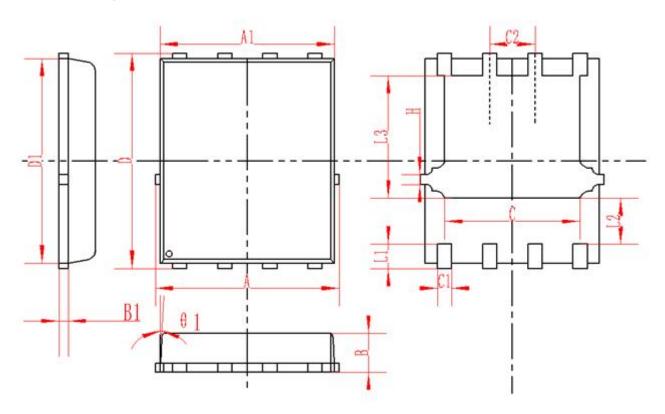


Square Wave Pluse Duration(sec)

**Figure 14 Normalized Maximum Transient Thermal Impedance** 



# DFN5X6-8L Package Information



SYMBOL	MM			INCH		
STIVIDOL	MIN	NOM	MAX	MIN	NOM	MAX
А	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF			0.010REF		
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP		0.5TYP			
θ1	8。	10 <sub>°</sub>	12。	8。	10 <sub>°</sub>	12。
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010

## **REEL SPECIFICATION**

P/N	PKG	QTY
FDMC4435BZ-MS	DFN5X6-8L	5000



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