## MSKSEMI















**ESD** 

TVS

TSS

MOV

**GDT** 

**PLED** 

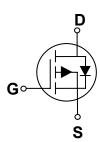
# Brodnet data speet

www.msksemi.com





SOT-23-3L



#### **Features**

- -20 V,-4.5A, RDS(ON)=40mΩ@VGS=-4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available

## **Applications**

- Notebook
- Load Switch
- Hend-Held Instruments

BVDSS	RDSON	ID
-20V	40mΩ	-4.5A

## **Absolute Maximum Ratings** Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-20	V
V <sub>G</sub> S	Gate-Source Voltage	±12	V
	Drain Current – Continuous (Tc=250)	-4.5	Α
ID	Drain Current – Continuous (Tc=1000)	-2.7	Α
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	-18	А
D	Power Dissipation (T <sub>C</sub> =250)	1.5	W
PD	Power Dissipation – Derate above 250	0.012	W/ C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
ReJA	Thermal Resistance Junction to ambient		80	C/ W

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-20			V
△ BV <sub>DSS</sub> / △ T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 250 , I <sub>D</sub> =-1mA		-0.02		V/ C
	Drain-Source Leakage Current	V <sub>DS</sub> =-20V , V <sub>GS</sub> =0V , T <sub>J</sub> =250			-1	uA
loss		V <sub>DS</sub> =-16V , V <sub>GS</sub> =0V , T <sub>J</sub> =1250			-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±12V , V <sub>DS</sub> =0V			±100	nA

On Chara	On Characteristics						
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3A		40	52	· mΩ	
TADS(ON)		V <sub>GS</sub> =-2.5V , I <sub>D</sub> =-2A		47	65		
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-0.3	-0.65	-1.0	\ \	
△ V <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient	VGS-VDS , ID250UA		2		mV/ C	
gfs	Forward Transconductance	V <sub>DS</sub> =-10V , I <sub>S</sub> =-3A		7		S	

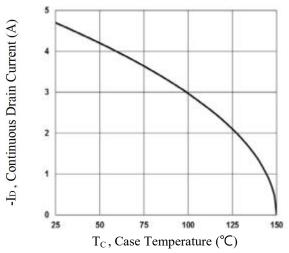
Dynamic	Dynamic and switching Characteristics					
Qg	Total Gate Charge <sup>2, 3</sup>			9.6		
Q <sub>gs</sub>	Gate-Source Charge <sup>2, 3</sup>	V <sub>DS</sub> =-10V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3A		1.6		nC
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>			2		
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2,3</sup>			6		
Tr	Rise Time <sup>2,3</sup>	$V_{DD}$ =-10V , $V_{GS}$ =-4.5V , $R_{G}$ =25 $\Omega$		21.6		nS
$T_{d(off)}$	Turn-Off Delay Time <sup>2,3</sup>	In=-1A		51		113
T <sub>f</sub>	Fall Time <sup>2, 3</sup>			13.8		
C <sub>iss</sub>	Input Capacitance			850		
Coss	Output Capacitance	V <sub>DS</sub> =-10V , V <sub>GS</sub> =0V , F=1MHz		70		pF
Crss	Reverse Transfer Capacitance			55		

## **Drain Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V . Force Current			-4.5	Α
I <sub>SM</sub>	Pulsed Source Current	VG-VD-OV, Force Current			-9.0	Α
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =250			-1.2	V

#### Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq 300 \, \text{us}$  , duty cycle  $\leq 2\%$ .
- 3. Essentially independent of operating temperature.



Continuous Drain Current vs.  $T_{c}$ 

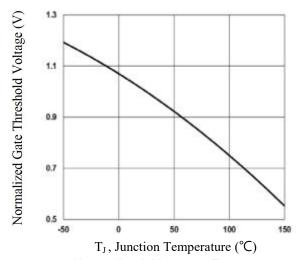
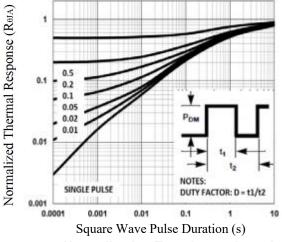
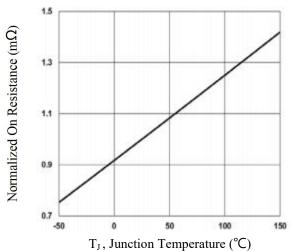


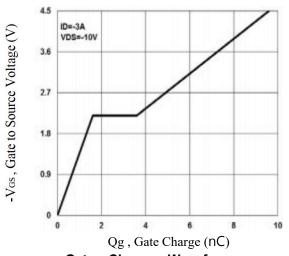
Fig. 3 Normalized  $V_{th}$  vs.  $T_J$ 



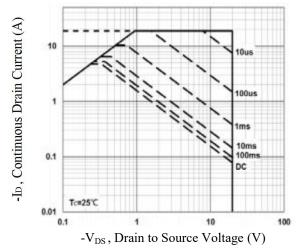
Normalized Transient **Impedance** 



Normalized RDSON vs. T<sub>J</sub> Fig. 2

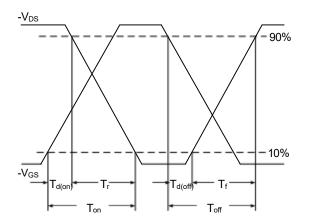


Gate Charge Waveform Fig. 4



Maximum Safe Operation Area Fig. 6







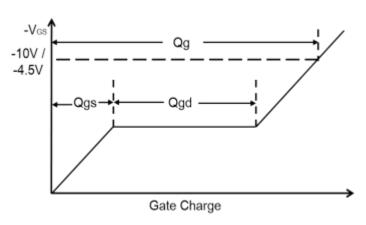
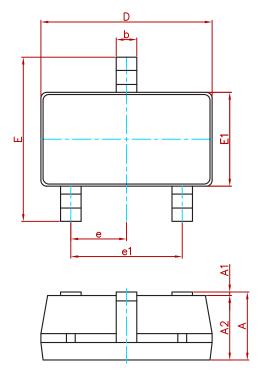
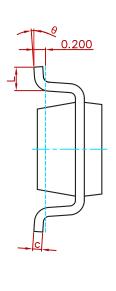


Fig. 8 Gate Charge Waveform

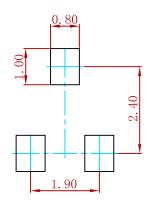
#### **PACKAGE MECHANICAL DATA**





Symbol	Dimensions In	n Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037(	(BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

## **Suggested Pad Layout**



#### Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

## **REEL SPECIFICATION**

P/N	PKG	QTY
AO3415	SOT-23-3L	3000



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