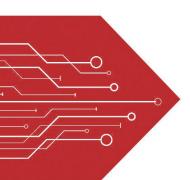
# MSKSEMI















**ESD** 

TVS

TSS

MOV

**GDT** 

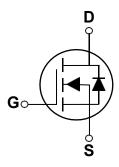
**PLED** 

# Brodnet data speet

www.msksemi.com



SOT-23



#### **Features**

- 30V, 2.0 A,  $RDS(ON) = 300m\Omega@VGS = 4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

## **Applications**

- Notebook
- Load Switch
- LED applications

BVDSS	RDSON	ID
30V	300m $Ω$	2.0A

### **Absolute Maximum Ratings** Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>G</sub> s	Gate-Source Voltage	±12	V
I-	Drain Current – Continuous (T <sub>C</sub> =25°C)	2.0	Α
ID	Drain Current – Continuous (Tc=100°C)	1.2	Α
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	4.0	А
D	Power Dissipation (Tc=25°C)	1.0	W
P <sub>D</sub>	Power Dissipation – Derate above 25°C	0.012	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	℃

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		80	°C/W

### Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.06		V/°C
	Dunin Course I calcone Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			10	uA
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±12V , V <sub>DS</sub> =0V			±100	nA

#### **On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =2A		200	400	mO	
NDS(ON)	Static Drain-Source On-Nesistance	V <sub>GS</sub> =2.5V , I <sub>D</sub> =1A		400	600	mΩ	
V <sub>GS(th)</sub>	Gate Threshold Voltage	\/aa=\/aa la =250uA	0.5	0.8	1.2	V	
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-3		mV/°C	
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>S</sub> =3A		7		S	

# **Dynamic and switching Characteristics**

Qg	Total Gate Charge <sup>2, 3</sup>		 8.4	
Q <sub>gs</sub>	Gate-Source Charge <sup>2, 3</sup>	$V_{DS}$ =10V , $V_{GS}$ =4.5V , $I_{D}$ =1A	 1	 nC
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>	1	 2.2	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2, 3</sup>		 4.5	
Tr	Rise Time <sup>2, 3</sup>	$V_{DD}$ =10V , $V_{GS}$ =4.5V , $R_{G}$ =25 $\Omega$	 13	 20
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2, 3</sup>	I <sub>D</sub> =1A	 27	 nS
T <sub>f</sub>	Fall Time <sup>2,3</sup>		 8.3	
Ciss	Input Capacitance		 695	
Coss	Output Capacitance	V <sub>DS</sub> =10V , V <sub>GS</sub> =0V , F=1MHz	 45	 pF
Crss	Reverse Transfer Capacitance		 36	
Rg	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	 1.5	 Ω

Drain-Source Diode Characteristics and Maximum Ratings						
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			2.0	Α
lsм	Pulsed Source Current	VG-VD-0V , Force Current			4.0	Α
VsD	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1.2	V

#### Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 3. Essentially independent of operating temperature.



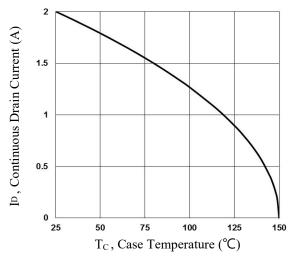


Fig.1 Continuous Drain Current vs. Tc

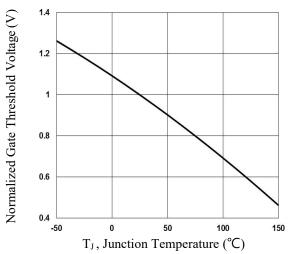


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

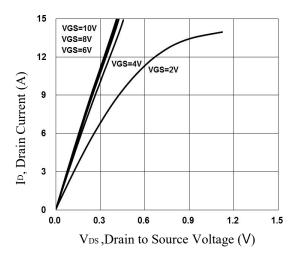


Fig.5 Typical Output Characteristics

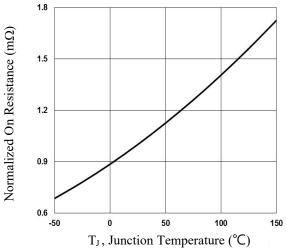


Fig.2 Normalized RDSON vs. T<sub>J</sub>

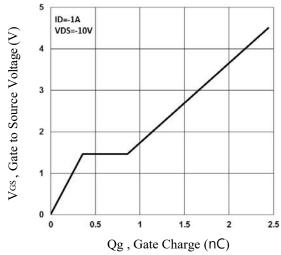


Fig.4 Gate Charge Waveform



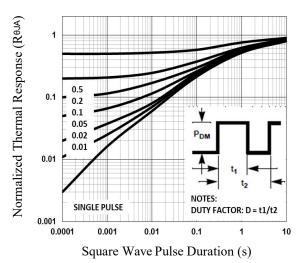


Fig.6 Normalized Transient Impedance

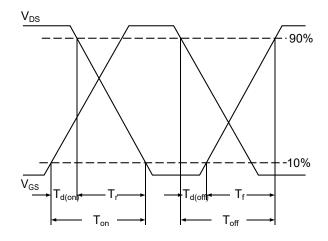


Fig.8 Switching Time Waveform

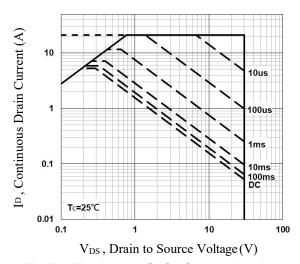


Fig.7 Maximum Safe Operation Area

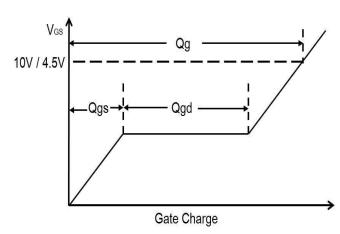
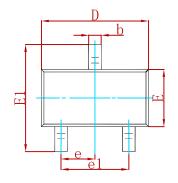
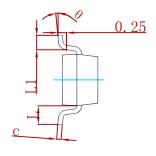
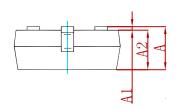


Fig.9 Gate Charge Waveform

# **PACKAGE MECHANICAL DATA**

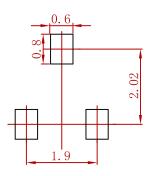






Symbol	Dimensions	Dimensions In Millimeters		s In Inches
Symbol	Min	Max	Min	Max
Α	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
С	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
Е	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
е	0.950 TYP		0.037	7 TYP
e1	1.800	2.000	0.071	0.079
L	0.550	) REF	0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

# **Suggested Pad Layout**



- 1.Controlling dimension:in millimeters.2.General tolerance:± 0.05mm.3.The pad layout is for reference purposes only.

# **REEL SPECIFICATION**

P/N	PKG	QTY
FDV303N	SOT-23	3000



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