

MSKSEMI

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



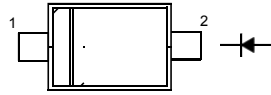
PLED

Product data sheet

www.msksemi.com

Features

- Fast switching speed
- Ultra-small surface mount package
- For general purpose switching applications
- High conductance



SOD-323

RNNING

PIN	DESCRIPTION
1	Cathode
2	Anode

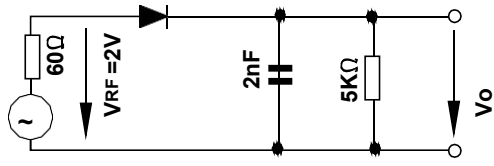
MARK:T4

Absolute Maximum Ratings (T_a = 25 °C)

Parameter	Symbol	Value	Unit
Peak Reverse Voltage	V _{RM}	100	V
Reverse Voltage	V _R	75	V
Average Rectified Forward Current	I _{F(AV)}	150	mA
Non-repetitive Peak Forward Surge Current at t = 1 μs	I _{FSM}	2	A
Power Dissipation	P _{tot}	400	mW
Thermal Resistance from Junction to Ambient Air	R _{θJA}	312	°C/W
Junction Temperature	T _j	150	°C
Storage Temperature Range	T _{stg}	- 65 to + 150	°C

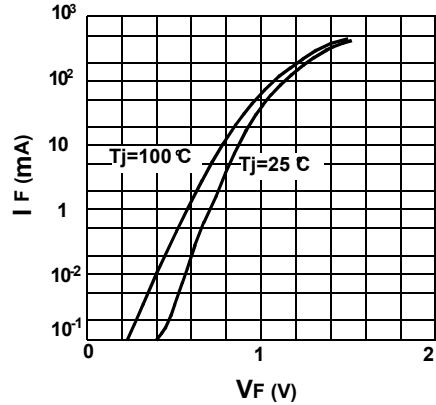
Characteristics at T_a = 25 °C

Parameter	Symbol	Min.	Max.	Unit
Reverse Breakdown Voltage at I _R = 1 μA	V _{(BR)R}	75	-	V
Forward Voltage at I _F = 1 mA at I _F = 10 mA at I _F = 50 mA at I _F = 150 mA	V _F	- - - -	0.715 0.855 1 1.25	V
Peak Reverse Current at V _R = 75 V at V _R = 20 V at V _R = 75 V, T _j = 150 °C at V _R = 25 V, T _j = 150 °C	I _R	- - - -	1 25 50 30	μA nA μA μA
Total Capacitance at V _R = 0 V, f = 1 MHz	C _T	-	2	pF
Reverse Recovery Time at I _{rr} = 0.1 X I _R , I _F = I _R = 10 mA, R _L = 100 Ω	t _{rr}	-	4	ns

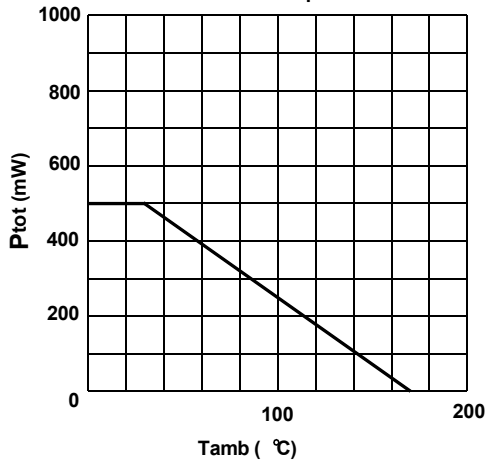


Rectification Efficiency Measurement Circuit

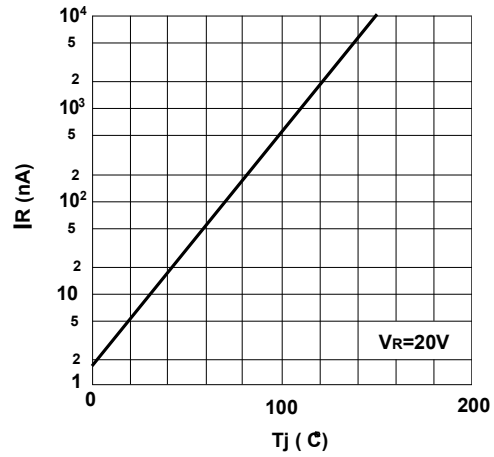
Forward characteristics



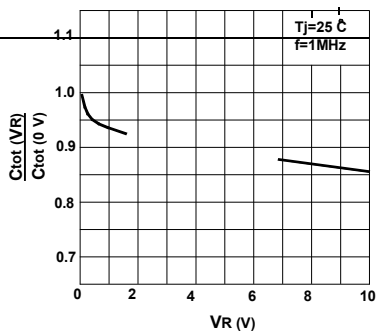
Ammissible power dissipation vs. ambient temperature



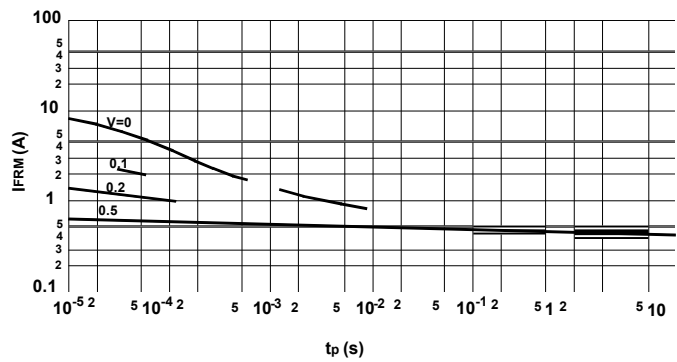
Leakage current vs. junction temperature



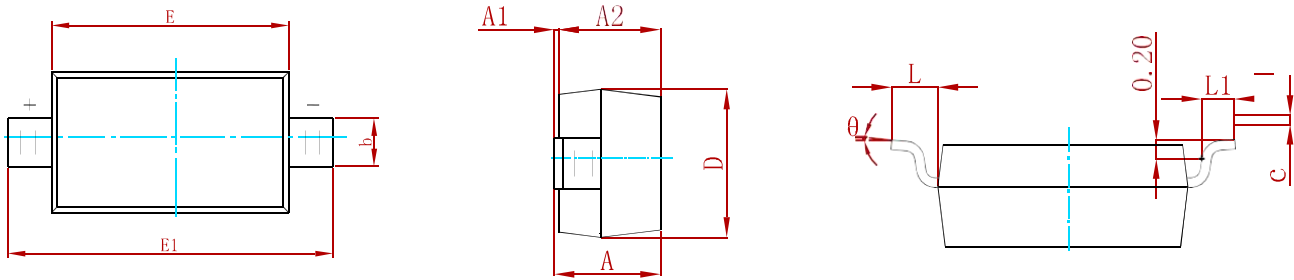
Reverse capacitance vs. reverse voltage



Ammissible repetitive peak forward current vs. pulse duration

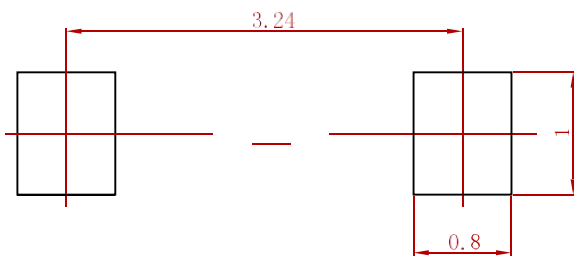


PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.450	0.650	0.018	0.026
c	0.080	0.150	0.003	0.006
D	1.500	1.700	0.059	0.067
E	2.600	2.800	0.102	0.110
E1	3.550	3.850	0.140	0.152
L	0.500 REF		0.020 REF	
L1	0.250	0.450	0.010	0.018
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.05 mm.
3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
1N4148WS-MS	SOD-123	3000

Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.

单击下面可查看定价，库存，交付和生命周期等信息

[>>MSKSEMI\(美森科\)](#)