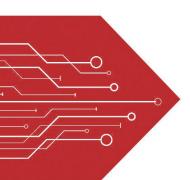
# MSKSEMI















**ESD** 

TVS

TSS

MOV

**GDT** 

**PLED** 

# Brodnet data speet

www.msksemi.com





The MSK80N03NF uses advanced trench technology and design to provide excellent RDS(ON) with low gatecharge. It can be used in a wide variety of applications.

#### **General Features**

VDS=30V,ID=80A

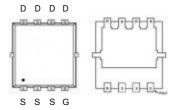
RDS(ON)<5mΩ@ VGS=10V

RDS(ON)<8m $\Omega$ @ VGS=4.5V

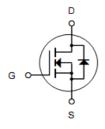
- •High density cell design for ultra low Rdson
- •Fully characterized Avalanche voltage and current
- •Good stability and uniformity with high EAS

#### **Application**

- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



DFN5X6-8L



N-Channel MOSFET

## Maximum ratings, at TA =25°C, unless otherwise specified

Symbol	Parameter	Rating	Unit	
V(BR)DSS	Drain-Source breakdown voltage		30	V
Is	Diode continuous forward current	Tc =25°C	80	А
1-		Tc =25°C	80	Α
l <sub>D</sub>	Continuous drain current@VGS=10V	Tc =100°C	45	Α
IDM	Pulse drain current tested ①	Tc =25°C	280	Α
EAS	Avalanche energy, single pulsed ②		56	mJ
PD	Maximum power dissipation	Tc =25°C	37	W
VGS	Gate-Source voltage		±20	V
TSTG TJ	Storage and operating temperature range		-55 to 150	°C

### **Thermal Characteristics**

Symbol	Parameter	Typical	Unit
Rejc	Thermal Resistance-Junction to Case	3.4	°C/W
Rеja	Thermal Resistance Junction-Ambient	30	°C/W

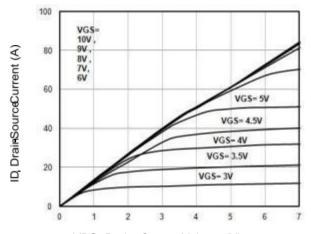


Compiance

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Static Ele	□ ectrical Characteristics @ Tj=25°C (unle	ss otherwise stated	)			
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	Vgs=0V Ip=250µA	30			V
	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V			0.1	μA
loss	Zero Gate Voltage Drain Current(T <sub>j</sub> =125℃)	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V			100	μA
lgss	Gate-Body Leakage Current	Vgs=±20V,Vps=0V			±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	1.0	1.7	2.5	٧
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance③	V <sub>G</sub> S=10V, I <sub>D</sub> =20A		3	4	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance③	Vgs=4.5V, ID=16A		5.4	8	mΩ
Dynamic	Electrical Characteristics @ T <sub>j</sub> = 25°C (	unless otherwise st	ated)			
Ciss	Input Capacitance			1930		pF
Coss	Output Capacitance	VDS=15V,VGS=0V, f=1MHz		310		pF
Crss	Reverse Transfer Capacitance			260		pF
Rg	Gate Resistance	f=1MHz		0.85		
$Q_g$	Total Gate Charge			38		nC
Q <sub>gs</sub>	Gate-Source Charge	VDS=15V,ID=20A, VGS=10V		5.1		nC
Qgd	Gate-Drain Charge	VGS-10V		12		nC
Switching	Characteristics		1			
<b>t</b> d(on)	Turn-on Delay Time			8.5		nS
t <sub>r</sub>	Turn-on Rise Time	V <sub>DD</sub> =15V,		9		nS
<b>t</b> d(off)	Turn-Off Delay Time	ID=20A,		31		nS
t <sub>f</sub>	Turn-Off Fall Time	Rg=3, Vgs=10V		9		nS
Source- I	Drain Diode Characteristics@ T <sub>j</sub> = 25°C	unless otherwise s	tated)	I	I	<u> </u>
V <sub>SD</sub>	Forward on voltage	Isp=20A,Vgs=0V		0.8	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	Tj=25°C,Isd=20A, VGS=0V		16		nS
Qrr	Reverse Recovery Charge	di/dt=500A/µs		42		nC

#### NOTE:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by T<sub>Jmax</sub>, starting T<sub>J</sub> = 25°C, L = 0.5mH,R<sub>G</sub> = 25 , I<sub>AS</sub> = 15A, V<sub>GS</sub> =10V. Part not recommended for use above this value
- ③ Pulse width ≤ 300 $\mu$ s; duty cycle≤ 2%.



VDS, Drain -Source Voltage (V) **Fig1.** Typical Output Characteristics

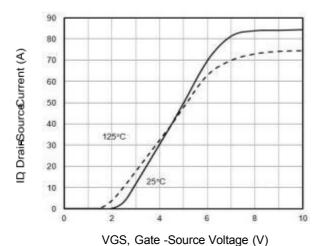
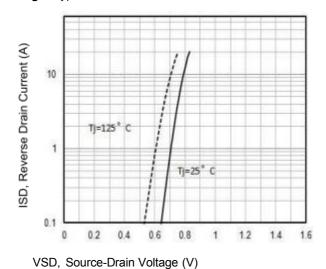


Fig3. Typical Transfer Characteristics



**Fig6.** Maximum Safe Operating Area Voltage

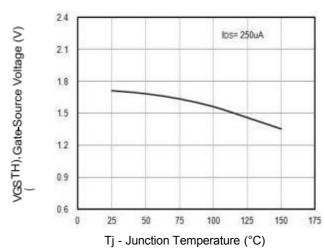


Fig2. VGS(TH) Gate -Source Voltage Vs.Tj

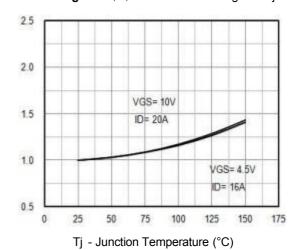
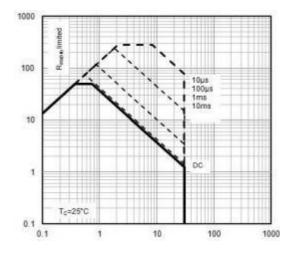


Fig4. Normalized On-Resistance Vs. Tj



VDS, Drain -Source Voltage (V)

Fig5. Typical Source-Drain Diode Forward

Normalized On Resistance

D Drain Current (A)





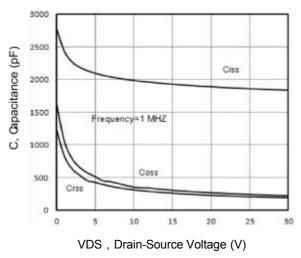


Fig7. Typical Capacitance Vs.Drain-Source Voltage

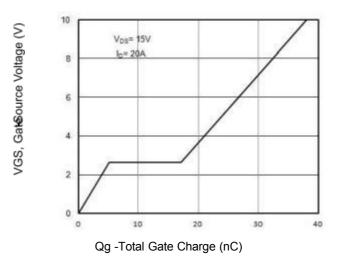


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

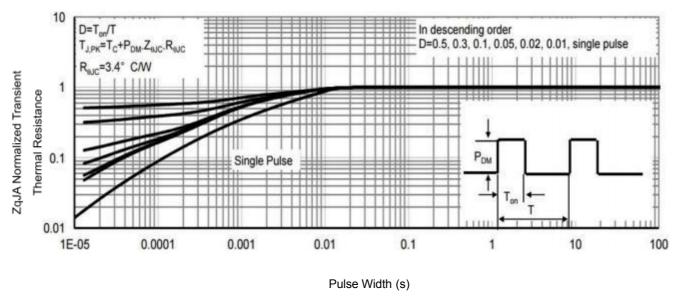


Fig9. Normalized Maximum Transient Thermal Impedance

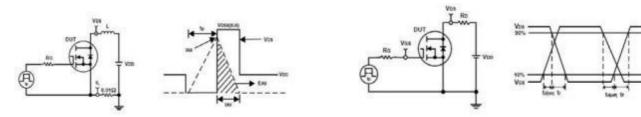
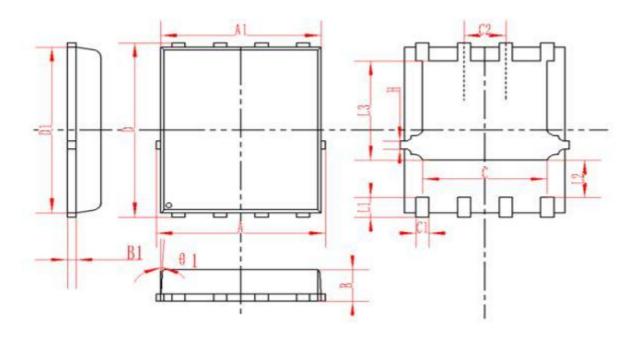


Fig10. Unclamped Inductive Test Circuit and waveforms

Fig11. Switching Time Test Circuit and waveforms



# **DFN5X6-8L Package Information**



SYMBOL		MM			INCH	
STIVIDOL	MIN	NOM	MAX	MIN	NOM	MAX
Α	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF			0.010REF		
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2		1.27TYP			0.5TYP	
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010

## **REEL SPECIFICATION**

P/N	PKG	QTY
MSK80N03NF	DFN5X6-8L	5000



Semiconductor



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